ANNEXURE-18
MANDATORY DISCLOSURE - 2025-2026

SHRI SANT GAJANAN MAHARAJ COLLEGE OF ENGINEERING, SHEGAON – 444 203 DIST – BULDANA [MAHARASHTRA STATE] INDIA

Mandatory Disclosure updated on:

AICTE File No.

AICTE Permanent Application ID	1-4792001
AICTE Current Application ID – 2025-26	1-44641650676

Date & Period of last approval

F.No. Western/1-44641650676/2025/EOA Dated 05-April-2025

18.1 Name of the Institution

- **Address Inculding Telephone, Mobile, E-mail**

Name of the Institution	SHRI SANT GAJANAN MAHARAJ COLLEGE OF ENGINEERING
Address of the Institution	KHAMGAON ROAD
City with Pin Code	SHEGAON – 444 203
State / UT	MAHARASHTRA (India)
Contact Number	Principal Office – 8669638081 Registrar Office - 8669638082
E-Mail Address	principal@ssgmce.ac.in, registrar@ssgmce.ac.in
Website	www.ssgmce.ac.in
Nearest Railway Station	2.5 Km
Nearest Bus Stand	3.00 km

18.2 Name and Address of the Trust

- **Address Including Telephone, Mobile, E-Mail**

Name and address of the Trust running the Institutite	SHRI GAJANAN SHIKSHAN SANSTHA
Address of the Institution	SSGMCE CAMPUS, KHAMGAON ROAD
City with Pin Code	SHEGAON – 444 203
State / UT	MAHARASHTRA (India)
Contact Number	8669638081, 8669638082
E-Mail Address	principal@ssgmce.ac.in, registrar@ssgmce.ac.in

18.3 Name and Address of the Principal

- **Address Including Telephone, Mobile, E-Mail**

Name of Principal	DR. SUNIL BHIKAMCHAND SOMANI
Address of the Principal	PRINCIPAL'S BUNGLOW, SSGMCE CAMPUS, KHAMGAON ROAD
City & Pin Code	SHEGAON – 444203, DIST-BULDHANA (M.S.)
Mobile No.	9422182216
Email	principal@ssgmce.ac.in

18.4 Name and Affiliating University

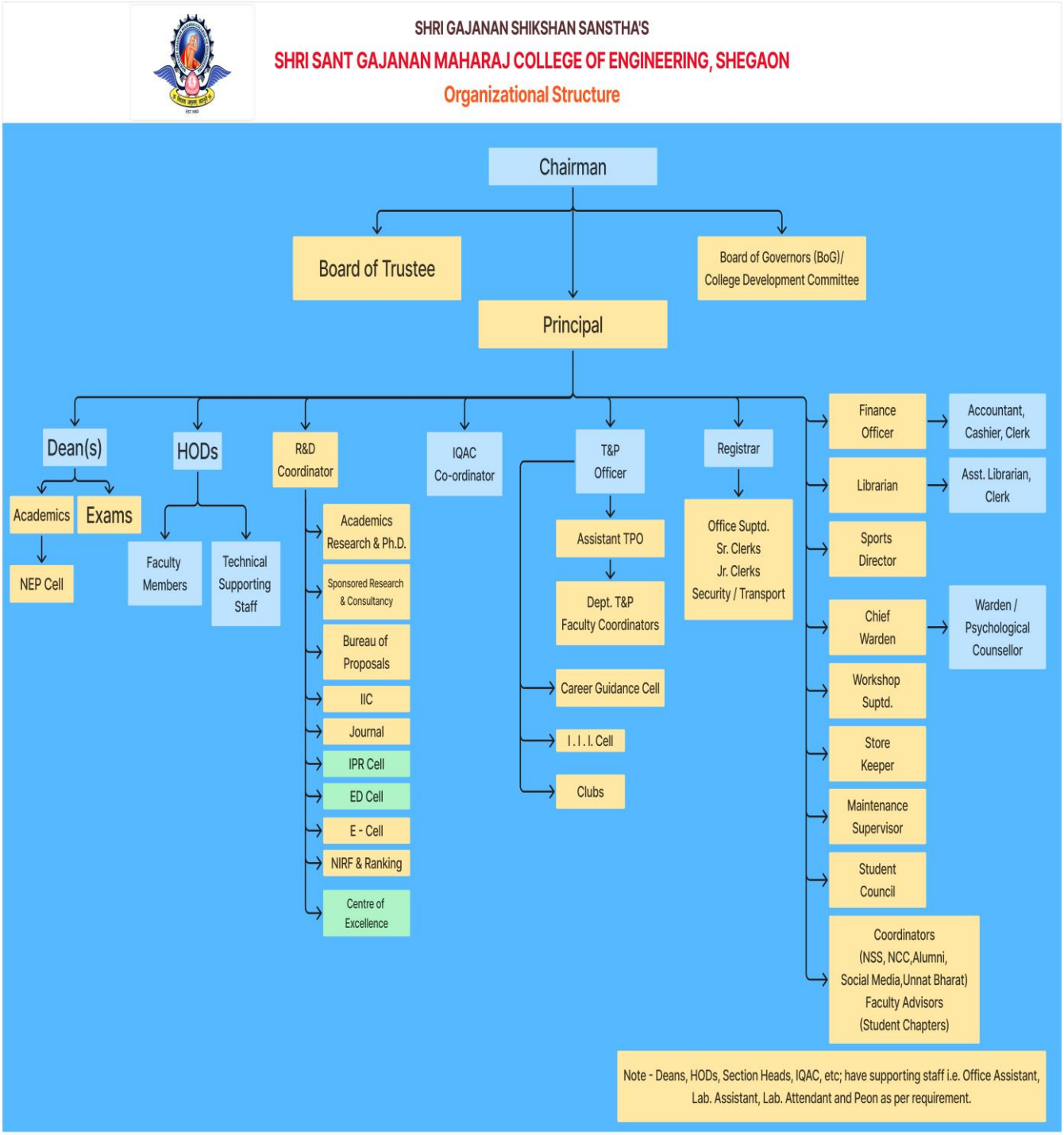
Name of the affiliating University	SANT GADGE BABA AMRAVATI UNIVERSITY, AMRAVATI
Address	TAPOWAN, UNIVERSITY CAMPUS, AMRAVATI
	PIN – 444 602, DIST.: – AMRAVATI (MAHARASHTRA)
Website	www.sgbau.ac.in

18.5 Governance :

- **Members of the Board and their brief background**

1	Shri Nilkanth Shivshankar Patil	:	Chairman [Ex-officio Chairman] (Sanstha)
2	Shri Kishor Trikandas Tank	:	Member (Sanstha)
3	Shri Ashok Trimbakrao Deshmukh	:	Member (Sanstha)
4	Shri Jay Kishor Tank	:	Member (Sanstha)
5	Shri Ramkrushna Nilkanth Patil	:	Member (Sanstha)
6	Prof. Dr. A. M. Mahalle	:	Member (Nominee of Affiliating University, Amravati)
7	Dr. Vinod Mohitkar	:	Member (Ex-Officio) Director of Technical Education (Nominee of the State Government)
8	Shri Vikas Chandra Rastogi	:	Member (Nominated by the State Government
9	Prof. (Dr.) Sunil Bhikamchand Somani	: 	Member Secretary (Principal/ Head of the Institute)
10	Dr. Ram Shankarrao Dhekekar	:	Member (Faculty member nominated from regular staff at the level of Professor)
11	Dr. Anjali Uday Jawadekar	:	Member (Faculty member nominated from regular staff at the level of Associate Professor)

i. Organizational Chart



ii. Grievances Redressal mechanism for Faculty, staff and students.

- To develop the responsive and accountable attitude amongst the students, faculty, supporting staff, parents and other stakeholders, the Redressal of Ceartain Grivances SSGMCE formed Grievance Redressal Committee.
- On receipt of an online complaint, the Institution refers the complaint to the grievance redressal committee within ten days.
- The committee fix a date for hearing the complaint
- An aggrieved person may appear in person to present the case.
- Grievance not resolved by the committee shall be refered to the ombudsman.
- The ombudsman may recommend action against the complainant, where a complaint is found to be false.

iii. Establishment of Anti Ragging Committee:

Click Here

https://www.ssgmce.ac.in/uploads/pdf/ANTI_RAGGING_COMMITTEE%20(3).pdf

iv. Establishment of Online Grievance Redressal Mechanizm:

Click Here **https://www.ssgmce.ac.in/uploads/pdf/GRC-AICTE.pdf**

v. Details of Grievance Redressal Committee in the Institution and OMBUDSMAN by the University: *Click Here* https://www.ssgmce.ac.in/uploads/pdf/Student%20Grevance%20Redressal%20Committee%20(SGRC).pdf

vi. Establishment of Internal Committee (IC): *Click Here* https://www.ssgmce.ac.in/uploads/pdf/Internal%20Committee.pdf

vii. Establishment of Committee for SC/ST: *Click Here* https://www.ssgmce.ac.in/uploads/pdf/IC-ST_ST.pdf

viii. Internal Quality Assurance Cell : *Click Here* https://www.ssgmce.ac.in/uploads/pdf/Internal%20Quality%20Assurance_27-12-2025.pdf

ix. Equal Opportunity facilities Cell: *Click Here*

18.6 Programmes

i. Name of Programmes approved by AICTE

Programmes	Level	Course	Intake Approved 2025-26
Engineering Technology	Under Graduate	Computer Science And Engineering	120
		Electronics and Telecommunication Engineering	120
		Mechanical Engineering	60
		Electrical Engineering (Electronics and Power)	60
		Information Technology	60
	Post Graduate	Electrical Power System	18
		Digital Electronics	18
		Computer Engineering	18
		Advanced Manufacturing and Mechanical System Design	18
Management	Post Graduate	M.B.A.	60

ii. Name of Programmes Accreddited by NBA

Programmes	Level
Engineering Technology	U.G.

iii. Status of Accreditation of the Courses

- **National Board of Accreditation (NBA)**

Programmes	Level	Course	Period of Validity
Engineering Technology	UG	B.E. Computer Science And Engineering	Academic Years 2022-2023 to 2024-2025 i.e. up to 30-06-2025 (3 Years)
		B.E. Electrical Engineering (Electronics and Power) Engineering	
		B.E. Mechanical Engineering	
		B.E. Electronics and Telecommunication Engineering	

- **National Assessment and Accreditation Council (NAAC)**

Programme	Level	Date	Period of Validity	GRADE	CGPA
Engineering Tehnology	U.G.	December 20, 2024	December 19, 2029	A^+	**3.29**
	P.G.				
Management	P.G.				

iv. Total number of Courses

Programmes	Level	Number of Course
Engineering Technology	UG	05
	PG	04
Management	PG	01

v. For each Programme the following details are to be given (Preferably in Tabular form):

a. Name

b. Number of Seats

c. Duration

Programme	Level	Name of the Courses	Number of Seats (Intake)	Duration
Engineering Technology	UG	Computer Science and Engineering	120	04 Years
		Electrical Engineering (Electgronics and Power)	60	
		Electronics & Telecommuniation Engineering	120	
		Mechanical Engineering	60	
		Information Technology	60	
	PG	Electrical Power System	18	02 Years
		Digital Electronics	18	
		Computer Engineering	18	
		Advanced Manufacturing &Mech.System Design	18	
Management	P.G.	M.B.A.	60	02 Years

d. Cut off marks/rank of admission during the lastyears

SN	Department	2023-24		2024-25		2025-26	
		High	Low	High	Low	High	Low
Engineering Technology (UG)							
01	Computer Science and Engineering	99.05	52.24	96.24	69.76	95.63	28.27
02	Electrical Engineering (Electgronics and Power)	86.05	49.04	87.95	45.34	91.35	43.91
03	Electronics & Telecommuniation Engineering	91.48	52.74	92.91	42.22	92.93	11.13
04	Mechanical Engineering	83.37	40.65	86.97	42.22	85.90	33.08
05	Information Technology	93.60	48.97	94.13	44.89	94.00	33.46
Engineering Technology (PG)							
06	Electrical Power System	2.33	2.00	9.33	3.00	7.00	1.01
07	Digital Electronics	--	--	19.67	6.00	67.2	---
08	Computer Engineering	4.00	---	10.62	9.96	58.00	3.89
09	Advanced Manufacturing & Mechanical System Design	1.33	---	62.8	0.67	22.00	---
Management (PG)							
10	M.B.A.	95.00	50.00	91.11	19.74	96.69	6.09

vi **Fees (as approved by the state government): Fees Regulating Authority, M.S., Mumbai**

SN	Department	Fee (Approved by the State Government) 2025-26
Engineering and Technology (UG)		
01	Computer Science and Engineering	Rs.1, 34,000/- (Rs. One Lack Thirty Four Thousand Only)
02	Electrical Engineering (Electgronics and Power)	
03	Electronics and Telecommuniation Engineering	
04	Mechanical Engineering	
05	Information Technology	
Engineering and Technology (PG)		
06	Electrical Power System	Rs.52,000/- (Rs. Fifty Two Thousand Only)
07	Digital Electronics	
08	Computer Engineering	
09	Advanced Manufacturing and Mechanical Systems Design	
Management (PG)		
10	M.B.A.	Rs.1,00,000/- (Rs. Ninenty Thousand Only)

vii. **Name and duration of Programme(s) having Twinning and Collaboration with Foreign University(s) and being run in the same Campus along with status of their AICTE approval. If there is Foreign Collaboration, give the following details, if any:**

No

viii. **Nature of Collaboration**

No

ix. **Complete details of payment a student has to make to get the full benefit of Collaboration**

No

x. **For each Programme Collaborated provide the following:**

No

xi. **Programme Focus**

No

xii. **Number of seats**

No

xiii. **Admission Procedure**

No

xiv. **Fee (as approved by the state government)**

No

xv. **Whether the Collaboration Programme is approved by AICTE? If not whether the Domestic/ Foreign University has applied to AICTE for approval -**

NO

18.7 Faculty

i. Course/Branch wise list Faculty members:

ii. Permanent Faculty

SN	Name of the Faculty	Designation	Qualification
01	Dr.S.B. Somani	Principal	M.E., Ph.D.
Department of Electrical Engineering (Electronics and Power) (UG/PG)			
02	Dr. S.R. Paraskar	Professor	M.E., Ph.D.
03	U.A. Jawadekar	Associate Professor	M.E.
04	Dr. A.U. Jawadekar	Associate Professor	M.E., Ph.D.
05	Dr. S.S. Jadhao	Associate Professor	M.E., Ph.D.
06	P.R. Bharambe	Assistant Professor	M.E.
07	Dr. R.S. Kankale	Assistant Professor	M.E., Ph.D.
08	M.R. Chavan	Assistant Professor	M.E.
09	R.K. Mankar	Assistant Professor	M.E.
10	Dr. G.N. Bonde	Assistant Professor	M.E., Ph.D.
11	B.S. Rakhonde	Assistant Professor	M.E.
12	P.R. Dhabe	Assistant Professor	M.Tech.
13	V.S. Karale	Assistant Professor	M.E.
14	V.A. Nagapure	Assistant Professor	M.Tech.
15	G.D. Khadsane	Assistant Professor	M.Tech.
Department of Electronics and Telecommunication Engineering (UG/PG)			
16	Dr. D.D. Navgaje	Associate Professor	M.E., Ph.D.
17	Dr. K.B. Khanchandani	Professor	M.E., Ph.D.
18	Dr. R.S. Dhekekar	Professor	M.E., Ph.D.
19	Dr. M.N. Tibdewal	Professor	M.E., Ph.D.
20	Dr. S.B. Patil	Professor	M.E., Ph.D., MBA
21	V.M. Umale	Associate Professor	M.Tech
22	D.L. Bhombe	Associate Professor	M.S.
23	Dr. D.P. Tulaskar	Associate Professor	M.E., Ph.D.
24	Dr. Bhavana P. Harne	Assistant Professor	M.E., Ph.D.
25	A.N. Dolas	Assistant Professor	M.E.
26	V.K. Bhangdiya	Assistant Professor	M.Tecch.
27	Dr. K.T. Kahar	Assistant Professor	M.Teh., Ph.D.
28	K.S. Vyas	Assistant Professor	M.E.
29	Dr. S.P. Badar	Assistant Professor	M.E., Ph.D.
30	T.P. Marode	Assistant Professor	M.E.
31	V.S. Ingole	Assistant Professor	M.E.
32	S.G. Nemane	Assistant Professor	M.E.
33	Ashwini A Deshmukh	Assistant Professor	M.E.
34	Harshvardhan Balkrushna Patil	Assistant Professor	M.E.
35	S.P Satal	Assistant Professor	M.E.
36	M.B. Dhamande	Assistant Professor	M.E.

SN	Name of the Faculty	Designation	Qualification
Department of Mechanical Engineering (UG/PG)			
37	Dr. S.P. Trikal	Professor	M.E., Ph.D.
38	Dr. V.K. Thute	Associate Professor	M.Tech., Ph.D.
39	Dr. J.G. Khan	Associate Professor	M.E., Ph.D.
40	Dr. N.H. Khandare	Associate Professor	M.E., Ph.D.
41	M.B. Bhambere	Assistant Professor	M.Tech.
42	C.V. Patil	Assistant Professor	M.E.
43	A.S. Bharule	Assistant Professor	M.Tech.
44	N.B. Borkar	Assistant Professor	M.Tech.
45	P.T. Patokar	Assistant Professor	M.E.
46	S.Q. Syed	Assistant Professor	M.Tech.
47	Dr. K.V. Chandan	Assistant Professor	M.Tech., Ph.D.
48	Dr. Piyush Dalke	Assistant Professor	M.Tech., Ph.D.
49	Kailash Dudhe	Assistant Professor	M.E.
50	Dr. Saurabh P. Joshi	Assistant Professor	M.E, Ph.D.
51	Vishnu T. Mahske	Assistant Professor	M.E.
52	Ganesh S. Wahile	Assistant Professor	M.E.
Department of Computer Science and Engineering (UG/PG)			
53	Dr. J.M. Patil	Associate Professor	M.Tech., Ph.D.
54	Dr. N.M. Kandoi	Associate Professor	M.S., Ph.D.
55	C.M. Mankar	Assistant Professor	M.E.
56	Dr. V.S. Mahalle	Assistant Professor	M.E.,Ph.D.
57	Dr. P.K. Bharne	Assistant Professor	M.E., Ph.D.
58	K.P. Sable	Assistant Professor	M.E.
59	Shrijeet Pagrut	Assistant Professor	M.E.
60	Dr. Rupali Zamare	Assistant Professor	M.E., Ph.D.
61	Dr. Neerja Suresh Dharmale	Assistant Professor	M.E., Ph.D.
62	Dr. Rupesh Subhash Mahamune	Assistant Professor	M.E., Ph.D.
63	Ms. Pooja Pohare	Assistant Professor	M.E.
64	Ms. S.M. Jawake	Assistant Professor	M.E.
65	Mr. R.V. Deshmukh	Assistant Professor	M.E.
66	Mrs. T.A. Puranik	Assistant Professor	M.Tech.
67	Ms. V.S. Kanherkar	Assistant Professor	M.E.
Department of Information Technology (UG)			
68	Dr. S.D. Padiya	Associate Professor	M.Tech, Ph.D.
69	P.V. Kale	Assistant Professor	M.Tech.
70	A.G. Sharma	Assistant Professor	M.Tech.
71	Faizan Khandwani	Assistant Professor	M.E.
72	S.S. Muddalkar	Assistant Professor	M.Tech.
73	Pallavi Bute	Assistant Professor	M.E.
74	Mrs. Shaila Nilesh Khandare	Assistant Professor	M.E.
75	Mrs. Nayana Nanarao Ghuikar	Assistant Professor	M.E.
76	Mr. P.A. Lod	Assistant Professor	M.E.
77	Dr. A.S. Manekar (Lean)	Associate Professor	M.E., Ph.D.

SN	Name of the Faculty	Designation	Qualification
Department of Applied Sciences & Humanities			
78	Dr. A.S. Tale	Associate Professor	M.Sc., Ph.D.
79	A.V. Patil	Associate Professor	M.Sc., M.Phil
80	Dr. R.M. Kharate	Associate Professor	M.Sc., Ph.D.
81	P.I. Chaure	Associate Professor	M.E.
82	G.L. Bayaskar	Associate Professor	M.A., M.P.Ed.
83	N.S. Thakare	Assistant Professor	M.Sc., M.Phil
84	A.S. Alane	Assistant Professor	M.Sc.
85	H.S. Patil	Assistant Professor	M.A., M.B.A.
86	K.P. Deshmukh	Assistant Professor	M.Sc.
87	S.V. Bhagat	Assistant Professor	M.A., M.Phil
88	Rutika Raut	Assistant Professor	M.Sc.
89	Dr.M.S. Pande	Assistant Professor	M.Sc., Ph.D.
90	Dr. Jayashree S. Gawande	Assistant Professor	M.Sc. , Ph.D.
Department of M.B.A.			
91	Dr. P.M. Kuchar	Assistant Professor	M.B.A., Ph.D.
92	Dr. L.B. Deshmukh	Associate Professor	MBA, Ph.D.
93	Dr. M.A. Dande	Assistant Professor	M.B.A., Ph.D.
94	Dr. S.M. Mishra	Assistant Professor	M.B.A., Ph.D.
95	Dr. W.Z. Suliya	Assistant Professor	M.B.A., Ph.D.
96	V.V. Patil	Assistant Professor	M.B.A.
97	Dr. B.T. Hussain	Assistant Professor	M.B.A., Ph.D.
98	Aadesh B. Solanke	Assistant Professor	M.B.A.

iii. Adjunct Faculty

SN	Name of the Faculty	Designation	Qualification
01	Nilesh Kothawale	Professor of Practice	M.Tech – ELPO
02	Yogesh Prabhakar Muramkar	Professor of Practice	M.E. – CSE
03	Mrs. Mohini Modak	Professor of Practice	M.B.A.
04	Ketan Pachpande	Professor of Practice	M.E. – IT
05	Manoj Yadav	Professor of Practice	M.E. – MECH
06	Girish Kangane	Professor of Practice	M.E. - EXTC

iv. Permanent Faculty: Student Ratio

SN	Programmes	Level	Ratio
1	Engineering and Technology	UG	1:20
		PG	1:15
2	Management (MBA)	PG	1:20

18.8 Profile of Vice Chancellor/Director/Principal/Faculty

1	Name	**Dr. Sunil Bhikamchand Somani - Principal**							
2	Date of Birth	8th July 1967							
3	Unique ID	1-426010041							
4	Education Qualifications	B.E., M.E., Ph.D (Engg.& Tech.), Diploma in Office Automation							
5	Work Experience	Teaching	36	Research	05	Industry	01	others	0
6	Area of Specialization	Advance water treatment, Advance waste water treatment, Solid waste management,							
7	Courses (Subjects) taught	Under Graduate	Engineering Mechanics, Mechanics of Material, Fluid Mechanics, Environment Studies						
		Post Graduate	Advance water treatment						
8	Research guidance (Number of Students)	No. of Papers Published in	National	17	International Journals	12	Conferences	00	
		Masters Degree	Completed	11		Ongoing		00	
		Ph.D.	Completed	01		Ongoing		02	
9	Projects Carried out	U.G. – 12 P.G. - 11							
10	Patents	Filled	01	Granted	01				
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National Journal	04	International Journal	08	Conferences	15		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	---							

➢ Department of Electrical Engineering (Electronics and Power)

1	Name	**Dr. Sudhir Ramdas Paraskar**							
2	Date of Birth	02/06/1966							
3	Unique ID	1-422786205							
4	Education Qualifications	Ph.D							
5	Work Experience	Teaching	29 years	Research	--	Industry	--	others	--
6	Area of Specialization	Transformer Protection, Power Quality,							
7	Courses (Subjects) taught	Under Graduate	Basic electrical engineering, Energy resources and generation, Electrical Power II, Electric drives and Control, Electrical Energy Distribution and utilization, Electrical Machine I, Electrical Machine II						
		Post Graduate	Advanced power system protection, FACTS and Power Quality						
8	Research guidance (Number of Students)	No. of Papers Published in	National	06	International Journals	58	Conferences	44	
		Masters Degree	Completed	25	Ongoing	01			
		Ph.D.	Completed	03	Ongoing	06			
9	Projects Carried out	Software and hardware projects based on Electrical engineering- U.G.-29-P.G.26							
10	Patents	Filled	03	Granted	01				
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	06	International Journal	58	Conferences	44		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)	Kunal N.Sawalakhe Dr. Sudhir R. Paraskar Shrikant Daulatrao Zilpe Pritesh R.Mhaiskar “Control Strategies For Cuk Converters In Dc – Dc Drive Applications” ISBN: 978 81-988406 5-3							

1	Name	**Uday Ananad Jawadekar**							
2	Date of Birth	27/06/1967							
3	Unique ID	1-422783697							
4	Education Qualifications	M.E. Electrical							
5	Work Experience	Teaching	28 years	Research	--	Industry	--	others	--
6	Area of Specialization	Electrical Power system engineering							
7	Courses (Subjects) taught	Under Graduate	Basic electrical engineering, Electrical measurements and instrumentation, Energy resources and generation.						
		Post Graduate	High voltage transmission, Generation planning and load dispatch.						
8	Research guidance (Number of Students)	No. of Papers Published in	National	01	International Journals	05	Conferences	09	
		Masters Degree	Completed	22	Ongoing	--			
		Ph.D.	Completed	---	Ongoing	---			
9	Projects Carried out	Software and hardware projects based on Electrical engineering- U.G.-28-P.G.22							
10	Patents	Filled	01	Granted	---				
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	01	International Journal	05	Conferences	09		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)	---							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Anjali .U. Jawadekar</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">22-02-1972</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-422786209</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">PhD</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>23</td><td>Research</td><td>15</td><td>Industry</td><td>-</td><td>others</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Signal Processing</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Signals& systems, control systems, Electromagnetic field theory , Power Electronics</td></tr>
<tr><td>Post Graduate</td><td colspan="7"></td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td></td><td>International Journals</td><td>53</td><td>Conferences</td><td>15</td></tr>
<tr><td>Masters Degree</td><td colspan="2">Completed</td><td>27</td><td colspan="2">Ongoing</td><td>02</td></tr>
<tr><td>Ph.D.</td><td colspan="2">Completed</td><td>01</td><td colspan="2">Ongoing</td><td>04</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">UG-35, PG- 27</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="3">01</td><td>Granted</td><td colspan="2"></td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td></td><td>International Journal</td><td>53</td><td>Conferences</td><td>15</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="8">1. Published a book chapter in Novel Perspectives of Engineering Research Vol. 7
“Assessment of Artificial Neural Network-based Induction Motor Fault Classifier Using Continuous Wavelet Transform” Print ISBN: 978-93-5547-513-8, eBook ISBN: 978-93-5547-514-5</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr Saurabh Sureshrao Jadhao</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">14th July 1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-698260791</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">PhD Electrical Engineering</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14</td><td>Research</td><td>08</td><td>Industry</td><td>Nil</td><td>others</td><td>NA</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Electrical Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">1. Microprocessors and Microcontroller
2. Electrical Measurement and Instrumentation
3. Analog and Digital Circuits
4. Energy Audit and Management
5. Power Supply Systems
6. Artificial Intelligence
7. Power Quality</td></tr>
<tr><td>Post Graduate</td><td colspan="7">1. Advanced Power System Protection
2. Digital Protection of Power System
3. Power Quality Improvement Techniques</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>01</td><td>International Journals</td><td>30</td><td>Conferences</td><td>32</td></tr>
<tr><td>Masters Degree</td><td colspan="2">Completed</td><td>11</td><td colspan="2">Ongoing</td><td>02</td></tr>
<tr><td>Ph.D.</td><td colspan="2">Completed</td><td>--</td><td colspan="2">Ongoing</td><td>02</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Design and Development of Artificial Intelligence based Cotton Picking Machine sponsored by ICAR and in collaboration with Dr PDKV Akola</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="3">10</td><td>Granted</td><td colspan="2">01</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">01 Design and Development of Artificial Intelligence based Cotton Picking Machine</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>01</td><td>International Journal</td><td>30</td><td>Conferences</td><td colspan="2">32</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication.)</td><td colspan="8">S. S. Jadhao, R. S. Kankale, P. R. Bharambe, and G. N. Bonde, “AI and Machine Learning in Electrical Systems”, India: Vinsa Publishing, 2025, ISBN: 978-9348460769.</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Purushottam Ramesh Bharambe</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">29 August 1974</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-424113003</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">ME (EPS), Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>16 Years</td><td>Research</td><td>06</td><td>Industry</td><td>-</td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td colspan="2">Under Graduate</td><td colspan="7">Basic Electrical Engineering, Electrical Machines, Electrical Drives, Electrical Measurement & Instrumentation, Electrical Machine Design, Switchgear & Protection, Power Supply System.</td></tr>
<tr><td colspan="2">Post Graduate</td><td colspan="7">Digital Protection of Power System</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td colspan="2">No. of Papers Published in</td><td>National</td><td></td><td>International Journals</td><td>7</td><td>Conferences</td><td>5</td></tr>
<tr><td colspan="2">Masters Degree</td><td colspan="2">Completed</td><td colspan="2">06</td><td>Ongoing</td><td>-</td></tr>
<tr><td colspan="2">Ph.D.</td><td colspan="2">Completed</td><td colspan="2">-</td><td>Ongoing</td><td>-</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Guided 06 P.G. Dissertations & 17 U.G. Projects</td></tr>
<tr><td>10</td><td>Patents</td><td colspan="2">Filled</td><td colspan="2">02</td><td colspan="2">Granted</td><td colspan="2">-</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">-</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">-</td><td>International Journal</td><td>7</td><td>Conferences</td><td>5</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">S. S. Jadhao, R. S. Kankale, P. R. Bharambe, and G. N. Bonde, “AI and Machine Learning in Electrical Systems”, India: Vinsa Publishing, 2025, ISBN: 978-9348460769.</td></tr>
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1	Name	**Dr. Ravishankar Shaligram Kankale**							
2	Date of Birth	8th March 1987							
3	Unique ID	1-426009813							
4	Education Qualifications	M.E., Ph.D.							
5	Work Experience	Teaching	15Years 7 Months	Research	6 years out of teaching experience	Industry	Nil	Others	Nil
6	Area of Specialization	Electrical Power System							
7	Courses (Subjects) taught	Under Graduate	Basic Electrical Engineering, Power System Operation & Control, High Voltage Engineering, Numerical Methods & Optimization Techniques, Switchgear & Protection						
		Post Graduate	High Voltage Transmission, HVDC Transmission, Power System Modeling & Control, High Voltage Transmission System, Power System Modelling and Control, Generation Planning and Load Dispatch						
8	Research guidance (Number of Students)	No. of Papers Published in	NationalJournals	02	International Journals	20	Conferences	22	
		Masters Degree	Completed	04	Ongoing	Nil			
		Ph.D.	Completed	Nil	Ongoing	Nil			
9	Projects Carried out	Guided 04 P.G. Dissertations & 15 U.G. Projects							
10	Patents	Filled	02		Granted	Nil			
11	Technology Transfer	NIL							
12	Research Publications (No. of papers published in)	NationalJournals	02	International Journals	20	Conferences	22		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	S. S. Jadhao, R. S. Kankale, P. R. Bharambe, and G. N. Bonde, “AI and Machine Learning in Electrical Systems”, India: Vinsa Publishing, 2025, ISBN: 978-9348460769.							

<table>
<tr><td>1</td><td>Name</td><td colspan="9">Mukesh Ravindra Chavan</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="9">22/01/1990</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="9">1-7419303613</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">ME Electrical Power System, Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>13</td><td>Research</td><td>-</td><td>Industry</td><td>-</td><td>others</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Energy Audit & Management, Power Supply System, Electrical Machines, Numerical Methods, Electrical Power-I, Computer Methods in Power System Analysis</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Computer Methods in Power System Analysis</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>4</td><td>Conferences</td><td colspan="2">3</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td colspan="2">-</td><td>Ongoing</td><td colspan="2">-</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td colspan="2">-</td><td>Ongoing</td><td colspan="2">-</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">Social Distancing Device(2020-21), Energy Audit Case Study of Oil Mill (2018-19), Industrial Process Management using PLC, SCADA AND GSM(2017-18), Fault Detection in Transmission Line Based On Wavelet Transform(2015-16)</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="3">NA</td><td>Granted</td><td colspan="3">NA</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">NA</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">NA</td><td>International Journal</td><td>4</td><td>Conferences</td><td colspan="2">3</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="9">--</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Ravindra Kisanrao Mankar</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">4th Dec 1979</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-740787110</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M.E. (EPS), Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>16 Years & 5 Months</td><td>Research</td><td>Nil</td><td>Industry</td><td>Nil</td><td>others</td><td>Nil</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Fault Analysis in Electrical Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td colspan="2">Under Graduate</td><td colspan="6">ElectricDrivesandMeasurement,
ElectricalEnergyUtilization,
EnergyAuditandManagement,
Power Supply System,
Basic Electrical Drives and Control,
Electric Drives and Control,
ComputerMethods inPowerSystemAnalysis</td></tr>
<tr><td colspan="2">Post Graduate</td><td colspan="6">Power System Dynamics and Control,
Computer Aided Power System Analysis,
High Voltage Transmission</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td colspan="2">No. of Papers Published in</td><td>National</td><td>01</td><td>International Journals</td><td>04</td><td>Conferences</td><td>06</td></tr>
<tr><td colspan="2">Masters Degree</td><td colspan="2">Completed</td><td colspan="2">02</td><td>Ongoing</td><td>Nil</td></tr>
<tr><td colspan="2">Ph.D.</td><td colspan="2">Completed</td><td colspan="2">Nil</td><td>Ongoing</td><td>Nil</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Guided 02 P.G. Dissertations & 12 U.G. Projects</td></tr>
<tr><td>10</td><td>Patents</td><td colspan="2">Filled</td><td colspan="3">Nil</td><td>Granted</td><td>Nil</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td colspan="2">National</td><td>01</td><td>International Journal</td><td>04</td><td>Conferences</td><td>06</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="8">Nil</td></tr>
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1	Name	**Dr.Ganesh Narayan Bonde**							
2	Date of Birth	15/09/1988							
3	Unique ID	1-7393188291							
4	Education Qualifications	ME (Electrical Power System), Ph.d							
5	Work Experience	Teaching	13	Research	6 years out of teaching experience	Industry	Nil	others	Nil
6	Area of Specialization	Power Quality, Transformer Protection, Electrical Power System							
7	Courses (Subjects) taught	Under Graduate	Basic Electrical Engineering, Control System, Power System Stability, Power Supply System, Electronic Devices and Circuits						
		Post Graduate	Advanced Control System						
8	Research guidance (Number of Students)	No. of Papers Published in	National	01	International Journals	12	Conferences	08	
		Masters Degree	Completed	Nil	Ongoing	Nil			
		Ph.D.	Completed	Nil	Ongoing	Nil			
9	Projects Carried out	U.G Project Completed 08							
10	Patents	Filled	02	Granted	Nil				
11	Technology Transfer	Nil							
12	Research Publications (No. of papers published in)	National	01	International Journal	12	Conferences	08		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Dr.S.S.Jadhao,Dr.R .S.Kankale, P.R.Bharambe, G.N.Bonde " AI and Machine Learning in Electrical Systems" Vinsa Publications, ISBN-13: 978-9348460769							

<table>
<tr><td>1</td><td>Name</td><td colspan="9">Bhushan Subhash Rakhonde</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="9">22/04/1989</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="9">1-7427831525</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">B.E. , M. E. (EPS), Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14</td><td>Research</td><td></td><td>Industry</td><td></td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Electrical Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Basic Electrical Engineering, Electrical Machine-I, Basic Electrical Drives & Control, Numerical Method and Optimization Techniques, power System Operation & control, Computer Aided Machine Design.</td></tr>
<tr><td>Post Graduate</td><td colspan="8">DSPA</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td></td><td>International Journals</td><td>32</td><td>Conferences</td><td>03</td></tr>
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<tr><td>Ph.D.</td><td colspan="2">Completed</td><td>Nil</td><td colspan="2">Ongoing</td><td>Nil</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">14 U.G. Project</td></tr>
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<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">Nil</td><td>International Journal</td><td>32</td><td>Conferences</td><td colspan="2">03</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">1. Published a book chapter “Assessment of Battery Electric Vehicles using the EDAS Method: A Comprehensive Evaluation of Performance and Sustainability” in Electrical and Automation Engineering, March 2025, REST Publisher. https://doi.org/10.46632/eae/4/1/1</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">PRATIK RAVI DHABE</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">3rd October 1992</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-7398363338</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M.Tech.(IPS), Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>7 Years 8 Months</td><td>Research</td><td>3 years out of Teaching Experience</td><td>Industry</td><td>Nil</td><td>others</td><td>Nil</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Power System, Battery Management System, Electric Vehicles, Energy Storage, Power Quality</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td colspan="2">Under Graduate</td><td colspan="6">Power System, High Voltage Engineering, Computer Applications in Power System, Basic Electrical Engineering, Digital Signal Processing, , Electrical Measurements and Instruments, Energy Resources and Generation</td></tr>
<tr><td colspan="2">Post Graduate</td><td colspan="6">Advance Electrical Drives, Digital Signal Processing & Applications</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td colspan="2">No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>06</td><td>Conferences</td><td>08</td></tr>
<tr><td colspan="2">Masters Degree</td><td>Completed</td><td colspan="2">Nil</td><td colspan="2">Ongoing</td><td>Nil</td></tr>
<tr><td colspan="2">Ph.D.</td><td>Completed</td><td colspan="2">Nil</td><td colspan="2">Ongoing</td><td>Nil</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Guided 4 UG Projects</td></tr>
<tr><td>10</td><td>Patents</td><td colspan="2">Filled</td><td colspan="2">Nil</td><td colspan="2">Granted</td><td colspan="2">Nil</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">Nil</td><td>International Journal</td><td>06</td><td>Conferences</td><td colspan="2">08</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">2. Published a book chapter “Solve Selective Harmonic Elimination problem with a new Metaheuristic Optimization Algorithm” in Recent Advances in Material, Manufacturing, and Machine Learning, 1st Edition 2023, CRC Press, Taylor & Francis Group. https://doi.org/10.1201/9781003370628

3. Published a book chapter “Assessment of Battery Electric Vehicles using the EDAS Method: A Comprehensive Evaluation of Performance and Sustainability” in Electrical and Automation Engineering, March 2025, REST Publisher. https://doi.org/10.46632/eae/4/1/1</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">VIJAY SHIVAJI KARALE</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">4th July 1990</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-7393383920</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M. E. (Electrical Power System), Ph.D. (Regd.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>10 Years 6 Months</td><td>Research</td><td>Nil</td><td>Industry</td><td>Nil</td><td>others</td><td>Nil</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Power Electronics,Electrical Power System , Power Quality</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td colspan="2">Under Graduate</td><td colspan="6">Power Electronics ,Energy Audit and Management , Power Supply System, Network analysis, Electrical Circuit Analysis,
Numerical methods and Computer Programming, Energy Recourses and Generation.</td></tr>
<tr><td colspan="2">Post Graduate</td><td colspan="6">Application of Power Electronics to Power System, Power Quality Improvement Technique</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td colspan="2">No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>12</td><td>Conferences</td><td>07</td></tr>
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<tr><td colspan="2">Ph.D.</td><td colspan="2">Completed</td><td>Nil</td><td colspan="2">Ongoing</td><td>Nil</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Guided 5 UG Projects</td></tr>
<tr><td>10</td><td>Patents</td><td colspan="2">Filled</td><td colspan="2">02</td><td colspan="2">Granted</td><td colspan="2">Nil</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">Nil</td><td>International Journal</td><td>12</td><td>Conferences</td><td>07</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">1. “Voltage Sag Reduction Using A Uvtg-Based Dynamic Voltage Restorer ”, Futuristic Trends in Electrical Engineering, , IIP Series, Volume 3, Book 2, Part 6, Chapter 1 , e- ISBN: 978-93-6252-039-5</td></tr>
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1	Name	**Mr. Vaibhav Ashok Nagpure**							
2	Date of Birth	14/03/1995							
3	Unique ID	1-43361224731							
4	Education Qualifications	M-Tech							
5	Work Experience	Teaching	03	Research	--	Industry	--	others	--
6	Area of Specialization	Power System, Electrical Machines							
7	Courses (Subjects) taught	Under Graduate	Basic Electrical Engineering, Generation of Electrical Energy,Electrical Circuit Analysis, Numerical methods and Computer Programming, Energy Recourses and Generation						
		Post Graduate							
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	02	
		Masters Degree							
		Ph.D.							
9	Projects Carried out	Guided 01 UG Projects							
10	Patents	Filled		Granted					
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	--	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)								

<table>
<tr><td>1</td><td>Name</td><td colspan="9">Mr. Ganesh Devidas Khadsane</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="9">17/09/1985</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="9">1-43363627471</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">M-Tech</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>15</td><td>Research</td><td>--</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Power Electronics</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Basic Electrical Engineering, Electrical Measurement and Instrumentations, Electrical Machines,</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Smart Grid Technology, Electric Vehicle</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>04</td><td>International Journals</td><td>02</td><td>Conferences</td><td>02</td></tr>
<tr><td>Masters Degree</td><td>02</td><td colspan="2">--</td><td colspan="2">--</td><td colspan="2">--</td></tr>
<tr><td>Ph.D.</td><td>--</td><td colspan="2">--</td><td colspan="2">--</td><td colspan="2">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">Guided 14 UG Projects, 02- PG</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="3">--</td><td>Granted</td><td colspan="3">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td colspan="2">04</td><td>International Journal</td><td>02</td><td>Conferences</td><td colspan="2">02</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">--</td></tr>
</table>

➢ Department of Electronis and Telecommunication Engineering

1	Name	**Dr. Devesh D. Nawgaje – Associate Professor**							
2	Date of Birth	01/04/1972							
3	Unique ID	1-424113367							
4	Education Qualifications	B.E., M.E., PhD							
5	Work Experience	Teaching	25	Research	--	Industry	4	others	--
6	Area of Specialization	Digital Electronics, Embedded System, Evolutionary Computing Techniques							
7	Courses (Subjects) taught	Under Graduate	Digital Electronics, Microprocessor, Microcontroller, Computer Organization, Computer Network						
		Post Graduate	Artificial Intelligent System, Computer Network						
8	Research guidance (Number of Students)	No. of Papers Published in	National	3	International Journals	35	Conferences		8
		Masters Degree	Completed	5	Ongoing	1			
		Ph.D.	Completed	1	Ongoing	3			
9	Projects Carried out	Implementation of Evolutionary Computing Techniques For Cancer Diagnosis							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	3		International Journal	35	Conferences		8
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Kamlesh Bhagchand Khanchandani – Professor**							
2	Date of Birth	07/04/1967							
3	Unique ID	283166529767							
4	Education Qualifications	M.E, PhD							
5	Work Experience	Teaching	35 YEARS	Research	20	Industry	--	others	
6	Area of Specialization	VLSI and Embedded System Design, Digital Signal and Image Processing, Power Electronics							
7	Courses (Subjects) taught	Under Graduate	Digital Signal Processing, Embedded Systems, Electronic Circuit Design,Power Electronics, Digital Electronics and Design						
		Post Graduate	VLSI Design, Advanced DSP, Computer Aided Design, Digital Instrumentation						
8	Research guidance (Number of Students)	No. of Papers Published in	National	20	International Journal	40	Conferences	20	
		Masters Degree	Completed	40	Ongoing	01			
		Ph.D.	Completed	08	Ongoing	-09			
9	Projects Carried out	08 –Research and Development Projects funded by AICTE							
10	Patents	Filled	--Nil--		Granted	--Nil--			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National Conferences	10	International Journal	10				
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	TEXT BOOK on “ POWER ELECTRONICS” Published by McGraw Hill India…1998 ISBN-13. 978-0070583894							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. R. S. Dkekekar - Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">02 June 1967</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-424113133</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D. 2014
M.E. 1998
B.E. 1989</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>36</td><td>Research</td><td>06</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Power Electronics AC DC Drives , Application of Power Electronics to Power System</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Control Systems Engineering , Microprocessors& Microcontrollers , Biomedical Engineering, Instrumentation, Power Electronics, Values & Ethics, EDC Electronics Circuit Design</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Modern Electronics Design Technology (MEDT) , UHV (Universal Human Values)</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>08</td><td>International Journals</td><td>12</td><td>Conferences</td><td>05</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>24</td><td colspan="3">Ongoing</td><td>00</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>01</td><td colspan="3">Ongoing</td><td>01</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Projects based on different digital controllers , project based of Power electronics , project based on embedded systems and Solar Applications</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td colspan="3">Granted</td><td colspan="2">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>02</td><td>International Journal</td><td>12</td><td>Conferences</td><td colspan="2">05</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">--</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Manish Narayandas Tibdewal – Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">21.05.1968</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-424113007</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">B.E., M.E.,Ph.D. (INDIAN INSTITUTE OF TECHNOLOGY,KHARAGPUR-WB</td></tr>
<tr><td>5</td><td>Work Experience-Year</td><td>Teaching</td><td>34</td><td>Research</td><td>15</td><td>Industry</td><td>01</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Signal/Image Processing, Biomedical Signal/Image Processing and
Biomedical Instrumentation and Wearable Health-care Devices</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">EDC-I, EDC-II, NA/NT, DICs, ADIC, ADC, CSE, DC, PE, AEDC, DIVP, MPMC, MC, DSP, LIC, SAS, MATLAB Programming, etc.
Total @ 18 Subjects taught</td></tr>
<tr><td>Post Graduate</td><td colspan="8">DIPA, ADSP – 02PG Subjects</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>05</td><td>International Journals</td><td>17(SCI/SCOPUS)</td><td>Int. Conferences</td><td colspan="2">36</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>23</td><td>Ongoing</td><td colspan="4">--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>02</td><td>Ongoing</td><td colspan="4">01</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">Total (UG+PG) = (61+23)
= 84 Projects guided</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td colspan="2">Granted</td><td colspan="4">--</td></tr>
<tr><td>11</td><td>Technology Transfer/Grant</td><td colspan="9">1. 3.5 Lakh ATAL FDP Grant Utilized for Faculty Training (2024-25)
2. 51 Lakh RPS grant proposal applied to AICTE, New Delhi (2025-26)
3. 30 Lakh grant proposal applied to IICSSR, New Delhi (2025-26)</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National Journal</td><td>02</td><td>International Journal</td><td>17</td><td>Conferences (International +National)</td><td colspan="3">36+03</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">Reviewer of Book on C-Puzzle
(Pearson Education, India)</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Santosh Balkrishna Patil - Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">26-04-1974</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-424385441</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D. (VLSI- Electronics Engineering
Master of Engineering (Electronics)
Master of Business Administration (Marketing Management)
Bachelor of Engineering (Electronics & Telecomm. Engg.)
ADCSSAA- PG Computer Diploma.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>25</td><td>Research</td><td>17</td><td>Industry</td><td>2</td><td>others</td><td>0</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">VLSI-Analog/RF/Mixed Signal
Internet of Things (IoT)
Embedded System Design</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">VLSI ,Digital system Design,Analog Circuits,
Electronics Devices and Circuits,Analog& Digital Integrated Circuits, Microcontroller Applications,Digital Signal Processing, Object Oriented Programming (Java, C++),</td></tr>
<tr><td>Post Graduate</td><td colspan="7">CMOS VLSI Design, Advance Microcontrollers, Microprocess and Microcontrollers</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>01</td><td>International Journals</td><td>23</td><td>Conferences</td><td>05</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>25</td><td>Ongoing</td><td colspan="3">02</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>01</td><td>Ongoing</td><td colspan="3">Nil</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">50+</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="2">02</td><td>Granted</td><td colspan="3">03</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">-</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>01</td><td>International Journal</td><td>23</td><td>Conferences</td><td colspan="2">05</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="8">--</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Mr. Vinayak M. Umale – Associate Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">17.5.1966</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-424083647</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">DEE, BE (Industrial Electronics)
M. Tech. (Instrumentation)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>37</td><td>Research</td><td>--</td><td>Industry</td><td>01</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Analog and Digital Systems, Biomedical Based Instrumentation,
Computer and Communication Networking</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>U.G.</td><td colspan="7">EDC, NA, ADC, ADIC,EDC-II, AEDC, LIC, DIC, PCI, BME, CN, DCN, AC</td></tr>
<tr><td>P.G.</td><td colspan="7">Digital Instrumentation and Control, Digital Instrumentation</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>32</td><td>Conferances</td><td>24</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>27</td><td>Ongoing</td><td colspan="3">02</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">02</td></tr>
<tr><td>9</td><td>Projects Carried out (Sponsored)</td><td colspan="8">1. “Teleconference facility”, sponsored by Videsh Sanchar Nigam Ltd. Training & Teleco. Development Centre, Dighi, Pune, (Projectee: Abhishek A. Pandharipande and Group-2001-02)
2. “ Complete Automation of wire Drawing Machine using Siemens make Programmable Controller”, sponsored by Tata SSL Limited Tarapur wire plant, Thane(PG student-Mr. Manoj Sankhe, 2003-04)
3. “ SCADA-Supervisory Control And Data Acquisition system for sugar industries using microcontroller”, sponsored by Sharangdhar sugar Mills Limited, Mehkar, DistBuldhana(MS). (PG student-Miss Swati S. Jayade, 2009-10).
4. “PLC Based ratio Controller and Material Distribution”, sponsored by reliance Ind. Ltd, Nagpur Manufacturing Division, Mohada, Dist-Nagpur(MS). (PG stud.-Miss V. M. Badwaik, 2011-12)
5. “ Intelligent Maximum Power Point Tracking” sponsored by SAMVED Energy Systems Pvt. Limited, Pune(PG student-Mr. SwapnilWadodkar, 2014-15)
6. “Solar powered field protection system”,Sponsord by Mahatma Gandhi Institute for RularIndustrilisation(MGIRI), Wardha, 2018-19 (Projectee: Bharat Dhabekar, DilipPatil, PranitVithalkar, SurajPohekar),
7. “Defective Soap Recognition Using Machine Learning”,Sponsord by SNG Packaging , Khamgaon, 2020-21 (Projectee: Tejas Gode, PiyushBhomale, PrathameshEdne, UdayKhadse)
8. “Design and Development of Agricultural Drone Crop Sprayer”,Sponsord by DoxPro Robotics Pvt. Ltd., Amravati, 2020-21 (Projectee: ManishaMasne, PragatiDivekar, PragatiJinde, Swati Hingne)
9. “A systematic machine Learning Approach for Detection of Hemoglobin Levels”,Sponsord by DoxPro Robotics Pvt. Ltd., Amravati, 2021-22(Projectee: AshwiniSangle, PrajaktaSangle, RujutaSakhare, GauriZambre)</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td colspan="5">Granted</td><td>--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--</td></tr>
<tr><td>12</td><td>Research Publications (No.of papers published in)</td><td>National</td><td>--</td><td>International Journal</td><td>32</td><td>Conferances</td><td colspan="3">24</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">Monograms of:
1. Volume-I (Electronics Devices and Circuits)
2. Volume-IV (Analog& Digital Circuits)
3. Volume-V (Linear Integrated circuits)</td></tr>
</table>

1	Name	**Mr. Dinkar Laxman Bhombe – Assosiate Professor**							
2	Date of Birth	15 April 1966							
3	Unique ID	1-424113261							
4	Education Qualifications	BE (Industrial Electronics) MS (Electronics & Control Engg.)							
5	Work Experience	Teaching	33	Research	00	Industry	00	others	00
6	Area of Specialization	Electronics & Control Engineering Communication Engineering Artificial Intelligent System							
7	Courses (Subjects) taught	Under Graduate	Communication Engineering, Digital Communication, Fuzzy Logic & Neural Network, Data Communication Network, Biomedical Engineering, Industrial Electronics						
		Post Graduate	Digital Communication, Artificial Intelligence System						
8	Research guidance (Number of Students)	No. of Papers Published in	National	02	International Journals	22	Conferences	09	
		Masters Degree	Completed	19	Ongoing	00			
		Ph.D.	Completed	00	Ongoing	00			
9	Projects Carried out	00							
10	Patents	Filled	00	Granted		00			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	02	International Journal	22	Conferences	09		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Bhavna Pandurand Harne – Assistant Professor**							
2	Date of Birth	31/08/1971							
3	Unique ID	1-424385511							
4	Education Qualifications	Ph.D.							
5	Work Experience	Teaching	21	Research	-	Industry	-	others	-
6	Area of Specialization	EEG signal processing							
7	Courses (Subjects) taught	Under Graduate	DID, EDC, NA, EMF, CSE, OOP, STLD, Microprocessor 8086<Parallel computing						
		Post Graduate	Parallel computing						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	08	Conferences	02	
		Masters Degree	Completed	--	Ongoing	--01(Under Review)			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	20+							
10	Patents	Filled	--	Granted		--			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	05	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Dhiraj Pandharinath Tulaskar – Associate Professor**							
2	Date of Birth	26th November 1983							
3	Unique ID	1-424113265							
4	Education Qualifications	Ph.D. (Electronics and Telecommunication Engineering), M. E (Digital Electronics) B.E. (Electronics & Telecommunication)							
5	Work Experience	Teaching	16	Research	2	Industry	-	others	-
6	Area of Specialization	VLSI Communication & DSP, RF/Microwave Circuit Design, Analog and Digital Communication							
7	Courses (Subjects) taught	Under Graduate	Satellite Communication,Communication Engineering-I, Communication Engineering-II, Wireless Communication,Digital Signal Processing, Fiber Optic Communication, Analog and Digital Communication						
		Post Graduate	Digital Communication Techniques						
8	Research guidance (Number of Students)	No. of Papers Published in	National	6	International Journals	15	Conferences	3	
		Masters Degree	Completed	1	Ongoing	0			
		Ph.D.	Completed	0	Ongoing	2			
9	Projects Carried out	18							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	1							
12	Research Publications (No. of papers published in)	National	6	International Journal	15	Conferences	3		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Amit N. Dolas (Assistant Professor)**							
2	Date of Birth	17 July 1983							
3	Unique ID	1-426009973							
4	Education Qualifications	ME (Digital Electronics) Ph.D. (Pursuing)							
5	Work Experience	Teaching	16 Yrs.	Research	--	Industry	02	others	--
6	Area of Specialization	Robotics & Automation, Biomedical Signal and Image Processing							
7	Courses (Subjects) taught	Under Graduate	Electronics Devices & Circuits, Analog& Digital Circuits, Linear Integrated Circuits, Analog Electronics-I, Analog Electronics-II, Control System Engineering, Computer Networking, Electronic Workshop						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	01	
		Masters Degree	Completed	--	--	--			
		Ph.D.	--	--	On-going	--			
9	Projects Carried out	31 UG Projects							
10	Patents	Filled	--		Granted	--			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	02	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Vikas Kamalkishor Bhangdiya – Assistant Professor** 							
2	Date of Birth	20-July-1982							
3	Unique ID	1-426010459							
4	Education Qualifications	M.Tech (Electronics), Ph.D. (Pursuing)							
5	Work Experience	Teaching	15 Yrs.	Research	--	Industry	1.5 Yrs.	others	--
6	Area of Specialization	Embedded System, Machine Learning, Quantum Computing							
7	Courses (Subjects) taught	Under Graduate	Digital System Design, Signals and Systems, Communication Engineering, Microcontroller and Application, Communication Theory, Object-Oriented Programming, Robotics and Application, Embedded System						
		Post Graduate	Embedded System Design, CMOS VLSI Design						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	20 UG							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	02	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Kamlesh Tulshiram Kahar – Assistant Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">23/10/1986</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-699072381</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">Ph.D. (Electronics & Telecommunication Engineering), MTech (Digital Communication)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>16</td><td>Research</td><td>03</td><td>Industry</td><td></td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">MEMS, Power Electronics, Digital Communication</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Power Electronics, Instrumentation & Sensors , Communication Engineering I & II, Linear Electronic Devices, Analog & Digital Communication, Electronic Devices & Circuits</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Digital Instrumentation, Technical Paper Writing</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>4</td><td>International Journal</td><td>8</td><td>Conferences</td><td>5</td><td></td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--Nil-</td><td>Ongoing</td><td colspan="4">--Nil--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--Nil-</td><td>Ongoing</td><td colspan="4">--Nil--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">14</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="2">3</td><td>Published</td><td colspan="4">3</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>4</td><td>International Journal</td><td>8</td><td>Conferences</td><td colspan="3">5</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">--Nil --</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="7">Mrs. Komal S. Vyas – Assistant Professor</td><td rowspan="4">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="7">19/04/1991</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="7">1-7393043452</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="7">PhD Pursuing, ME (Digital Electronics)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>12</td><td>Research</td><td>-</td><td>Industry</td><td>-</td><td colspan="2"></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Digital Image Processing
Communication Engineering</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">C&NS, WC, SC, FOC, FO,FOSC, IWT, SOFC,CE,CE-II(Old),CE-II(New),DCN,CN,DC</td></tr>
<tr><td>Post Graduate</td><td colspan="7">-</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--Nil--</td><td>International Journals</td><td>03</td><td>Conferences</td><td>01</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--Nil--</td><td>Ongoing</td><td colspan="3">--Nil--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--Nil--</td><td>Ongoing</td><td colspan="3">--Nil--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">12</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--Nil--</td><td>Granted</td><td colspan="5">--Nil--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--Nil--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>01</td><td>International Journal</td><td>02</td><td>Conferences</td><td colspan="3">03</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">--Nil--</td></tr>
</table>

1	Name	**Dr. Swapnil PanjabraoBadar – Assistant Professor** 							
2	Date of Birth	04/12/1986							
3	Unique ID	1-11121397101							
4	Education Qualifications	M.Tech (Electronics), Ph.D (Electronics and Telecommunication)							
5	Work Experience	Teaching	16	Research	05	Industry	1.5 Yr.	others	--
6	Area of Specialization	VLSI Design							
7	Courses (Subjects) taught	Under Graduate	VLSI Design, CMOS Design, Network Analysis/Network Theory, Digital Electronics/Digital System Design/Fundamental of Digital Electronics, Electronics Devices & Circuits, Electronics Devices & Components						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	--						
		Ph.D.	--						
9	Projects Carried out								
10	Patents	Filled	--Nil--	Granted		--Nil--			
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	01	International Journal	02	Conferences	05		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)	--Nil--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Mr. TEJRAO PANJABRAO MARODE – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">23/06/1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-7392232118</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">ME(DIGITAL ELECTRONICS)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>10yrs</td><td>Research</td><td>--</td><td>Industry</td><td>--</td><td>others</td><td>8yrs</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">PROGRAMMING AND NETWORKING</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Web Development/OOPS/Introduction to JAVA/Introduction to Python/Cryptography and Network Security/Communication Networks/Electronics Workshop</td></tr>
<tr><td>Post Graduate</td><td colspan="7">NIL</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--Nil--</td><td>International Journal</td><td>4</td><td>Conferences</td><td>-Nil-</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--Nil--</td><td colspan="3"></td><td>--Nil--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--Nil--</td><td colspan="3"></td><td>--Nil--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Implementations (SAP-ERP)</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--Nil--</td><td>Granted</td><td colspan="5">--Nil--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td></td><td>International Journal</td><td>7</td><td>Conferences</td><td colspan="2">2</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">--Nil--</td></tr>
</table>

1	Name	**Mr. Shon Gajanan Nemane – Assistant Professor**							
2	Date of Birth	30/07/1988							
3	Unique ID	1-4448215358							
4	Education Qualifications	BE(EXTC),ME(Digital Electronics)Ph.D. (Electronics and Telecommunication Engineering) Reg.							
5	Work Experience	Teaching	11 Years	Research	01	Industry	--	others	--
6	Area of Specialization	Digital Electronics							
7	Courses (Subjects) taught	Under Graduate	EDC, ADE, DE, DSD, ADC, SC, AC						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences		--
		Masters Degree	Completed	--	Ongoing				---
		Ph.D.	Completed	---	Ongoing				--
9	Projects Carried out	09							
10	Patents	Filled	--	Granted		--			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	Completed	08	International Journal	08	Conferences			01
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	(Lean Manufacturing. ISBN-13-979-8899290817, 29/04/25)							

1	Name	**Mr. Vikram Suresh Ingole – Assistant Professor**							
2	Date of Birth	17/06/1988							
3	Unique ID	1-4448215276							
4	Education Qualifications	PhD(Pursuing) M.E.,B.E.							
5	Work Experience	Teaching	14	Research	--	Industry	--	others	--
6	Area of Specialization	AI/ML,IOT							
7	Courses (Subjects) taught	Under Graduate	Microprocessor, Microcontroller, AVR Microcontroller ,Computer Architecture						
		Post Graduate	--Nil--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--Nil--	International Journals	7	Conferences	1	
		Masters Degree	Completed	--Nil--	Ongoing			--Nil--	
		Ph.D.	Completed	--Nil--	Ongoing			--Nil--	
9	Projects Carried out	1.Implementation of cost effective smart Hydrophonic System Monitoring and controlling using IoT 2.Cattle health monitoring system 3.Crop Yield Disease Detection Using Deep Learning							
10	Patents	Filled	--Nil--	Granted	--Nil--				
11	Technology Transfer	--Nil--							
12	Research Publications (No. of papers published in)	National	--Nil--	International Journal	07	Conferences	01		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--Nil--							

1	Name	**Ms. Ashwini Deshmukh – Assistant Professor**							
2	Date of Birth	23-10-91							
3	Unique ID	1-9312315828							
4	Education Qualifications	M.E Ph.D. (Pursuing)							
5	Work Experience	Teaching	7.5	Research	--	Industry	--	others	--
6	Area of Specialization	Digital Electronics							
7	Courses (Subjects) taught	Under Graduate	EMF,BASIC ELECTRONICS,DIGITAL TECHNIQUES,IWT, ENGINEERING ECONOMICS,MOBILE COMMUNICATION,5G-6G, ANALOG DIGITAL COMMUNICATION.						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	3	Conferences		
		Master's Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	3							
10	Patents	Filled	--	Granted		--			
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	1	International Journal	2	Conferences	3		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Harshavardhan Balkrushana Patil – Assistant Professor**							
2	Date of Birth	10th October 1991							
3	Unique ID	1-43603801551							
4	Education Qualifications	**M.E. (Digital Electronics), B.E. (Electronics and Telecommunication Engg.)**							
5	Work Experience	Teaching	10	Research	--	Industry	--	others	--
6	Area of Specialization	Digital and Analog Electronics, Communication Systems							
7	Courses (Subjects) taught	Under Graduate	Digital System Design, Network Theory, Wireless Sensor Network, Wireless Communication.						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	2. Autonomous Robot Controlled System 3. Elevating Canteen Management with a Modern Web 4. Indoor Navigation System For Visually Impaired Using Beacon Technology							
10	Patents	Filled	--Nil--	Granted	01-Emotional Insight Monitoring Wearable Device				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--Nil--	International Journal	03	Conferences	06		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	----							

1	Name	**Mr. Mahendra Bhaurao Dhamande – Assistant Professor** 							
2	Date of Birth	17 September 1963							
3	Unique ID	1-10971449164							
4	Education Qualifications	ME (Digital Electronics)							
5	Work Experience	Teaching	04	Research	--	Industry	--	others	32
6	Area of Specialization	Analog & Digital Electronics, Instrumentation							
7	Courses (Subjects) taught	Under Graduate	Lab: SDL-VI, Instrumentation and sensors, EDC, VSEC-1, Electrical Measurement and Instrumentation, ELBI, AC, ADC, MTT						
		Post Graduate	M.E. LAB: DCT, VLSI, AM, DIP&A						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	Software and hardware projects based on Electrical engineering- U.G.-25-P.G.20							
10	Patents	Filled	--	Granted	---				
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	--	International Journal	--	Conferences	--		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Sanjaykumar Purushottam Satal – Assistant Professor** 							
2	Date of Birth	11/11/1969							
3	Unique ID	1-10971449176							
4	Education Qualifications	M.E. (Digital Electronics)							
5	Work Experience	Teaching	1	Research	-	Industry	-	others	-
6	Area of Specialization	Power Electronics, Instrumentation							
7	Courses (Subjects) taught	Under Graduate	Value & Ethics, Power Electronics						
		Post Graduate	M.E.						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	3	Conferences	---	
		Masters Degree	Completed	---	Ongoing	---			
		Ph.D.	Completed	---	Ongoing	---			
9	Projects Carried out	Software and hardware projects based on Electrical engineering- U.G.-25-P.G.20							
10	Patents	Filled	---		Granted	---			
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	---	International Journal	3	Conferences	---		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	---							

➢ Department of Mechanical Engineering (Please update data 2025-26)

1	Name	**Dr. Shivshankar P. Trikal – HOD & Professor**							
2	Date of Birth	19/12/1971							
3	Unique ID	1-424537075							
4	Education Qualifications	M.E, Ph.D.							
5	Work Experience	Teaching	30	Research	--	Industry	03	others	--
6	Area of Specialization	Manufacturing Engineering							
7	Courses (Subjects) taught	Under Graduate	1) MP-I, 2) MP-II, 3) TE, 4) MTD, 5) PPC						
		Post Graduate	1) AMP, 2) RPT						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--		Conferences	--
		Masters Degree	Completed	15	Ongoing	05			
		Ph.D.	Completed	--	Ongoing	03			
9	Projects Carried out	30							
10	Patents	Filled	01	Granted	--				
11	Technology Transfer	-							
12	Research Publications (No. of papers published in)	National	02	International Journal	25	Conferences	05		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Vinay K. Thute – Associate Professor**							
2	Date of Birth	15/04/1972							
3	Unique ID	1-424537079							
4	Education Qualifications	B.E. (Mechanical Engg.), M. Tech. (Mech. Systems Design), Ph.D. (Mechanical Engg.)							
5	Work Experience	Teaching	29	Research	--	Industry	01	others	--
6	Area of Specialization	Mechatronics, Theory of Machines & Mechanisms, Machine Condition Monitoring, Mechanical Systems Design							
7	Courses (Subjects) taught	Under Graduate	1) Theory of Machine, 2) Mechatronics, 3) Engg. Graphics, 4) Dynamics of Machines, 5) Manufacturing Processes, 6) Energy Conversion						
		Post Graduate	1) Mechatronics in Systems Design, 2) Design of Material Handling Equipments						
8	Research guidance (Number of Students)	No. of Papers Published in	National	02	International Journals	01	Conferences	01	
		Masters Degree	Completed	--	--	--			
		Ph.D.	Completed	--	--	--			
9	Projects Carried out	25							
10	Patents	Filled	01		Granted	00			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	02	International Journal	06	Conferences	04		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Madhao Baliram Bhambere – Assistant Professor**							
2	Date of Birth	15/10/1981							
3	Unique ID	1-425538641							
4	Education Qualifications	M-Tech. (R), Ph.D. (Pursuing)							
5	Work Experience	Teaching	21 Years	Research	02	Industry	--	others	--
6	Area of Specialization	Thermal Engineering, Computational Fluid Dynamics.							
7	Courses (Subjects) taught	Under Graduate	1) Heat Transfer, 2) Fluid Power-I, 3) Fluid Power-II, 4) EG, 5)CSE, 6) TOM-I, 7) RAC.						
		Post Graduate	1) Research Methodology						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	08	Conferences	04	
		Masters Degree	Completed	03	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	23							
10	Patents	Filled	--	Granted		--			
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	08	Conferences	04		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Chinmay Vasant Patil – Assistant Professor**							
2	Date of Birth	11/07/1978							
3	Unique ID	1-425538645							
4	Education Qualifications	M.E. (CAD/CAM) Ph. D (Registered)							
5	Work Experience	Teaching	22	Research	--	Industry	01	others	--
6	Area of Specialization	Computer Aided Design & Manufacturing							
7	Courses (Subjects) taught	Under Graduate	1) EM, 2) IRA, 3) AI						
		Post Graduate	1) CADE						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	03	Conferences	--	
		Masters Degree	Completed	04	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	Design and Analysis of advanced Seed Sowing machine							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	02	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Javed G. Khan – Associate Professor**							
2	Date of Birth	06/11/1978							
3	Unique ID	1-7393732985							
4	Education Qualifications	Ph.D.							
5	Work Experience	Teaching	18	Research	03	Industry	03	others	--
6	Area of Specialization	Manufacturing Engineering							
7	Courses (Subjects) taught	Under Graduate	1) Automobile Engineering, 2) Automation Engineering, 3) Lean Manufacturing, 4) Production Planning and Control, 5) Engineering Graphics						
		Post Graduate	1) Lean Manufacturing, 2) Rapid Prototyping						
8	Research guidance (Number of Students)	No. of Papers Published	National	03	International Journals	08	Conferences	04	
		Masters Degree	Completed	04	Ongoing	01			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	16							
10	Patents	Filled	01	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	04	International Journal	12	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Ajay Suresh Bharule – Assistant Professor**							
2	Date of Birth	11/08/1983							
3	Unique ID	1-425658591							
4	Education Qualifications	M-Tech.							
5	Work Experience	Teaching	16	Research	-	Industry	02	others	--
6	Area of Specialization	Machine Design & Analysis							
7	Courses (Subjects) taught	Under Graduate	1) EM, 2) MOM, 3) MD, 4) TOM, 5) ORT 6) DME						
		Post Graduate	1) AMD, 2) ESA						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	10	Conferences	--	
		Masters Degree	Completed	10	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	-			
9	Projects Carried out	15							
10	Patents	Filled	--		Granted	--			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	-	International Journal	5	Conferences	--		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Pradeep T. Patokar – Assistant Professor**							
2	Date of Birth	22/10/1986							
3	Unique ID	1-458662391							
4	Education Qualifications	ME(Manufacturing)							
5	Work Experience	Teaching	16	Research	--	Industry	--	others	--
6	Area of Specialization	Manufacturing							
7	Courses (Subjects) taught	Under Graduate	Workshop-I, Workshop-II						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals		Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	08							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	12	Conferences	--		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Nilesh Haribhau Khandare – Associate Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">14/08/1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-1480531633</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">D.M.E (Mech. Engg.), B.E (Mech. Engg.) M.E (CAD/CAM), Ph.D. (Mech. Engg.)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14.8</td><td>Research</td><td>18</td><td>Industry</td><td>4.8</td><td>others</td><td>02</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Manufacturing Engineering, Production Technology, Industrial Engineering, Lean & Agile Manufacturing, Robotics and Industrial Applications</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">• Robotics and Industrial Applications
• Automation Engineering
• Metrology & Quality Control
• Automobile Engineering
• Material Science /Engineering Metallurgy
• Productivity Techniques
• Machine Design
• Manufacturing Process I
• Manufacturing Process II
• Production Technology
• Measurement System
• Non- Conventional Energy Sources</td></tr>
<tr><td>Post Graduate</td><td colspan="8">• Lean Manufacturing
• Computer Assisted Production Management
• Advanced Material Technology</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>05</td><td>International Journals</td><td>04</td><td>Conferences</td><td>03</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>04</td><td>Ongoing</td><td colspan="3">01</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">18</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>02</td><td>Granted</td><td colspan="5">02</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">1) “Leakage Testing of Reverse Sleeve Yoke without water” (Transferred to Industry)
2) "Design and Development of Solar PV Panel Cleaning System" under RGSTC, Mumbai & SGBAU Amravati (Transferred to Industry)</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>11</td><td>International Journal</td><td>18</td><td>Conferences</td><td colspan="3">02</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">01 – Books of Abstract-1st Doctoral Colloquium on Artificial Intelligence by “LAP Lamberts Academic Publishing”,
ISBN- 978-620-9-21600-8,
Year of Publication – 30th August 2025</td></tr>
</table>

1	Name	**Mr. Nitin B. Borkar – Assistant Professor**							
2	Date of Birth	09/10/1984							
3	Unique ID	1-1478729608							
4	Education Qualifications	B.E. ,M.Tech							
5	Work Experience	Teaching	15	Research	--	Industry	--	others	--
6	Area of Specialization	Production Engineering							
7	Courses (Subjects) taught	Under Graduate	1) EG, 2) IMC, 3) MOM, 4) MDR, 5) PT, 6) E Met.						
		Post Graduate	1) CAPM 2)DMHE						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	05	Conferences	--	
		Masters Degree	Completed	05	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	16							
10	Patents	Filled	01	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	06	Conferences	03		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. S. Q. Syed – Assistant Professor**							
2	Date of Birth	06/11/1984							
3	Unique ID	1-1435368613							
4	Education Qualifications	M-Tech, PhD (Pursuing)							
5	Work Experience	Teaching	14	Research	--	Industry	01	others	--
6	Area of Specialization	Thermal Engineering							
7	Courses (Subjects) taught	Under Graduate	1) Energy Conversion-I, 2) Energy conversion-II, 3) Engineering Thermodynamics, 4) Non-conventional energy sources , 6) Heat Transfer						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	-			
9	Projects Carried out	14							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	3	Conferences	1		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Dr. Kiran V. Chandan – Assistant Professor**							
2	Date of Birth	01/07/1989							
3	Unique ID	1-7398363239							
4	Education Qualifications	M.Tech. Ph.D.							
5	Work Experience	Teaching	10	Research	--	Industry	02	others	--
6	Area of Specialization	Mechanical Engineering Design							
7	Courses (Subjects) taught	Under Graduate	1) Fluid Mechanics, 2) IC Engines, 3) Thermodynamics 5) Basics of Mechanical engineering, 6) Engineering Mechanics 7) Machine Drawings.						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National		International Journals		Conferences		
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	Productivity Enhancement of Crimping operation LPG based incinerator for Medical waste Passive Solar tracker (IEI, sponsored project)							
10	Patents	Filled	1	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	1	International Journal	05	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Piyush Ashokrao Dalke – Assistant Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">07/02/1993</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-10970730651</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">B.E , M-Tech , Ph-D</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>10</td><td>Research</td><td>-</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Production Engineering</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">EME,PT,ED,DTIL,AEEV,IPR,MP</td></tr>
<tr><td>Post Graduate</td><td colspan="8">--</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>-</td><td>Conferences</td><td>-</td></tr>
<tr><td>Master’s Degree</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="4">-</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="4">-</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">07</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>18</td><td>Granted</td><td colspan="5">04</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>01</td><td>International Journal</td><td>10</td><td>Conferences</td><td>04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">• “Introduction to Mechanical Engineering” in LAMBERT International publication ISBN No.978- 613-9-97609-6, 2018
• “Basic Mechanical Engg.” in Gigatech Publishing House national publication under SPPU Pune syllabus for FE departmental. ISBN No. 978-81-93505-78-6 ,2019
• “Engineering Graphics” in Nirali prakashan Publishing House national publication under SPPU Pune syllabus for FE departmental. ISBN No. 978-93-89825-75-6,2020
• “Engineering Graphics” in Nirali prakashan Publishing House national publication under SGBAU Amravati syllabus for FE departmental. ISBN No.978-93-89825-75-6,2020
• “Fluid Mechanics” in Aditya Publishing House national publication under SGBAU Amravati syllabus for SE departmental. ISBN No.938994483-X, 2021
• GRIN Publishing GmbH “Integration of compressors for air powered bicycles” online as an e-book and has the following ISBN: 9783346148360, 2019
• “Production Process” in Nirali Publication under Mumbai University syllabus ISBN:9789354515521, 2023</td></tr>
</table>

	Name	**Dr. Saurabh P. Joshi – Assistant Professor**							
2	Date of Birth	18th August 1988							
3	Unique ID	1-38081534742							
4	Education Qualifications	M.E., Ph.D.							
5	Work Experience	Teaching	10	Research	--	Industry	--	others	--
6	Area of Specialization	Thermal Engineering, Energy Management, Heat Transfer Augmentation							
7	Courses (Subjects) taught	Under Graduate	Applied Thermodynamics, Fluid Mechanics, Energy Conversion, Non Conventional Energy Sources, Turbo Machines & Automobile Engineering						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	02							
10	Patents	Filled	--	Granted	02				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	06	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Prof. Kailas R. Dudhe – Assistant Professor**							
2	Date of Birth	30th December 1992							
3	Unique ID	1-38076510081							
4	Education Qualifications	Ph.D.(Pursuing), M.E(Thermal Engineering), B.E.(Mechanical Engineering)							
5	Work Experience	Teaching	6.5	Research	--	Industry	--	others	--
6	Area of Specialization	Thermal Engineering & Plate Heat Exchanger							
7	Courses (Subjects) taught	Under Graduate	Engineering Graphics, Operation Research Techniques, Fluid Mechanics, Design of Machine Element, Project Management, Metrology and Quality Control						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	--							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	1	Conferences	1		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Prof. Vishnu T. Mahske – Assistant Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">8th August 1991</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-43604623428</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">Ph. D (Pursuing) M.E., B.E.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>06</td><td>Research</td><td>-</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Cryogenics Engineering, Heat and Power, Material Science, Refrigeration And Air Conditioning.</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Engineering Thermodynamics, IC Engine, Theory of Machine (TOM), Material Science, Automobile Engineering, Refrigeration and Air Conditioning & Manufacturing Processes</td></tr>
<tr><td>Post Graduate</td><td colspan="8">--</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>--</td><td>Conferences</td><td>--</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>Completed</td><td colspan="3">Ongoing</td><td>--</td></tr>
<tr><td>Ph.D.</td><td>Pursuing</td><td>Pursuing</td><td colspan="3">Ongoing</td><td>--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">1. Design and development of plastic waste management system using cryogenic techniques
2. Cryogenic investigation of 52100 bearing steel in comparison with dry and flood machining.
3. Performance analysis of diesel engine using waste transformer oil blends.</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>--</td><td>International Journal</td><td>02</td><td>Conferences</td><td>02</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">--</td></tr>
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1	Name	**Prof. Ganesh S. Wahile – Assistant Professor**							
2	Date of Birth	3rd October 1991							
3	Unique ID	1-43604623540							
4	Education Qualifications	Ph. D Pursuing (Govt. College of Engg. Amravati) M.E., B.E.							
5	Work Experience	Teaching	10	Research	--	Industry	--	others	--
6	Area of Specialization	Thermal Engineering / Mechatronics / IoT/AI-ML/Photovoltaic Panel							
7	Courses (Subjects) taught	Under Graduate	Engineering Mechanics, Industrial Robotics and Applications, Mechatronics, Automation & Machine Learning, Thermodynamics, Heat Transfer, Material Technology, Production Processes-I, Non-Conventional Energy sources.						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	--	Conferences	01	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Pursuing	--	Ongoing	--			
9	Projects Carried out	Total - 10 1. Experimental Investigation of Thermal Management of Photovoltaic Module 2. Thermal Management of Electrical Vehicle Battery System using Immersion Cooling 3. Latent Heat Storage System By Using Different Phase Change Materials 4. Experimental Investigation of Photovoltaic Panel Using Evaporative Cooling etc.							
10	Patents	Filled	01	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	03 (SCI) 09 (Scopus)	Conferences	13		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)								

➢ Department of Computer Science and Engineering

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Jaikumar M. Patil – HOD & Associate Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">10-Nov-1972</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-1478310273</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M. Tech (CSE), Ph.D.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>23 Yrs</td><td>Research</td><td>12 Yrs</td><td>Industry</td><td>3 Yrs</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Data Mining and Learning Analytics</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Computer Programming, Software Engineering, Computer Architecture and Organization, Web Engineering, Computer Organization, Computer Architecture, File Structures and Data Processing, Object Oriented Programming, Programming Methodology, Object Oriented Analysis and Design.</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Advanced Computer Architecture, Embedded System Design</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>09</td><td>Conferences</td><td>05</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>10</td><td>Ongoing</td><td colspan="3">01</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">05</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">UG - 22 PG - 14</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>07</td><td>Granted</td><td colspan="5">01</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>03</td><td>International Journal</td><td>24</td><td>Conferences</td><td colspan="3">13</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication.)</td><td colspan="8">03
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<tr><td>1</td><td>Name</td><td colspan="8">Dr. Narendra M. Kandoi – Associate Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">27th November 1967</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-426009707</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph. D. (Computer Science and Engg)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>36 Years</td><td>Research</td><td>12 Years</td><td>Industry</td><td>---</td><td>others</td><td>----</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Web Technology and Security</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Programming Methodology, Operating System, Web Engineering ,Computing Resource Management ,File Structure and Data Processing ,Computer Organization, Theory of Computation, Introduction to Cyber Security, Design and analysis of Algorithm , 'C' programming Block chain Fundamentals, Distributed Ledger Technology</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Expert System Design ,Embedded System ,System Security ,Advanced Compilation Techniques</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>----</td><td>International Journals</td><td>08</td><td>Conferences</td><td>12</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>12</td><td>Ongoing</td><td colspan="3">01</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>01</td><td>Ongoing</td><td colspan="3">04</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8"></td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>01</td><td>Granted</td><td colspan="4">------</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>-------</td><td>International Journal</td><td>16</td><td>Conferences</td><td colspan="3">04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">Name of the Book :AI Driven Healthcare
Publisher: VINSA
ISBN: 978-81-978305-0-1
Year:2024</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="9">Chandrashekhar Marotrao Mankar – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="9">30/10/1968</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="9">1-421854911</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">B. E. (CSE), M.E.(CSE)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>27 Years</td><td>Research</td><td>--</td><td>Industry</td><td>2 Years</td><td>others</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Computer Network, Data Sci & ML</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Computer Network & Internet, Artificial Intelligence, Database Systems, Theory of Computation, System Software, Compiler Design, Software Engineering,Network Security, Advanced Unix/ Linux Programming, Mobile Computing, Embedded Systems.</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Operating System Design,Advanced Compiler Design, Algorithmics,Mobile Computing,Embedded Systems Design.</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>17</td><td>Conferences</td><td>21</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>17</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">-</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>-</td><td>Granted</td><td colspan="5">-</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">Worked as a resource person during the “ AI & ML Workshop .</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>04</td><td>International Journal</td><td>05</td><td colspan="3">Conferences</td><td>04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">-</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Vishwanath Sampat Mahalle – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">1st February 1967</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-421855191</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">B. E. (CSE), M.E.(CE), PhD</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>27 Years</td><td>Research</td><td>--</td><td>Industry</td><td>1 Year</td><td>others</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Computer Vision and Machine Learning</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Object Oriented Programming (C++), Object Oriented Programming (Java), Programming Methodology, Data Structures, Object Oriented Analysis and Design, Software Engineering, Network Security, Cryptography. Professional Ethics</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Object Oriented System
System Security</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>17</td><td>Conferences</td><td>21</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>14</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">-</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>-</td><td>Granted</td><td colspan="5">-</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Worked as a resource person during the “ Ai & Ml Workshop.</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>04</td><td>International Journal</td><td>05</td><td>Conferences</td><td colspan="2">04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="8">-</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Pankaj K. Bharne – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">25-07-1982</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-426009817</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D.(CSE), M.E., MBA(HR)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14.9 Yrs</td><td>Research</td><td>03 Yrs.</td><td>Industry</td><td>1 .10 Yr.</td><td>other</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Data Science & Statistics, ERP Solutions, HR Payroll</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">• Data Science & Statistics
• Operating System
• Professional Ethics and Management
• Artificial Intelligence
• Discrete Structure & Graphics design
• Social Science & Engg. Economics</td></tr>
<tr><td>Post Graduate</td><td colspan="7">• Operating System Design
• Advanced Compiling Techniques</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>05</td><td>Conferences</td><td>05</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>04</td><td>Ongoing</td><td colspan="3">02</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">UG: 08 PG: 02</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>-</td><td>International Journal</td><td>17</td><td>Conferences</td><td colspan="3">14</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">• Book Published on title “ Data Science with Python and Dask” Rest Publication Feb. 25, ISBN No 978-81-984044-2-8</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Ms. Kalyani P. Sable – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">16.11.1988</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-1476665323</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">B.E. (CSE), M.E. (CSE), Pursuing Ph. D.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14</td><td>Research</td><td>--</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Machine Learning</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Data Structures, Computer Programming, Object Oriented Programming, Discrete Structures, Data Communication, Software Engineering, Data Communication & Networking, Digital Integrated Circuits, Network Security, Database Management Systems, E-commerce.</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Algorithmics, Embedded Systems Design</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>--</td><td>Conferences</td><td>--</td></tr>
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<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">UG-27 ,PG- 3</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
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<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>--</td><td>International Journal</td><td>03</td><td>Conferences</td><td colspan="2">06</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">1
Data Structures , ISBN: 9789373614472, 9789373614472
2025-26,
Alpha International Publication</td></tr>
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<tr><td>1</td><td>Name</td><td colspan="8">Shrijeet Bharatbhushan Pagrut - – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">07-07-1991</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-10971449117</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M.E.(CSE),Phd Purusing</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>10</td><td>Research</td><td>0</td><td>Industry</td><td>0</td><td>others</td><td>0</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Mobile Computing and Information Security, Drone Technology.</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Mobile Communication, Data Communication, Network Security, Computer Architecture, File Structure and Data Processing, Theory of Computing, Computer Programming using C ,Digital Forensic, Secirity policy & governance.</td></tr>
<tr><td>Post Graduate</td><td colspan="7">ACA</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>-</td><td>International Journals</td><td>02</td><td>Conferences</td><td>01</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">10</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>08</td><td colspan="2">Granted</td><td colspan="3">0</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Work as a Resource Person in “Carrier guidance in cyber security gaidevi Mahavidhyala at Akola.</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>5</td><td>International Journal</td><td>02</td><td>Conferences</td><td>01</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)</td><td colspan="8"></td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Rupali Anantro Zamare – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">16/10/1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-10971449081</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">BE(IT),ME(CSE),MBA(SYSTEMS),PhD</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14.5</td><td>Research</td><td>--</td><td>Industry</td><td>--</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Computer Science and Engineering</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Programming and Problem Solving(Python),Fundamentals of Programming Language, Database Management System ,Software architecture Design Patterns, Advance DBMS, Computer Organization, Object Oriented Modelling, Information Retrieval and Design,</td></tr>
<tr><td>Post Graduate</td><td colspan="7">Organizational Behaviour and ERP, MIS.</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>1</td><td>International Journals</td><td>5</td><td>Conferences</td><td>--</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>1</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">21 (U.G.)</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>1</td><td>International Journal</td><td>7</td><td>Conferences</td><td colspan="2">3</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.</td><td colspan="8">--</td></tr>
</table>

1	Name	**Ms. Pooja Ramesh Pohare – Assistant Professor**							
2	Date of Birth	26/10/1994							
3	Unique ID	1-44716982686							
4	Education Qualifications	B.E.(CSE), M.E.(CE)							
5	Work Experience	Teaching	1.5 Yrs	Research	---	Industry	01	others	
6	Area of Specialization	Web Technology and Object Oriented Programming							
7	Courses (Subjects) taught	Under Graduate	Introduction to Web Technology, Computer Fundamentals, Foundation of Computing and Programming, Computer Hardware and Networking						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	----	International Journals	01	Conferences	-Nil-	
		Masters Degree	----						
		Ph.D.	---						
9	Projects Carried out	02							
10	Patents	Filled	--	Granted	---				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	---	International Journal	02	Conferences	---		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	-----							

1	Name	**Ms. Sayali Manikrao Jawake – Assistant Professor**							
2	Date of Birth	05-02-1991							
3	Unique ID	1-4837736632							
4	Education Qualifications	B.E.(Information Technology), M.E.(Computer Science Information Technology)							
5	Work Experience	Teaching	9 Yrs	Research	---	Industry	---	others	---
6	Area of Specialization	Operating system, Cloud Computing							
7	Courses (Subjects) taught	Under Graduate	Operating System, Cloud Computing, Software Engineering, Database Management System, C-Programming, Web Technology, Computer Architecture and organization -Ecommerce, Digital signal processing, Electronic Devices and Circuits						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	------	International Journals	02	Conferences	01	
		Masters Degree	----						
		Ph.D.	----						
9	Projects Carried out	06							
10	Patents	Filled	----	Granted	----				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	03	Conferences	05		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Book Published: 01 Name of the Book: Cloud Computing Publisher: Clever Fox Publishing ISBN No: 9789367070338 Year of Publication: 2025							

1	Name	**Mr. Ritesh Vilasrao Deshmukh – Assistant Professor**							
2	Date of Birth	23/12/1984							
3	Unique ID	1-2667909839							
4	Education Qualifications	B. E. (IT), M.E.(IT), PhD pursuing							
5	Work Experience	Teaching	12Yrs	Research		Industry	02 Yrs.	others	
6	Area of Specialization	Database System ,Cloud Computing, Machine Learning							
7	Courses (Subjects) taught	Under Graduate	Database Management Systems, Discrete Mathematics, Data Communication, Software Engineering, Data Communication & Networking, Cloud System and infrastructure,Cyber law and Ethics, Theory of Computing						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	02	Conferences	-Nil-	
		Masters Degree	---						
		Ph.D.	---						
9	Projects Carried out	UG:14							
10	Patents	Filled	---	Granted	---				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	---	International Journal	09	Conferences	01		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Title: “Data Base Management System with No SQL” ISBN-13:978-613-9-95904-4 Publishing house: LAP LAMBERT Academic Publishing , Number of pages: 188, Published on: 2018-11-29							

1	Name	**Mrs. Tejaswini Abhishek Puranik – Assistant Professor**							
2	Date of Birth	18-04-1994							
3	Unique ID	1-44731115839							
4	Education Qualifications	Ph.D.(Pursuing),M.Tech (CSE),B.E.(CSE)							
5	Work Experience	Teaching	05	Research	00	Industry	0.6	others	
6	Area of Specialization	Machine Learning, Artificial Intelligence							
7	Courses (Subjects) taught	Under Graduate	Discrete Structures and Graph Theory, Introduction to Web Technology, Data Communication and Networking, Data Communication, Web Engineering, Computing Resources Management, Cyber Security, E-Commerce, Data Structures 1,Natural Language Processing, Neural Network and Applications, Data Structures using C, Software Engineering						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	----	Conferences	-Nil-	
		Masters Degree	---						
		Ph.D.	---						
9	Projects Carried out	08							
10	Patents	Filled	--	Granted	---				
11	Technology Transfer	Worked as a resource person during the workshop on “Basics of HTML”							
12	Research Publications (No. of papers published in)	National	--	International Journal	04	Conferences	06		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	-------							

1	Name	**Ms. Vaishnavi Sunil Kanherkar – Assistant Professor**							
2	Date of Birth	25-08-2000							
3	Unique ID	1-45954925629							
4	Education Qualifications	B.E(IT) , M.E(CE)							
5	Work Experience	Teaching	01	Research	---	Industry	---	others	---
6	Area of Specialization	Machine Learning							
7	Courses (Subjects) taught	Under Graduate	C, C++, Ecommerce						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	----	Conferences	-Nil-	
		Masters Degree	---						
		Ph.D.	---						
9	Projects Carried out	----							
10	Patents	Filled	---	Granted	---				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	----	International Journal	01	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)								

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. R. S. Mahamune – Assistant Professor</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">14/09/1982</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-43368580500</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">M.Tech (VLSI), PhD</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>9</td><td>Research</td><td>00</td><td>Industry</td><td>00</td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Biomedical Signal Processing & Machine Learning, Bioinformatics</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Electromagnetic Fields, Signals and Systems, Microprocessor, Microcontroller, Embedded Systems, Computer Networks, Sensors and Transducers, etc.</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Modelling of Digital Systems (Verilog, VHDL), Embedded Systems</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--Nil-</td><td>International Journal</td><td>Nil</td><td>Conferences</td><td>-Nil-</td><td></td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--Nil-</td><td>Ongoing</td><td colspan="4">--Nil--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--Nil-</td><td>Ongoing</td><td colspan="4">--Nil--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9"></td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td colspan="2">2</td><td>Granted</td><td colspan="4">--Nil--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>--Nil--</td><td>International Journal</td><td>06</td><td>Conferences</td><td colspan="3">08</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">5. Part of the book series: Lecture Notes in Electrical Engineering ((LNEE,volume 1237)). Mahamune, R., Laskar, S.H., Dharmale, N. (2025). The Multi-scale Wavelet Approach to Remove the Eyeblink Artifacts from EEG Signals. In: Stroe, DI., Nasimuddin, D., Laskar, S.H., Pandey, S.K. (eds) Emerging Electronics and Automation. E2A 2023. Lecture Notes in Electrical Engineering, vol 1237. Springer, Singapore. https://doi.org/10.1007/978-981-97-6802-8_27.
Online ISBN: 978-981-97-6802-8. eBook Packages</td></tr>
</table>

1	Name	**Ms. N. S. Dharmale – Assistant Professor**							
2	Date of Birth	17/09/87							
3	Unique ID	1-43370818348							
4	Education Qualifications	PhD (Material Characterization and Device simulation)							
5	Work Experience	Teaching	8	Research	4	Industry	-	others	-
6	Area of Specialization	Material Characterization and Device simulation, Digital Image and Signal Processing,							
7	Courses (Subjects) taught	Under Graduate	DIVP, MTT, EDC, DSD, II, FRS, DSP, CA, SS						
		Post Graduate	CCN						
8	Research guidance (Number of Students)	No. of Papers Published in	National	02	International Journals	01	Conferences	05	
		Masters Degree	Completed	02	Ongoing	--Nil--			
		Ph.D.	--Nil--	--Nil--	Ongoing	--Nil--			
9	Projects Carried out	09(UG)+02(PG)							
10	Patents	Filled	--Nil--	Granted	02				
11	Technology Transfer	--Nil--							
12	Research Publications (No. of papers published in)	National	--Nil--	International Journal	14	Conferences	12		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--Nil--							

➢ Department of Information Technology

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Sagar Damodar Padiya (Associate Professor)</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">07/06/1987</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-2376823465</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">M. Tech (IT), Ph.D. (CSE)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>15</td><td>Research</td><td>04</td><td>Industry</td><td>-</td><td>others</td><td>-</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Wireless Network, Network Protocol Design</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Computer Programming (C, C++)
Discrete Structure & Graph Theory
Communication Engineering
E-Commerce
Web- Commerce
Real Time embedded System</td></tr>
<tr><td>Post Graduate</td><td colspan="8">-</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research Guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>10</td><td>Conferences</td><td>04</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">Design and Development of Featherweight Eddystone Protocol for BLE Beacon</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>01</td><td>Granted</td><td colspan="5">NIL</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>--</td><td>International Journal</td><td>11</td><td>Conferences</td><td colspan="3">04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">1. Chapter 10: “Analysis of Bluetooth Versions (4.0, 4.2, 5, 5.1, and 5.2) for IoT Applications” (pages 153-178) in the Implementing Data Analytics and Architectures for Next-Generation Wireless Communications, IGI Global, August 2021.
2. International Book "IoT with BLE Beacons: Research Opportunities, Planning and Strategy", LAP LAMBERT Academic Publishing on April 2021 in four languages, English Edition ISBN: 978-6203848113, Portuguese Edition ISBN: 978-6203777185, Italian Edition ISBN: 978-6203777246 and German Edition ISBN: 978-6203777215.</td></tr>
</table>

1	Name	**Ms. Priti Vasant Kale** (Assistant Professor)							
2	Date of Birth	02/04/1983							
3	Unique ID	1-426009657							
4	Education Qualifications	M. Tech (IT) Ph.D. (CSE) Pursuing							
5	Work Experience	Teaching	20	Research	--	Industry	--	others	--
6	Area of Specialization	Machine Learning, Medical Image Processing, and Healthcare AI							
7	Courses (Subjects) taught	Under Graduate	Object Oriented Programming, Operating Systems, Computer Networks, Distributed Database Management Systems						
		Post Graduate	Advanced Computer Networks						
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	01	Conferences	00	
		Masters Degree	Completed	01	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	--							
10	Patents	Filled	01	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	05	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	1. "AI Driven Healthcare", Vinsa Publication, ISBN: 978-81-978305-0-1, 2025							

1	Name	**Mr. Anand Gajanan Sharma** (Assistant Professor)							
2	Date of Birth	04/11/1988							
3	Unique ID	1-7428356968							
4	Education Qualifications	M. Tech (IT), Ph.D. (CSE) (Pursuing)							
5	Work Experience	Teaching	12	Research	--	Industry	--	others	--
6	Area of Specialization	Data Science, Machine Learning, NLP							
7	Courses (Subjects) taught	Under Graduate	Data Science and Statistics, Design and Analysis of Algorithms, Cyber Law and Ethics, Artificial Intelligence, Data Structures and Algorithm, Object Oriented Programming.						
		Post Graduate	--						
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	07	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	--							
10	Patents	Filled	02	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	14	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	1. "AI-Driven Healthcare Transforming Diagnosis for Effective Treatment", ISBN: 9788197 830501)							

1	Name	**M. Faizan I. Khandwani** (Assistant Professor)							
2	Date of Birth	07/08/1991							
3	Unique ID	1-7392849073							
4	Education Qualifications	M.E. (CSE)							
5	Work Experience	Teaching	13	Research	--	Industry	--	others	--
6	Area of Specialization	Database Management, Data Mining							
7	Courses (Subjects) taught	Under Graduate	Big Data Analytics, Database Management Systems Computer Organization and Architecture, Analog and Digital Electronics, Distributed Database Management Systems, Data Warehousing and Data Mining, Electronic Devices and Circuits, Communication Engineering, Visual C++, Object-Oriented Programming, Unified Modeling Language						
		Post Graduate	-						
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	--							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	--	International Journal	20	Conferences	01		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Mr. Sumit Sanjay Muddalkar (Assistant Professor)</td><td rowspan="3">  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">21/08/1989</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-7419115038</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">M. Tech (CSE), Ph.D. (CSE) (Pursuing)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>09</td><td>Research</td><td>--</td><td>Industry</td><td>3.9</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Machine Learning, Cloud Computing, Java Full Stack</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Machine Learning,
Artificial intelligence,
Computer Programming,
Discrete Structure,
Operating System,
Theory of Computation,
Entrepreneurship & Project Management,
Web Technology,
Introduction to Computer Networks,
Ecommerce,
Unix Laboratory,
HTML Laboratory,
Content Management System Using Joomla Laboratory</td></tr>
<tr><td>Post Graduate</td><td colspan="8">-</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research Guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>10</td><td>Conferences</td><td>--</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">Android Application for Auto-Lounge, Pune & Web Application for Beyond Temptations, Pune</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td colspan="2">Granted</td><td colspan="4">-</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">Hands-on workshop on Talend ETL Tool for students.</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>--</td><td>International Journal</td><td>--</td><td>Conferences</td><td colspan="4">01</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">--</td></tr>
</table>

1	Name	**Ms. Pallavi Bute** (Assistant Professor)							
2	Date of Birth	20/05/1988							
3	Unique ID	1-3359301266							
4	Education Qualifications	M.E. (CSE)							
5	Work Experience	Teaching	10	Research	--	Industry	--	others	--
6	Area of Specialization	Computer Science & Engineering							
7	Courses (Subjects) taught	Under Graduate	ALP, ICN, NAS, DAA, OS, BCF, VAR, CYL						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	02	Conferences		
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out								
10	Patents	Filled	--	Granted	--				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	--	Conferences	03		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	1. "Network Essentials" REST Publisher, India, ISBN NO : 978-81-985636-3-7, 2025							

1	Name	**Mrs. Shaila Nilesh Khandare** (Assistant Professor)							
2	Date of Birth	26/06/1989							
3	Unique ID	1-38081534711							
4	Education Qualifications	M.E. (C.E.)							
5	Work Experience	Teaching	10	Research	--	Industry	--	others	--
6	Area of Specialization	Data Science, Data analytics and Big Data Computing and Applied Artificial Intelligence							
7	Courses (Subjects) taught	Under Graduate	Data Structures, Design and Analysis of Algorithm, Operating Systems, Data Base Management System, Compiler Design, Computer Network, Artificial Intelligence, Dev Ops, IoT (Raspberry-Pie), Angular 20, Mobile Computing						
		Post Graduate	Distributed Operating Systems, Algorithmic						
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	04	Conferences	01	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	---							
10	Patents	Filled	02	Granted	01				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	06		Conferences	02	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Ms. Nayana Nanarao Ghuikar** (Assistant Professor)							
2	Date of Birth	13/11/1997							
3	Unique ID	1-43607114615							
4	Education Qualifications	M.E. (CSE)							
5	Work Experience	Teaching	06	Research	--	Industry	0.6 Months	others	--
6	Area of Specialization	Programming and Software Testing							
7	Courses (Subjects) Taught	Under Graduate	Computer Programming, Data Structure, Software Engineering, Python Programming, Web Technology, Java Programming, Software Testing						
		Post Graduate	---						
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	---							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	06		Conferences	--	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

1	Name	**Mr. Pranav Arun Lod** (Assistant Professor)							
2	Date of Birth	25/02/2025							
3	Unique ID	1-46984032273							
4	Education Qualifications	M.E. (Computer Engineering)							
5	Work Experience	Teaching	0.5	Research	--	Industry	05	others	--
6	Area of Specialization	Business Intelligence , SQL, Azure , Azure Data Factory							
7	Courses (Subjects) Taught	Under Graduate	Business Intelligence, Information Security System, Professional Ethics and Management						
		Post Graduate							
8	Research Guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	--							
10	Patents	Filled	--	Granted	--				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National	--	International Journal	--		Conferences	--	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

➢ Department of Applied Sciences & Humanities (First Year)

1	Name	**Dr. Anand Shriram Tale – HOD & Associate Professor**							
2	Date of Birth	21/12/1985							
3	Unique ID	1-7398363804							
4	Education Qualifications	M.Sc,Ph.D							
5	Work Experience	Teaching	16	Research	--	Industry	--	others	--
6	Area of Specialization	Solid State Physics							
7	Courses (Subjects) taught	Under Graduate	Engineering Physics						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out								
10	Patents	Filled	01	Granted	--				
11	Technology Transfer								
12	Research Publications (No. of papers published in)	National		International Journal	14	Conferences	09		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	1. A textbook of "Nano science & Technology" (RK Publications, ISBN 978-81-971218-5-2, Year 2024). 2. A textbook of "Applied Physics" (BR Publications, ISBN 978-93-47231-00-1, Year 2025). --							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Anil VasantraoPatil – Associate Professor & Registrar</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">09/09/1968</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-425739421</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M.Sc. M. Phil, P.G.D.I.P.M</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>33</td><td>Research</td><td>2</td><td>Industry</td><td>1</td><td>others</td><td>0</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Organic Chemistry, Environmental Chemistry</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Engineering Chemistry, Applied Chemistry, Environmental Science</td></tr>
<tr><td>Post Graduate</td><td colspan="7">---</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>---</td><td>International Journals</td><td>---</td><td>Conferences</td><td>---</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>---</td><td>Ongoing</td><td colspan="3">---</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>---</td><td>Ongoing</td><td colspan="3">---</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">---</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>---</td><td>Granted</td><td colspan="5">---</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">---</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>1</td><td>International Journal</td><td>---</td><td colspan="3">Conferences</td><td>02</td></tr>
<tr><td>13</td><td>No. of Books published with details</td><td colspan="8">---</td></tr>
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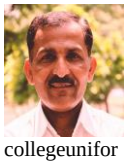
collegeunifor

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Rajesh Mahadeorrao Kharate – Associate Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">24/08/1973</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-425753859</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D. B.Ed. M.Sc.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>27</td><td>Research</td><td>12</td><td>Industry</td><td>Nil</td><td>others</td><td>BOS</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Synthetic Organic Chemistry ,Polymers ,Environmental Chemistry</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">02(Engineering Chemistry and Environmental Science)</td></tr>
<tr><td>Post Graduate</td><td colspan="7">00</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>04</td><td>International Journals</td><td>08</td><td>Conferences</td><td>04</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>Nil</td><td>Ongoing</td><td colspan="3">Nil</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>00</td><td>Ongoing</td><td colspan="3">00</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Nil</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>Nil</td><td>Granted</td><td colspan="5">Nil</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>04</td><td>International Journal</td><td>08</td><td>Conferences</td><td>04</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">A Text Book of Engineering Chemistry Dnyanpath Publication, Amravati First Edition: 14th Feb.2016</td></tr>
</table>

1	Name	**Mr. Prashant Ishwar Chaure – Associate Professor**							
2	Date of Birth	17th October 1973							
3	Unique ID	1-425521425							
4	Education Qualifications	B.E.(Civil), M.E.							
5	Work Experience	Teaching	17	Research	--	Industry	25	others	--
6	Area of Specialization	Structural Designing, Waste water Treatment, Environmental Engineering							
7	Courses (Subjects) taught	Under Graduate	Mechanics, Engineering Drawing, Fluid Mechanics						
		Post Graduate	Nil						
8	Research guidance (Number of Students)	No. of Papers Published in	National	--	International Journals	--	Conferences	--	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Completed	--	Ongoing	--			
9	Projects Carried out	Smart stick for blind people.							
10	Patents	Filled	1	Granted	Nil				
11	Technology Transfer	Nil							
12	Research Publications (No. of papers published in)	National	4	International Journal	2		Conferences	Nil	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Nil							

1	Name	**N.S.Thakare – Assistant Professor** 							
2	Date of Birth	14/10/1971							
3	Unique ID	1-425753709							
4	Education Qualifications	M.Sc,B.Ed,M.Phil.Ph.d.(Regd.)							
5	Work Experience	Teaching	25	Research	--	Industry	--	others	--
6	Area of Specialization	Integral Transform							
7	Courses (Subjects) taught	Under Graduate	M-I,M-II,M-III,M-IV,NMOT,ORM,OT,NMORT,NSM						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	01	International Journals	01	Conferences	02	
		Masters Degree	Completed	--	Ongoing	--			
		Ph.D.	Registered	--	Ongoing	--			
9	Projects Carried out	In NSS.							
10	Patents	Filled	Nil	Granted	Nil				
11	Technology Transfer	Rural Lifestyle Improvement/Development Related.							
12	Research Publications (No. of papers published in)	National	01	International Journal	01	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Nil --							

1	Name	**Anil Shamrao Alane – Assistant Professor** 							
2	Date of Birth	19/07/1979							
3	Unique ID	1-425753767							
4	Education Qualifications	M.Sc. B.Ed.Ph.D (Pursuing)							
5	Work Experience	Teaching	17	Research	02	Industry	--	others	0
6	Area of Specialization	Organic Chemistry, Environmental Chemistry							
7	Courses (Subjects) taught	Under Graduate	Engineering Chemistry, Environmental Studies						
		Post Graduate	---						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	---	Conferences	--	
		Masters Degree	Completed	---	Ongoing	---			
		Ph.D.	Completed	---	Ongoing	---			
9	Projects Carried out	---							
10	Patents	Filled	---	Granted	---				
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	--	International Journal	---	Conferences		02	
13	No. of Books published with details	---							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Prof.Harshali S.Patil – Assistant Professor
  </td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">09-09-1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-426010049</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">B.Sc, B.Ed, M.A.(English Lit), M.B.A., P.G. Diploma in English Communication Skills, PGCTE (Hyderabad), PH.D.(registered)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>14 Yrs</td><td>Research</td><td>--</td><td>Industry</td><td>02 Yrs.</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">English Communication Skills, H. R.,Marketing</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Communication Skills, IMQC, ITK, Soft Skills training & Spoken English workshop</td></tr>
<tr><td>Post Graduate</td><td colspan="7">----</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>--</td><td>Conferences</td><td>--</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3"></td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3"></td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">--------</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">---------</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>4</td><td>International Journal</td><td>1</td><td colspan="3">Conferences</td><td>2</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">(Book Published-01 , name: Literature meet Libraries, Prime publishing house, ISBN : 978-93-7344-139-9,Oct.2025)</td></tr>
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1	Name	**Kanchan Prakash Deshmukh – Assistant Professor** 							
2	Date of Birth	19/09/1985							
3	Unique ID	1-698260803							
4	Education Qualifications	M.Sc. B.Ed.							
5	Work Experience	Teaching	15	Research	--	Industry	--	others	--
6	Area of Specialization	Pure Mathematics , Applied Mathematics							
7	Courses (Subjects) taught	Under Graduate	Engineering Mathematics-I,II,III Numerical methods and Operation Research Techniques						
		Post Graduate	---						
8	Research guidance (Number of Students)	No. of Papers Published in	National	---	International Journals	---	Conferences	---	
		Masters Degree	Completed	---	Ongoing	---			
		Ph.D.	Completed	---	Ongoing	---			
9	Projects Carried out	---							
10	Patents	---	---		Granted	---			
11	Technology Transfer	---							
12	Research Publications (No. of papers published in)	National	01	International Journal	---	Conferences	05		
13	No. of Books published with details	---							

1	Name	**Bhagat Sachin Vasant – Assistant Professor** 							
2	Date of Birth	30/0/1985							
3	Unique ID	1-2188734219							
4	Education Qualifications	M.A., M. Phil, UGC-NET, Diploma in Mass Media, Ph.D. (Thesis Submitted)							
5	Work Experience	Teaching	12 Yrs.	Research	05	Industry	-	others	-
6	Area of Specialization	English Language, Communication Skills, Soft Skills, IKS							
7	Courses (Subjects) taught	Under Graduate	Communication Skills, Social Science and Engineering Economics, Professional Ethics, ITK						
		Post Graduate	-						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	-	Conferences	-	
		Masters Degree	Completed	-	Ongoing	-			
		Ph.D.	Completed	-	Ongoing	-			
9	Projects Carried out	-							
10	Patents	Filled	-	Granted	-				
11	Technology Transfer	-							
12	Research Publications (No. of papers published in)	National	--	International Journal	--	Conferences	02		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	-							

1	Name	**Rutika Raut -– Assistant Professor** 							
2	Date of Birth	05/03/1996							
3	Unique ID	1-4459733609							
4	Education Qualifications	M.Sc Physics							
5	Work Experience	Teaching	8 Yrs.	Research	--	Industry	--	others	05 Yrs.
6	Area of Specialization	None							
7	Courses (Subjects) taught	Under Graduate	Engineering Physics (1A2)						
		Post Graduate	--						
8	Research guidance (Number of Students)	No. of Papers Published in	National	Nill	International Journals	Nill	Conferences	Education 2.0 New Horizons in T&L organised by BITS Pilani Goa Campus (Participated)	
		Masters Degree	Completed	Nill	Ongoing			Nill	
		Ph.D.	Completed	Nill	Ongoing			Nill	
9	Projects Carried out	Fabrication of Power Supplies & Synthesis of Luminesce Sample							
10	Patents	Filled	Nill	Granted		Nill			
11	Technology Transfer	--							
12	Research Publications (No. of papers published in)	National	Nill	International Journal	Nill	Conferences		Nill	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Nill							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Mrs. Manisha Sandeep Pande – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">03/10/1982</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-3599702359</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">M.Sc. (Physics), B.Ed., Ph.D.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>17</td><td>Research</td><td>05</td><td>Industry</td><td>--</td><td>others</td><td>1</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Solid State Physics, X-Rays, Nanomaterial based gas Sensor.</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Engineering Physics</td></tr>
<tr><td>Post Graduate</td><td colspan="7">--</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>--</td><td>Conferences</td><td>--</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8"></td></tr>
<tr><td>10</td><td>Patents</td><td>--</td><td>--</td><td>Granted</td><td colspan="5">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8"></td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>02</td><td>International Journal</td><td>10</td><td>Conferences</td><td colspan="3">09</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">1. A text book of “Research Innovative Basket” (National Education Policy 2020 ,ISBN:978-81-970810-4-0),23 March 2024.
2. “Scientific,Cultural & Philosophical Dimensions of Indian Knowledge System : Part-1”,ISBN:978-81-981569-1-4,14 Jan.2026.</td></tr>
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<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Mrs. Jayashree Shekhar Gawande – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">28/05/1986</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-44497976574</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D. (Mathematics), M.Sc.(Mathematics), B.Sc.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>11 Yrs.</td><td>Research</td><td>--</td><td>Industry</td><td>--</td><td>others</td><td>---</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Differential Equations, Numerical Methods, Partial Differential Equations, Integral Transforms.</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Engineering Mathematics I, II, III, VI</td></tr>
<tr><td>Post Graduate</td><td colspan="7">--</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td></td><td>Conferences</td><td></td></tr>
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<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="3">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">----</td></tr>
<tr><td>10</td><td>Patents</td><td>---</td><td>---</td><td colspan="2">Granted</td><td colspan="3">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>04</td><td>International Journal</td><td>03</td><td>Conferences</td><td colspan="2">06</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">Nil</td></tr>
</table>

1	Name	**Gunwant Laxman Bayaskar – Associate Professor** 							
2	Date of Birth	28/09/1967							
3	Unique ID	1-421854919							
4	Education Qualifications	M.A. Mped							
5	Work Experience	Teaching	30	Research	-	Industry	-	others	-
6	Area of Specialization	Coaching in Basket Ball / Hand Ball							
7	Courses (Subjects) taught	Under Graduate	All Indoor &Out door sports.						
		Post Graduate	All Indoor &Out door sports.						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	-	Conferences	-	
		Masters Degree	Completed	-	Ongoing	-			
		Ph.D.	Completed	-	Ongoing	-			
9	Projects Carried out	-							
10	Patents	Filled	-	Granted	-				
11	Technology Transfer	-							
12	Research Publications (No. of papers published in)	National	-	International Journal	-	Conferences	-		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	-							

➢ Department of M.B.A.

<table>
<tr><td>1</td><td>Name</td><td colspan="9">Dr. Pavan Mahadeo Kuchar –Assistant Professor and HOD</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="9">27-12-1983</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="9">1-423118306</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="9">Dip. Digital Fabrication, B. Pharmacy, MBA, UGC-NET, Ph.D.</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>16.5</td><td>Research</td><td>14.5</td><td>Industry</td><td>10 Mon.</td><td>others</td><td>--</td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="9">Marketing, Strategic Management</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="8">Industrial Management and Quality Control (IMQC); BE-EXTC-Sem 4,
Principles of Management (POM); BE-IT-Sem 6
Principles of e-Marketing for Engineering (PEME) BE-Open Elective-Sem 5;
Healthcare and Hospitality Management (HCHM) – BBA Sem 3
Entrepreneurship Development – BE (EXTC) – 3ET207EM</td></tr>
<tr><td>Post Graduate</td><td colspan="8">Principles and Practices of Management - MBA/101
Computer Applications in Management – MBA/107
Logistics Management – MBA/207
Entrepreneurship – MBA-207
Indian Financial System – MBA3101/F
International Marketing Strategy – MBA/3201/M
Sales and Distribution Management – MBA/3202/M
Brand Management – MBA/3205/M
Performance Management – MBA/3306/H
Strategic Management – MBA/401
Sales Promotion Management – MBA/4201/SM
Marketing of Services – MBA/4202/SM
Marketing of Social Services – MBA/4206/M
International HRM – MBA/4306/OB
Digital Marketing – MBA/4201/SM</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>--</td><td>International Journals</td><td>15</td><td>Conferences</td><td>06</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>96</td><td>Ongoing</td><td colspan="4">08</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>--</td><td>Ongoing</td><td colspan="4">--</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="9">--</td></tr>
<tr><td>10</td><td>Patents</td><td>Filed</td><td>01</td><td colspan="4">Granted</td><td colspan="3">--</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="9">--</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>05</td><td>International Journal</td><td>09</td><td colspan="3">Conferences</td><td>18</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="9">Managerial Innovation- Bloomsbury Publications, 2022, ISBN: 978-93-93715-21-0</td></tr>
</table>

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Laxmikant Baburao Deshmukh – Associate Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">31/08/1976</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-425658663</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">BE (Mech), MBA(Marketing), PhD(Business Management) V-SAT (Software Dip)</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>18 yrs</td><td>Research</td><td>12 Yrs</td><td>Industry</td><td>3 Yrs</td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">Marketing Management and Production Operations Management</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">B.E.(EXTC), B. Sc (Mechanical Engineering) & BBA
Business Statistics, Sales and Distribution Management, Marketing Management, Instrumentation, Industrial Management and Quality Control etc.</td></tr>
<tr><td>Post Graduate</td><td colspan="7">MM, QM, POM, ME, MIS, RM, LM, SDM, AM, CB,RTM, IMQC, TM, HBAW, HRD</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published in</td><td>National</td><td>11</td><td>International Journals</td><td>03</td><td>Conferences</td><td>10</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>163</td><td>Ongoing</td><td colspan="3">08</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>Nil</td><td>Ongoing</td><td colspan="3">04</td></tr>
<tr><td>9</td><td>Projects Carried out</td><td colspan="8">Nil</td></tr>
<tr><td>10</td><td>Patents</td><td>Filled</td><td>Nil</td><td colspan="4">Granted</td><td>Nil</td></tr>
<tr><td>11</td><td>Technology Transfer</td><td colspan="8">Nil</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>11</td><td>International Journal</td><td>3</td><td colspan="2">Conferences</td><td>10</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">Innovation Management; In Pipe line by Bloomsbury Publishing House
Will be published by Apr 2022</td></tr>
</table>

1	Name	**Dr. Mayur Anil Dande – Assistant Professor**							
2	Date of Birth	22 August 1984							
3	Unique ID	1-423118310							
4	Education Qualifications	B.Sc., M. A., M.B.A., UGC- NET, Ph.D.							
5	Work Experience	Teaching	16.5	Research	13	Industry	0	others	0
6	Area of Specialization	Marketing							
7	Courses (Subjects) taught	Under Graduate	Marketing Management, Values and Ethics, Professional Ethics and Management, Project Management and Entrepreneurship						
		Post Graduate	Business Ethics, Indian Ethos and Values, Marketing Management, Consumer Behaviour, Brand Management, Advertising Management, Retail Management, Sales Promotion, Strategic Marketing for Non profit Organizations and Social Services, Management of financial services, Team Dynamics at Work, Performance Management, International Human Resource Management						
8	Research guidance (Number of Students)	No. of Papers Published in	National	00	International Journals	00	Conferences	04	
		Masters Degree	Completed	102	Ongoing	08			
		Ph.D.	Completed	00	Ongoing	00			
9	Projects Carried out	102 projects related to areas like Consumer Behavior, Brand Management, Advertising, Marketing, Financial Services etc.							
10	Patents	Filled	01	Granted	00				
11	Technology Transfer	Nil							
12	Research Publications (No. of papers published in)	National	01	International Journal	28	Conferences	24		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	01- Digital Innovation- Bloomsbury Publications, 2022, ISBN: 978-93-93715-14-2							

1	Name	**Dr. Satya Mohan Mishra – Assistant Professor** 							
2	Date of Birth	01/03/1985							
3	Unique ID	1-424042464							
4	Education Qualifications	Ph.D.; MBA(PGDM)							
5	Work Experience	Teaching	16	Research	14	Industry	0	Others	0
6	Area of Specialization	Economics, Finance, Banking & Human Resource							
7	Courses (Subjects) taught	Under Graduate	Principles of Economics; Management Accounting; Indian Economy; Engineering Economics; Social Science & Engineering Economics						
		Post Graduate	Managerial Economics; Financial Management/Corporate Finance; Management Science; International Financial Management; Banking System/ Managing Banks & Financial Institutions; Investment Science; Compensation Management; Investment Analysis & Portfolio Management; Financial Derivatives						
8	Research guidance (Number of Students)	No. of Papers Published in	National	4	International Journals	12	Conferences	4	
		Masters Degree	Completed	95	Ongoing	10			
		Ph.D.	Completed	NA	Ongoing	NA			
9	Projects Carried out	Nil							
10	Patents	Filled	01	Granted				Nil	
11	Technology Transfer	Nil							
12	Research Publications (No. of papers published in)	National	6	International Journal	18	Conferences		06	
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	Nil							

1	Name	**Dr. Wechansing Zyamsing Suliya – Assistant Professor** 							
2	Date of Birth	15-03-1986							
3	Unique ID	1-1435368830							
4	Education Qualifications	B.sc (Microbiology),MBA(Human Resource Management and Production Management)							
5	Work Experience	Teaching	14	Research	14	Industry	1	others	-
6	Area of Specialization	Human Resource Management and Production Management							
7	Courses (Subjects) taught	Under Graduate	_ Microbiology, Chemistry and Environmental Science						
		Post Graduate	Organizational Behavior and Effectiveness, Human Resource Management, Management of Industrial relations, Human Relations and Legal framework, International Marketing, Rural Marketing, Knowledge Management, Corporate Leadership Management.						
8	Research guidance (Number of Students)	No. of Papers Published in	National	-	International Journals	11	Conferences	1	
		Masters Degree	Completed	131	Ongoing	9			
		Ph.D.	Completed	1		-			
9	Projects Carried out	1. Designing and fabrication of compressed concrete Bricks making machine (Working on Mechanical Engineering Project)-Project completed testing is in process. 2. Designing automated poultry chick hatching machine (Working on Electronics Engineering Project) -Project completed testing is in process.							
10	Patents	Filled	in Process	Granted	-				
11	Technology Transfer	-							
12	Research Publications (No. of papers published in)	National	1	International Journal	21	Conferences	4		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication,etc.)	-							

1	Name	**VISHAL VINAYAKRAO PATIL – Assistant Professor** 							
2	Date of Birth	02-01-1980							
3	Unique ID	1-7393461729							
4	Education Qualifications	LL.B, M.B.A.(FIN), DSK&PD, M.A.(Eco), Ph.D. (Pursuing)							
5	Work Experience	Teaching	12	Research	2	Industry	0	others	4
6	Area of Specialization	Law, Finance and Human Resource Management							
7	Courses (Subjects) taught	Under Graduate	Business Legislation, Company Account, Advance Account						
		Post Graduate	• Business Law• Management Science •Accounting of Mangers • Organizational Development & Intervention Strategies • Management of Financial Services • Financial Decision Analysis • Human Resources Development • Risk Management • Working Capital Management						
8	Research guidance (Number of Students)	No. of Papers Published in	National	4	International Journals	16	Conferences	4	
		Masters Degree	Completed	50+	Ongoing	8			
		Ph.D.	Completed	NA	Ongoing	NA			
9	Projects Carried out	-NIL-							
10	Patents	Filled	2	Granted	-NIL-				
11	Technology Transfer	-NIL-							
12	Research Publications (No. of papers published in)	National	6	International Journal	20	Conferences	4		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	1. Human Resource Management (Sai Jyoti Publication) ISBN No. 978-93-81432-41-9 2. FINANCIAL ACCOUNTING AND CORPORATE GOVERNANCE (Book Rivers) ISBN: 978-93-6884-649-9							

<table>
<tr><td>1</td><td>Name</td><td colspan="8">Dr. Bilal T. Husain – Assistant Professor</td></tr>
<tr><td>2</td><td>Date of Birth</td><td colspan="8">31-10-1992</td></tr>
<tr><td>3</td><td>Unique ID</td><td colspan="8">1-9312315964</td></tr>
<tr><td>4</td><td>Education Qualifications</td><td colspan="8">Ph.D (Business Management, Commerce and Management);
UGC-NET (Management);
Masters of Management Studies in Finance (NLDIMSR, Mumbai),
B.Tech –ExTC (Government College of Engineering, Amravati).</td></tr>
<tr><td>5</td><td>Work Experience</td><td>Teaching</td><td>6</td><td>Research</td><td>3</td><td>Industry</td><td>2</td><td>others</td><td></td></tr>
<tr><td>6</td><td>Area of Specialization</td><td colspan="8">FINANCE</td></tr>
<tr><td rowspan="2">7</td><td rowspan="2">Courses (Subjects) taught</td><td>Under Graduate</td><td colspan="7">Courses Taught to Engineering Students:
Principles of Management,(IT Branch)
Project Management and Entrepreneurship, (ExTC Branch)
Principles of Economics and Management (Mechanical Branch)</td></tr>
<tr><td>Post Graduate</td><td colspan="7">First year MBA courses:
Principles and practices of Management,
Management of Information systems (Computer Application for Business), Research Methodology,
Second year MBA courses:
Financial Derivatives and Risk Management, Behavioural Finance, Security Analysis and Portfolio Management, Investment Science, Strategic Management, Financial Management.</td></tr>
<tr><td rowspan="3">8</td><td rowspan="3">Research guidance (Number of Students)</td><td>No. of Papers Published</td><td>National</td><td>2</td><td>International Journals</td><td>16</td><td>Conferences</td><td>3</td></tr>
<tr><td>Masters Degree</td><td>Completed</td><td>40</td><td>Ongoing</td><td colspan="3">9</td></tr>
<tr><td>Ph.D.</td><td>Completed</td><td>-</td><td>Ongoing</td><td colspan="3">-</td></tr>
<tr><td>9</td><td>Projects Carried</td><td colspan="8">01</td></tr>
<tr><td>10</td><td>Patents/Copyrights</td><td>Filled</td><td>-</td><td colspan="3">Granted</td><td colspan="2">Copyrights granted: 02</td></tr>
<tr><td>11</td><td>Technology Trans.</td><td colspan="8">-</td></tr>
<tr><td>12</td><td>Research Publications (No. of papers published in)</td><td>National</td><td>5 (2 Scopus Indexing)</td><td>International Journal</td><td>18 (1 ABDC indexing)</td><td colspan="2">Conferences</td><td>6</td></tr>
<tr><td>13</td><td>No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)</td><td colspan="8">03 Books.
1. Digital Innovation, Bloomsbury, ISBN 978-93-93715-14-2, 2022
2. Student Empowerment in Higher Education, Vol I: A2Z EduLearningHub, ISBN: 978-81-967798-2-5.
3. Foundational Hypothesis Testing: Z, T, ANOVA, Chi-Square, Self Published, ASIN : B0F517J8ST(kindle edition), B0F5BWH2DZ(paperback).</td></tr>
<tr><td></td><td>Awards</td><td colspan="8">Best Paper Award by IAASSE, Springer Conference(SCOPUS)</td></tr>
</table>

1	Name	**Adesh B. Solanke – Assistant Professor and TPO** 							
2	Date of Birth	01-02-1979							
3	Unique ID	1-43724200431							
4	Education Qualifications	B.E., M.Tech, Ph D (Pursuing)							
5	Work Experience	Teaching	16	Research	0	Industry	0	others	0
6	Area of Specialization	Human Resource of Development and Management (HRDM)							
7	Courses (Subjects) taught	Under Graduate	--						
		Post Graduate	Organization Behaviour, Human Resource Management, Research Methodology and Data Analysis						
8	Research guidance (Number of Students)	No. of Papers Published in	National	0	International Journals	2	Conferences	0	
		Masters Degree	Completed	Yes	Ongoing	0			
		Ph.D.	Completed	NA	Ongoing	NA			
9	Projects Carried out	-NIL-							
10	Patents	Filled	-NIL-	Granted	-NIL-				
11	Technology Transfer	-NIL-							
12	Research Publications (No. of papers published in)	National	02	International Journal	07	Conferences	0		
13	No. of Books published with details (Name of the book, Publisher with ISBN, year of publication, etc.)	--							

18.9 Fee

i. No. of Hostel and Annual Food Charges (Mess) Fees waivers granted with amount and name of students (2025-26)

Sn	Name of the Students	Year/Branch	Fee Wavers granted with Amount
Hostel Fees			
1	ABHISHEK MANMODE	3S	25,000/-
2	KU. AMBIKA PATIL	4N	25,000/-
3	KU. AYASHA JADHAV	4U2	25,000/-
4	KU. CHAITALI CHOPADE	MBA	25,000/-
5	DIPAK INGLE	4S	25,000/-
6	KU. DIVYA SHENDE	2S	25,000/-
7	KU. GAURI RAUT	3S	25,000/-
8	ISHWAR SHARMA	4N	25,000/-
9	KRISHNA KOLHEKAR	4R	25,000/-
10	PAWAN BHARAMBE	2U1	25,000/-
11	PAWAN LAKKAS	4R	25,000/-
12	KU. POOJA GAIKWAD	1S	25,000/-
13	KU. PRAGATI REKHATE	MBA	25,000/-
14	KU. SAKSHI RAJANKAR	2N	25,000/-
15	KU. SHRADDHA SURYAWANSHI	1N	25,000/-
16	KU. SHRUTI GHANOKAR	2S	25,000/-
17	TONI VIHIRKAR	4S	25,000/-
18	KU. TRUPTI ATKAR	2S	25,000/-
19	UTKARSHA PATIL	MBA	25,000/-
20	KU. MAYURI MAHALE	4U1	25,000/-
		Total A)	**5,00,000/-**
Mess Fees			
1	KU. ARTI BAGADE	2S	18,000/-
2	KU. AMBIKA PATIL	4N	18,000/-
3	ANIRUDDHA POTE	1M	18,000/-
4	ATHARVA BODADE	2N	18,000/-
5	KU. CHAITALI CHOPADE	MBA	18,000/-
6	KU. DIVYA SHENDE	2S	18,000/-
7	KARTIK HANDE	1R	18,000/-
8	KRISHNA KOLHEKAR	4R	18,000/-
9	MAYUR RAUT	3N	18,000/-
10	MAYUR WADALKAR	2S	18,000/-
11	PARTH DESHMUKH	2S	18,000/-
12	PAWAN LAKKAS	4R	18,000/-
13	KU. PAYAL DABERAO	1N	18,000/-
14	KU. PRAGATI REKHATE	MBA	18,000/-
15	KU. POOJA GAIKWAD	1S	18,000/-
16	RAKESH LANDE	2U2	18,000/-
17	ROHAN KHANDAR	2U2	18,000/-
18	RUSHIKESH BORKAR	2N	18,000/-
19	KU. SAKSHI RAJANKAR	2N	18,000/-
20	SHANKAR BONDRE	4S	18,000/-
21	KU. SHRADDHA SURYAWANSHI	1N	18,000/-
22	KU. SHRUTI GHANOKAR	2S	18,000/-
23	KU. TRUPTI ATKAR	2S	18,000/-
24	KU. UTKARSHA PATIL	MBA	18,000/-
		Total B)	**4,32,000/-**

Summary Report

Sn	Scheme	Total Number of Beneficiaries	Total Amount Sanctioned
1	Hostel Fees Waivers	20	5,00,000/-
2	Mess Fees Waivers	24	4,32,000/-
		Total	**9,32,000/-**

ii. Number of scholarship offered by the Institution, duration and amount

Summary of Scholarships and Awards (2024-25)

SN	Type of Scholarship		Number of Beneficiaries	Total Amount in Rs.
1	Merit Scholarship		191	3,48,769/-
2	Donor's Scholarship		142	2,37,332/-
3	Best Project Award (Sponsored)		52	13,500/-
4	Achievements		08	9,300/-
5	Hostel and Mess Waiver		44	9,32,000/-
6	Soial Media Cell		12	43,263/-
TOTAL		**In Fig.**	**449**	**15,84,146/-**
		In words	**Four Hundred Forty Nine**	**Fifteen Lakh Eighty Four ThousandOne Hundred Forty Six Only**

18.10 Admission

i. Number of seats sanctioned with the year of approval

Programmes	Level	Course	No. of Seats Approved 2025-26
Engineering Technology	Under Graduate	Computer Science And Engineering	120
		Electronics and Telecommunication Engineering	120
		Mechanical Engineering	60
		Electrical Engineering (Electronics and Power)	60
		Information Technology	60
	Post Graduate	Electrical Power System	18
		Digital Electronics	18
		Computer Engineering	18
		Advanced Manufacturing and Mechanical System Design	18
Management	Post Graduate	M.B.A.	60

ii. Number of Students admitted under various categories each year in the last three years

Year	Open	OBC	SC	ST	NT	DTVJ	SBC	SEBC	TFWS	EWS	PMSSS/ NEUT	Total
B.E. Computer Science and Engineering (UG)												
2023-24	04	42	06	03	03	01	01	00	03	04	02	69
2024-25	08	37	04	02	04	01	00	04	03	06	00	69
2025-26	05	68	12	07	11	04	02	11	06	12	00	138
B.E. Electronics and Telecommunication Engineering (UG)												
2023-24	20	69	13	03	11	02	02	00	06	12	01	139
2024-25	09	72	09	04	11	02	05	08	05	11	00	136
2025-26	01	78	13	04	08	03	01	12	06	11	00	137
B.E. Electrical Engineering (Electronics and Power) (UG)												
2023-24	06	41	07	01	04	00	01	00	03	05	00	68
2024-25	03	39	05	01	06	01	01	04	03	05	00	68
2025-26	04	34	06	02	06	01	02	05	03	06	00	69
B.E. Mechanical Engineering (UG)												
2023-24	08	36	08	01	04	01	02	00	03	04	00	67
2024-25	04	38	06	02	03	02	01	04	03	06	00	69
2025-26	01	42	07	01	04	01	00	04	03	06	00	69
B.E. Information Technology (UG)												
2023-24	11	35	05	02	04	02	01	00	03	06	01	70
2024-25	05	38	04	03	03	02	01	04	03	05	01	69
2025-26	03	33	06	04	06	02	00	06	02	06	00	68
M.E. Eletrical Power System (PG)												
2023-24	01	01	01	00	00	00	00	00	00	00	00	03
2024-25	01	03	00	01	00	00	00	00	00	00	00	05
2025-26	00	01	01	00	00	00	00	00	00	00	00	02
M.E. Digital Electronics (PG)												
2023-24	00	00	00	00	00	00	00	00	00	00	00	00
2024-25	02	00	00	00	00	00	00	00	00	00	00	02
2025-26	01	00	00	00	00	00	00	00	00	00	00	01
M.E. Computer Engineering (PG)												
2023-24	02	01	00	00	00	00	00	00	00	00	00	03
2024-25	01	03	01	01	00	00	01	00	00	00	00	07
2025-26	03	00	03	00	00	00	00	00	00	01	00	07
M.E. Advanced Manufacturing and Mechanical System Design												
2023-24	00	01	00	00	00	00	00	00	00	00	00	01
2024-25	00	01	01	00	00	00	00	00	00	00	00	02
2025-26	00	01	00	00	00	00	00	00	00	00	00	01
M.B.A.												
2023-24	11	34	08	01	04	02	00	00	03	06	00	69
2024-25	17	28	07	01	03	01	01	02	03	06	00	69
2025-26	11	33	09	00	05	00	00	02	03	06	00	69

iii. Number of applications received during last year for admission under Management Quota and number admitted

Year	Number of Application Received	Number of Students Admitted
B.E. Engineering		
2023-24	568	72
2024-25	642	72
2025-26	Management Quota Seats surrender for the Academic Year 2025-26.	
M.E. Engineering		
2023-24	02	02
2024-25	04	04
2025-26	Management Quota Seats surrender for the Academic Year 2025-26	
M.B.A.		
2023-24	61	12
2024-25	104	12
2025-26	Management Quota Seats surrender for the Academic Year 2025-26.	

18.11 Admission Procedure

i. Mention the admission test being followed, name and address of theTest Agency/State Admission Authorities and its URL (website)

Name of the Course	Name of Admission Test	Address of the Test Agency	Website
B.E. Engineering	MH CET Common Enterance Test	Common Entrance Test Cell, MS, Mumbai	www.mahacet.org
	JEE Main Enterance	National Testing Agency	www.nta.ac.in
M.E. Engineering	GATE Exam	GATE 2025 IISc, Bengaluru	https://gate2025.iisc.ac.in
M.B.A.	MH CET Common Enterance Test	Common Entrance Test Cell, MS, Mumbai	www.mahacet.org

ii. Number of seats allotted to different Test Qualified candidate separately (AIEEE//JEE/ CET (State conducted test/ University tests/ CMAT)/ Association conducted test etc.)

- Admissions to B.E./M.E./M.B.A Courses of the institute are done as per the norms and guidelines of State Comman Entarance Test Cell, Maharashtra State through Centralized Admission Process (CAP). 100% Seats are filled through Centralize Admission Process for the Academic Session 2025-26 as per the guidelines given by State Comman Entarance Test Cell, Maharashtra State, and Mumbai. Moreover Over and above PMSSS, Central Govt. Nominee, Tuition Fee Waiver Scheme Seats (TFWS - 5% of Sanctioned Intake per branch) & Economically Weaker Scheme (EWS – 10 % Sanctione Intake per branch) are available at our Institute.

iii. Calendar for admission against CAP quota seats

SN	Activity	Date & Timings	
		B.E./M.E.	M.B.A.
1	Last date of Submission of Application Form.	7th September 2025	12th September 2025
2	Display of Provisional Merit List on College Notice Board & Submission of Grievances if any	7th September 2025 at 5.00 pm	13th September 2025 at 11.00 a.m.
3	Display of Final Merit List on College Notice Board / College Website	7th September 2025 at 9.00 pm	
4	***Confirmation of Admission by Counseling Round.***	8th September 2025 at 11.00 am.	

- **Admission Scheduled of State Comman Entarance Test Cell, Maharashtra State, Mumbai**

Particulars		B.E. 1st Year Admissio	M.B.A.	M.E.
iv.	Last date of request for applications	14th July 2025	14th July 2025	16th July 2025
v.	Last date of submission of applications	15th July 2025	15th July 2025	17th July 2025
vi.	Dates for announcing final results	24th July 2025	24th July 2025	26th July 2025
vii.	Release of admission list	31st July 2025	31st July 2025	1st August 2025
viii.	Date for acceptance by the candidate	1st August 2025	1st August 2025	2nd August 2025
ix.	Last date for closing of admission & Starting of the Academic session	15th September 2025 & 7th September 2025	15 th September 2025 & 16th September 2025	15th September 2025 & 16th September 2025
x.	The waiting list shall be activated only on the expiry of date of main list	--	--	--
xi	The policy of refund of the Fee, in case of withdrawal, shall be clearly notified	***Institute shall refund the entire fees to the candidate after deduction of Rs.1000/-***		

18.12 Criteria and Weightages for Admission

- **Describe each critewrion with its respective weightages i.e. Admission Test, marks in qualifying examination etc.**

1. **CAP Admission :** Candidate should be an Indian National and should have passed the HSC (Std.XII) examination of Maharashtra State Board of Secondary and Higher Secondary Education or its equivalent examination with subjects English, Physics, Chemistry and Mathematics and Secured Minimum 150 marks out of 300 marks (minimum 45% marks i.e. 135 marks out of 300 marks in case of candidates of Backward class categories belonging to Maharashtra state only) in the PCM added together and Obtained non – Zero Score in Physics, Chemistry and Mathematics at MH – CET AND JEE.

2. **Institute level and Vacant seat admission:** Candidate should be an Indian National and should have passed the HSC (Std.XII) examination of Maharashtra State Board of Secondary and Higher Secondary Education or its equivalent examination with subjects English, Physics, Chemistry and Mathematics and Secured Minimum 150 marks out of 300 marks (minimum 45% marks i.e. 135 marks out of 300 marks in case of candidates of Backward class categories belonging to Maharashtra state only) in the PCM added together and Obtained non – Zero Score in Physics, Chemistry and Mathematics at MH – CET and JEE.

   For Lateral Entry i.e. Admission to Direct Second Year Admission: Diploma holders who have passed the Diploma course in Engineering / Technology with minimum of 50 % marks and medium of instruction as English from Polytechnics affiliated to MSBTE or AICTE approved autonomous Polytechnics situate in or outside the Maharashtra state.

- **Mention the minimum level of acceptance, if any**

  Candidate should be an Indian National and should have passed the HSC (Std.XII) examination of Maharashtra State Board of Secondary and Higher Secondary Education or its equivalent examination with subjects English, Physics, Chemistry and Mathematics and Secured Minimum 150 marks out of 300 marks (minimum 45% marks i.e. 135 marks out of 300 marks in case of candidates of Backward class categories belonging to Maharashtra state only) in the PCM added together and Obtained non – Zero Score in Physics, Chemistry and Mathematics at MH – CET and JEE.

- **Mention the cut-off levels of percentages and percent ilescore of the candidates in the admission test for the last three years.**

SN	Name of the Course	Cut-Off levels of percentage and percentile score		
		2023-24	2024-25	2025-26
1	B.E. Computer Science and Engineering	99.05/52.24	96.24/69.76	95.63/28.27
2	B.E. Electronics and Telecomm. Engg.	91.48/8.71	92.91/3.13	92.93/11.13
3	B.E. Electrical Engg. (Electronics and Power)	86.05/7.59	87.95/32.17	91.35/ 43.91
4	B.E. Mechanical Engineering	83.37/12.69	86.97/9.60	85.90 / 33.08
5	B.E. Information Technology	93.60/48.97	94.13/44.89	94.00/ 33.46
6	M.E. Electrical Power System	2.33	9.33/3.00	7.00/1.01
7	M.E. Digital Electronics	--	19.67/6.00	67.2
8	M.E. Computer Engineering	4.00	25.22/10.62	58.00/ 3.89
9	M.E. Advanced Mnufacturing and Mechanical System Design	1.33	62.08/0.67	22.00
10	MBA	93.00/50.00	91.11/19.74	96.69/6.09

- **Display marks scored in Test etc. and in aggregate for all candidates who were admitted.**

  Teaching Learning Process\B.E. First Year 2025-26 Marks Scored in Test etc. and in Aggregate for all Candidates who were Admitted..pdf

18.13 List of Applicants

- List of candidate whose applications have been received along with percentile/percentages core for each of the qualifying examination in separate categories for open seats. List of candidate who have applied along with percentage and percentile score for Management quota seats (Merit wise)

  As per CET & JEE exam score.

  Clek Here: Against CAP Merit list 2025-26.pdf

18.14 Results of Admission under Management Seats / Vacant Seats

i. Composition of selection team for admission under Management Quota

This information be made available in the public domain during the admission process

ii. List of candidate who have been offered admission

34 Students have been offered admission Against Vacancies remain after CAP round.

Clek Here: Against CAP Round Admitted Students 2025-26.pdf

iii. Waiting list of the candidate in order of merit to be operative from the last date of joining of the first list candidate. 1011 Students was waiting list for Vacant Seats admission

18.15 Information of Infrastructure and Other Resources Available

i.	Number of Class Rooms and size of each	:	31	2502 Sq.M.
ii.	Number of Tutorial rooms and size of each	:	15	780 Sq.M.
iii.	Number of Laboratories and size of each	:	63	5996.98 Sq.M.
iv.	Number of Computer Centres with capacity of each	:	02	505 Sq.M.
v.	Central Examination Facility, Number of rooms and capacity of each.	:	16 Rooms	35 Students each rooms.
vi.	Online examination facility (Number of Nodes, Internet bandwidth, etc.)	:	Nodes 150	1000 Mbps.
vii.	Barrier Free Built Environment for disable and elderly persons	:	Yes, available	
viii.	Fire and Safety Certificate	:	Yes	
ix.	Hostel Facilities	:	Yes	

x. Number of Library books / Titles / Journals available (Programme-wise)

Total No of Titles	**:**	**30133**
Total No of Books	**:**	**92361**

S.No	Subject	Text Books	Ref. Books	No. Of Books	Total No of Titles	Total No of DVDs
1.	Computer Science & Engineering	8698	3608	12306	4454	162
2.	Electronics & Telecommunication Engg	10362	4696	15058	5261	93
3.	Electrical Engineering	6471	3015	9486	3558	26
4.	Mechanical Engineering	5581	2593	8174	3114	51
5.	Information Technology	3072	1812	4884	1735	45
6.	M.B.A.	4929	3088	8017	3576	140
7.	Pure Science	5380	1993	7373	2106	30
8.	Social Science and Arts	2664	1441	4105	1947	80
9.	Humanities	6063	2364	8427	4382	268
10	Back-Volumes	--	8409	8409	--	--
11	Electronics Information Collection	--	4295	4295	--	---
12	Project / Seminar	--	1426	1426	--	---
13	ISI	--	401	401	--	--
	Total Books	**53220**	**39141**	**92361**	**30133**	**895**

xi List of online National / International Journals subscribed

Technical Journals (International) - 36
Technical Journals (National) - 41
Non-Technical Journals - 10
Online E- Journals - 479

xii National Digital Library (NDL) subscription details : YES

xiii. List of Major Equipment / Facilities in each Laboratory/Workshop

xiv. List of Experimental Setup in each Laboratory/Workshop

❖ **Department of Electrical Engineering (Electronics and Power)**

SN	Name of the Laboratory	Name of the Major Equipment's/ Facility in each Laboratory
1	Electrical machines Lab	D.C. Shunt Motor with Break Drum Arrangement for Loading (3 HP, 230 V, 12 A, 1500 RPM)
		Dc Shunt Motor (4 HP, 220 V ,13.6 A, 1500 RPM) Mechanically Coupled with Three Phase Alternator (3.5 KVA, 400 V, 4.3 A, 1500 RPM)
		D.C. Shunt Motor (5hp, 230 V, 1500 RPM) Mechanically Coupled with D.C. Shunt Generator (2.2 kW, 230 V, 1500 RPM)
		D.C. Compound Motor (5HP, 230 V, 19.6 A, 1500 RPM) Mechanically Coupled with D.C. Compound Generator (2.5 kW, 230 V, 11 A, 1500 RPM)
		D.C. Series Motor with Mechanical Loading Arrangement (3 HP, 230 V, 10.5 A, 1500 RPM)
		Three Phase Reluctance Motor with Mechanical Loading Arrangement (1 HP, 400 V, 2 A,1500 RPM)
		D.C. Shunt Motor (5 HP, 230 V ,16.2 A, 1500 RPM) Mechanically Coupled with Three Phase Alternator (3 kVA, 440 V, 4/3.2 A, 1500 RPM)
		D.C. Shunt Motor (0.75 HP ,230 V, 6.8 A, 3000rpm) Mechanically Coupled with Three Phase Alternator (1kva ,400v ,1.5 A ,3000rpm)
		D.C. Compound Motor (5 HP, 230 V, 16.2A, 1500 RPM) Mechanically Coupled with Three Phase Synchronous. Induction Motor (5HP, 415V, 7A, 1500 RPM)
		D.C. Shunt Motor with Flywheel (3 H.P. ,230 V, 12 A, 1500rpm)
		Single Phase Ac Series Motor (3 HP, 230 V, 11.5 A, 1500/3000rpm)
		Three Phase Induction Motor with Mechanical Loading Arrangement (3 HP ,415 V, 5.5 A, 920 RPM)
		Single Phase Induction Motor (2 HP,230 V,10 A, 1440 RPM)
		Three Phase Slipring Induction Motor (5HP, 440V, 7.5A, 1440 RPM) Mechanically Coupled with D.C. Series Generator (2.2 KW, 230V, 9.7A 1500 RPM)
		Three Phase Induction Motor (10HP, 415V, 14.4A, 1440 RPM) Mechanically Coupled with D.C. Compound Generator (5.5KW ,230 V, 24 A 1500 RPM)
		Three Phase Induction Motor with Mechanical Loading Arrangement (3HP 4.8A 415V 1440RPM)
		Single Phase Transformer (2.5 kVA, 250/440 V)
		Thrre Phase Schrage Motor (5.2/13.5HP ,420V, 12.5/17.5A, 600/1250RPM)
		Three Phase Synchronous Motor with D.C. Exciter (3HP ,400V, 4.3A, 1500RPM)
		Three Phase Induction Motor (5HP, 415V, 9.7A, 1440 RPM)
		Universal Motor with Mechanical Loading Arrangement (1HP ,230V, 4A, 1000 RPM)
		Three Phase Transformers 2Nos (5KVA , 433/250V)
		Three Phase Induction Motor with Pole Changing facility (2 HP, 440 V)
		Rectifier (250V DC, 50A)

		Storage C.R.O. (20 MHZ 38 WATT)
		Stroboscope (TYPE-5901)
		Digital Storage Oscilloscope DSO (TDS – 2002 TEKTRONIX MAKE,60 MHZ)
		D.C. Shunt Motor (: 5 HP, 220 V,19 A , 1500 RPM) Mechanically Coupled with D.C. Shunt Generator (3KW,13.6A, 1500 RPM)
		B.L.D.C Motor with Control Panel (1 HP,48 VOLT,3000 RPM)
		Ac Drive (VFD) Trainer Kit With 3phase I.M. (1HP,415V,1440 RPM)
		D.C. Compound Motor (5HP,220V,1500RPM) Three Phase Alternator Set (3KVA,415V,1500RPM)
		Synchronizing Panel with Phase Sequence Indicator, Synchroscope, Frequency Meter
2	Power System Lab	Demonstration Unit for MCB and RCCB
		Demonstration Unit for Motor and Winding Protection Relay
		Demonstration Unit for AC Contactors and AC/DC Relay
		Demonstration Unit for Under Voltage/Over Voltage and Time Delay Relay
		3 Phase Relay Testing Kit
		Motor Protection Relay
		Digital Storage Oscilloscope
		Phase Shifting Transformer
		Oil Circuit Breaker
		Microprocessor Based Relay (L& T Make)
		Over Current Protection (Numerical) Relay
		Primary Injection Unit
3	High Voltage lab	100 KV AC Testing Set
		100 KV DC Testing Set
		Insulating oil Tester
		Impulse Tester
4	Control System & Power Electronics Lab	D. C. Position Control
		Stepper Motor Kit
		Dc Motor Generator Set (220V, 4.6 Amp, 1500 RPM)
		3 Phase Induction Motor (3HP, 2.2KW,415V, 4.5 Amp)
		Dc, Shunt Motor Generator Set (2.5KW, 220V, 2.2Amp, 1500 RPM, 0.75HP)
		D.C Dual Power Supply (30V, 5Amp)
		Function Generator (5Mhz, with USB (4Nos)
		Synchro Kit (80V AC)
		D.S.O. (100MHz)
		Autotransformer 1-Phase 230 V, 8A
5	Electrical Measurement Lab	Autotransformer 3 Phase (440 V, 8 A)
		3 Phase Induction Motor (3HP, 2.2KW,415V, 4.5Amp)
		D.C Dual Power Supply (30V, 5 Amp)
		Stroboscope
		Autotransformer 1-Phase (230 V, 8A)
		Earth Tester
		LCR Meter
		Kelvin Double Bridge Kit Trainer
		Maxwells Induction Capacitance Bridge
		De'sauty's Bridge

6	Basic Electrical Engineering Lab	Single Phase Autotransformer (230-volt, 10 Amp)
		Three Phase Autotransformer (415volt, 15 Amp, 12.21 KVA)
		D.C. Regulated Power Supply (Dual) (Model 93 C) 0-30 V, 5 A
		Resistive Load Bank (2KW, 230 V, 10 Amp)
		Single Phase Transformer with tapping's (1KVA, 115 V/230 V)
7	PLC & Factory Automation Lab	Advanced Modular PLC Training Kit for MITSUBISHI Q-PLC. (Mitsubishi Q -series Modular PLC Training Kit with HMI, Remote IO network, Analog IO with Major Elements) – 02 Nos
		Automation Training Kit (Complete Automation Training Kit with Mitsubishi Fx-5 U series PLC with Servo and HMI 7 - 02 Nos
		Socket Tray – 02 Nos
		Desktop Computer: 20 Nos CPU: Lenovo Make, Intel (R), core TM, 3.9 GHz .32 Bit operating System, i3, 7th Generation 7100, 3.9 GHz, 2C Processor, single modular 4GB RAM. Monitor: 19.5 Inch LCD Backlite color Monitor Lenovo Make. VKB Series Keyboard: Lenovo Make Mouse: Lenovo Make
		UPS: 5KVA, with battery back-up Model: OnfinitI 5 KVA FMI C
8	Computer & Microprocessor Lab	Computer System: T5, 12 Generation (22 Nos)
		ETAP, PSCAD Software
		Microprocessor kits with peripherals
9	Power Quality lab	Power Quality Analyser
		Digital Storage Oscilloscope
		Data Acquisition System
		Multi-storage Oscilloscope
10	Electrical Power Research Lab	Lab VIEW
		Data Acquisition System
		Compact RIO
		PCB Designing facility

(List of Experimental Setup in each Laboratory / Workshop)

❖ **Department of Electrical Engineering (Electronics and Power)**

SN	Name of the Laboratory	Title of Experimental Setup
1	Electrical machines Lab	Determine the critical resistance of dc generator from it's OCC
		Determination of efficiency and regulation of dc compound generator by load test.
		Speed control of dc shunt motor by armature control & field control method.
		Conversion of three-phase a.c. supply into two-phase supply by using Scott connection.
		Perform the Swinburne's test on dc machine.
		Perform the oc & sc test on single phase transformer.
		To determine the efficiency and losses of a given transformer by Sumpner's test.
		Conduct the Load test on three phase Induction motor to determine its performance characteristics
		Plot the Circle diagram for three Phase Induction Motor by conducting the No load & Blocked rotor test on it.
		Separation of No-Load Losses of a Three Phase Induction Motor.
		Speed Control of Three-Phase Slip-ring Induction Motor.

		Determine the regulation of a three-phase alternator by EMF & MMF Method
		To synchronize the three-phase alternator with an Infinite Bus bar & perform the load test on it.
		Plot the V and inverted V curves of the three-phase synchronous motor.
		Determination of Xd and Xq by slip test
		Determination of Xd' and Xd'' by sudden symmetrical short circuit test.
		Determination of Xd'' and Xq'' by conducting static test.
		Determination of X1, X2 and X0 of synchronous machine by conducting the direct test.
		Determination of X1, X2 and X0 of synchronous machine by conducting the In-direct test.
		Load test on dc shunt motor.
		Rheostatic braking of d. C. Shunt motor
		Rheostatic braking of three-phase induction motor.
		Load test on single phase induction motor
		Speed Control of Three Phase Induction Motor in Forward & Reverse Direction by Using VFD.
2	Power System Lab	Study of relaying components and control circuit development
		Study of relaying components and control circuit development
		Determination of Through fault stability of differential relay
		Study of combine over current and earth fault protection scheme of an alternator
		Study the protection of three-phase transformer using differential relay (Merz-price protection scheme)
		Plot the characteristics of rewireable fuse and miniature circuit breaker (MCB)
		Study of under voltage and overvoltage protection of three phase induction motor using static relay
3	High Voltage Lab	Study of arc extinction phenomenon
		To study the breakdown in air using Sphere gap.
		To study the Corona effect.
		To study the testing of transformer oil.
		To study the testing of insulator (Dry and wet test).
		To study the lightning arrestor.
		To study the impulse voltage generator
		To study the high voltage 100Kv A.C testing set
		To study the high voltage 100Kv D.C testing set.
		To study the testing of high voltage cable.
4	Control System & Power Electronics Lab	Study of Potentiometer
		Study of A.C Synchoros
		Determination Of Transfer Function of D.C Generator
		Determination of Transfer Function of D.C Servomotor and Its Characteristics
		Performance Characteristics of A D.C Motor angular Position Control System (Manual Control)

		Determination of Frequency Response of Given R-C Network
		Determination of Transfer Function of A.C Tacho-Generator
		Experiment Study of The Operating Characteristics of a Small Stepper Motor And Its Controller
5	Electrical Measurement & Network Analysis lab	Verification Of Output Response of Series R-C Circuit for Step Input
		Study Of Dot Convention and Determination of A) Mutual Inductance B) Coupling Coefficient of A Given Transformer
		Verification of Mesh and Node Method of Analysis on A Given Network.
		Verification of Superposition Theorem on A Given Network.
		Verification of Thevenin's Theorem on A Given Network.
		Verification of Maximum Power Transfer Theorem on A Given Network.
		Verification of Reciprocity Theorem on A Given Network.
		Study of Milliman's Theorem & Verification of A Given Network.
		Determination of Abcd Parameters T-Network & Π-Network.
		Determination of Abcd Parameters of Two Port Network for A Given T-Network And Π Network
		Study of Tie Set and Cut Set Schedule for A Given Network
		Measurement of Active Power in Three Phase Circuit by Two Wattmeter
		Measurement of Reactive Power in Three Phase Circuit by Single Wattmeter
		Calibration of Single-Phase Wattmeter by Phantom Loading
		Measurement of High Resistance by Loss of Charge Method
		Measurement of Low Resistance by Kelvin Double Bridge
		Calibration of Single-Phase Energy Meter
		Determination of Unknown Capacitance By Desauty's Bridge
		Determination of Self Inductance by Maxwells Inductance-Capacitance Bridge
		Measurement of Speed by Using Stroboscope.
		Study of Phase Sequence Indicator and Megger.
6	Computer & Microprocessor Lab	Addition of 8-Bitnubers And 16-Bit Numbers
		Subtraction of Two 8-Bit Numbers
		Finding The Larger and Smaller One Among the Two 8-Bit Numbers
		Finding The Largest and Smallest Number from An Array of Ten,8-Bit Numbers
		Masking And Setting of Nibbles
		Block Data Transfer in Same and Reverse Order
		Sorting Of Even and Odd Numbers from An Array of 8-Bit Numbers
		Multiplication of Two 8-Bit Numbers
		Square Wave Generation Using 8255
		Stepper Motor Control Using 8255
		Write a computer programme in 'C' language to estimate the efficiency & regulation of a single-phase transformer at desired load condition from its OC & SC test data.
		Write a computer programme in 'C' language for core design of a three-phase core type transformer.

		Write a computer programme in 'C' language for Estimation of Iron losses in a three-phase core type transformer.
		Write a computer programme in 'C' language for windings design of a three-phase transformer.
		Write a computer programme in 'C' language for Estimating the No load current of a three-phase transformer.
		Write a computer programme in 'C' language for tank design and calculating the number of cooling tubes required for three phase transformers.
		Write a computer programme in 'C' language to calculate the Main dimensions (D & L) of a three phase Induction motor. Write a computer programme in 'C' language for stator core design of a three-phase induction motor.
		Write a computer programme in 'C' language for a squirrel cage rotor design of three phase induction motor.
7	Basic Electrical Engineering Lab	To Verify Kirchhoff's current Law and Kirchhoff's voltage Law.
		To verify Superposition Theorem.
		To verify Thevenin's Theorem.
		To verify vector relationship of current & voltage in the given Single-phase R-L-C Series circuit.
		To verify vector relationship of current & voltage in the given Single-phase R-L-C Parallel circuit.
		To verify line & phase relationship of current & voltage between line and phase quantities in a balanced Star connected load.
		To verify line & phase relationship of current & voltage between line and phase quantities in a balanced Delta connected load.
		To conduct the load test on Single-phase transformer to obtain the efficiency and regulation.
		Study the Construction (Various parts) of D.C. Motor
		To study the measuring instruments.
8	Electronics Technology Lab	To study the different electronic components.
		To study the different measuring instruments and equipment's.
		To find the value of resistor using resistor colour code and digital multimeter (DMM).
		To find the value of capacitor using capacitor identification code and capacitor colour code
		To find the value of inductor using inductor colour code.
		To study the use of breadboard and build a led circuit on breadboard.
		To soldering and Desoldering of electronic components.
		To study the PCB design and fabrication.
		To study VI characteristics of P-N junction diode
		To study VI characteristics of Zenner diode and load regulation
		To study Half Wave Rectifier with & without Filter
		To study Centre-tap Full Wave Rectifier with & without Filter
		To study transistor as a switch
		To study Common Emitter transistor Characteristics
		To study Common Base transistor Characteristics
		To study UJT relaxation oscillator

9	Power Electronics Lab	To Verify the Characteristics Of SCR And To Find Ih And Il .also To Verify The Gate Characteristics Of SCR
		To Verify Forward and Reverse Characteristics Of DIAC
		To Verify Forward and Reverse Characteristics Of TRIAC
		To Study UJT Relaxation Oscillator Using UJT
		Ac Voltage Control Using Triac Diac Combination
		To Verify the Operation of Half Controlled And Full Controlled Bridge Converter Of SCR Using Digital Firing Circuit.
		To Verify the Operation of SCR Commutation Circuits
		To Design Dc-Dc Buck Converter and Simulate It with MATLAB
		To Plot Frequency Response of Non-Inverting Mode of Op-Amp Using IC 741. Also To Determine Band Width and Maximum Gain.
		To Plot Frequency Response of Inverting Mode Of Op-Amp Using IC 741 Also To Determine Band Width And Maximum Gain
		To Study Op-Amp as an Integrator using Op-Amp IC 741 For Different Signals
		To Study Op-Amp as a Differentiator Using Op-Amp IC 741 For Different Signals
		Design the Circuit for Supplying 5V, 50 mA as A Low Voltage Regulator Using IC 723.
		To design and Set Up a stable Multivibrator of 1000 Hz Frequency And 60% Duty Cycle Using Ic 555
		Verification Of Truth Table of Various Logic Gates Using Various ICs
		To Study and Verify the Operation Sr and M-S J-K Flip-Flop.
		To Verify the Operation of Multiplexer Using IC 74153 (Dual 4:1 Mux)
		To Design and Verify Function of Decade Counter Using IC 7490
10	PLC & FA Lab	Write a program to find bus incidence matrix for a given Power System Network.
		Write a program to find basic Cut set incidence matrix & basic loop incidence matrix for a given Power System Network.
		Write a program to find bus admittance (Ybus) matrix for a given Power System Network by building algorithm.
		Write a Program to find bus Admittance (Ybus) matrix for a given Power System Network using Singular Transformation.
		Perform short circuit analysis for a given Power System Network by using simulation software.
		Write a program for load flow Analysis for a given Power System Network using bus Admittance (Ybus) Matrix
		To plot Power-Angle curve of Synchronous Machine for stability analysis
		To plot Swing curve of Synchronous Machine for stability analysis
		Generation of Power Quality event using Line Fault Model and simulate it with MATLAB.
		Generation of Power Quality event using Line Fault Model and simulate it with MATLAB.
		Simulation of Half Controlled Bridge Converter (Semi-converter)
		To calculate the Root Mean Square value of voltage signal using MATLAB

		To calculate the Peak value of voltage signal using MATLAB.
		To calculate the fundamental value of voltage signal using Fourier transform Method
		Development of Control Circuit of Direction Control of Three Phase IM using PLC
		Development of Control Circuit of DOL Starter for Three Phase IM Using PLC
		To Design and simulate charging and discharging circuit of supercapacitor

- **List of Major Equipment / Facilities in each Laboratory/Workshop**

❖ **Department of Electronics and Telecommunicaion Engineering**

SN	Name of the Laboratory	Name of the Important equipment	Facilities in laboratory
1	Digital Signal Processing Laboratory	Universal Trainer Kit for CPLD/FPGA TK Based, Universal Programmer, UPS 10 KVA, Logic Analyzer LA-5000 Series, DM 642 EVM Based Board for DSP, Interfacing Board for DM642, IBM eServer 226, arbitrary waveform Generator, Arbitrary Waveform Generator, TMS 320C6711DSP Kit, ADC THS 1206 EVM, TMS 320C6711 DSP Kit	Signal Processing and Analysis Facility Project Development Facility
2	Microprocessor & Microcontroller Laboratory.	Virtex II Xilinx Board with Xilinx & Sysgen Software, ARM Processor Titan Board, ARM Processor Metis Board, ARM Processor Explorer Board, ARM Processor Voyager Board	Internet FacilityProject Development FacilityLCD Projector
3	Power Electronics and Instrumentation lab	PC-based instrumentation trainer Educational PLC, DSP kit TMS320, CRO 100 MHz, CRO 60 MHz, High voltage power electronics kits, MCK 28335 Pro-SCIM, Transducer display	Internet FacilityProject Development FacilityLCD Projector
4	Analog and Digital IC Laboratory	Digital CRO, Function Generator, Thermometer, Multimeter	Analog and digital Circuit Development Facility
5	Electronic device and Circuit Laboratory	Digital CRO Function Generator Thermometer Multimeter, 10 PC with Python Programming.	Electronic Devices and Circuits Analysis and Design facility
6	Communication Engineering Laboratory	Spectrum Analyzer, Signal Generator, Synthesized Function Gen, Fiber Optic Trainer (T& R), Antenna Trainer Unit, GSM Evaluation Kit, Satellite Comm. Trainer, CDMA Direct Sequence Spread Spectrum, EPABX M/c with UPS ,Micro strip Trainer,Microwave Training System, Vector Network Analyzer	RF Circuits and Antenna Facility
7	Electronics workshop Laboratory	LCD Projector, Component tester, PCB Prototype Machine (EP-2002)	PC With Internet connectionProject Development Facility
8	Project Laboratory	PC, Function Generator, Digital CRO, CD system, Multimeter, Lux meter, Tachometer, Thermometer	Project Development Facility

9	VLSI & Embedded System Design Center (Complete Cadence Suit with Synopsis Tools).	Cadence IC 6.14, analog/RF/digital mixed signal EDA tools, virtuoso ADE verifier, virtuoso analog design environment. virtuoso space-based router	Signal Processing and AnalysisFacility Project Development Facility
10	PG Laboratory	universal trainer kit for CPLD /FPGA, TK-based TMS 320C6713 DSP starter kit, cranes MSP - 430 starter kit, Xilinx 13.4, and sysgen software	Practical and Projects are Conducted on the Computational, Kits and Boards facilities available.
11	AIML Lab	AIML Master Node: 01; Compute Note: 01; Desktop Computers: 32, Software: Jupytor Notebook (Anaconda), IoT Kits, IoT related skills	Advanced Artificial Intelligence and Machine Learning to facilitate research projects in AIML
12	VLSI Cadence Lab	Cadence VLSI Design Suite	Analog/RF/digital mixed signal design

- **List of Experimental Setup in each Laboratory / Workshop**

❖ **Department of Electronics and Telecommunication Enginering**

SN	Name of the Laboratory	Title of Experimental Setup
1	PE LAB	To verify the characteristics of SCR. Obj: 1. To plot V-I characteristics of SCR. 2. To measure Latching and Holding current of SCR.
		To verify the characteristics of DIAC. Obj: 1.To plot V-I characteristics of DIAC when MT1 is +ve w.r.t. MT2. 2. To plot V-I characteristics of DIAC when MT1 is -ve w.r.t. MT2
		To verify the characteristics of TRIAC. Obj: 1.To plot V-I characteristics of TRIAC when MT1 is +ve w.r.t. MT2. 2. To plot V-I characteristics of TRIAC when MT1 is -ve w.r.t. MT2
		To study UJT relaxation oscillator for the triggering of SCR.
		To verify the operation of Full controlled Bridge Rectifier for Resistive & Inductive load.
		To verify the working of SCR Commutation Obj:- 1. To examine class A, class B, class C commutation of SCR 2. To draw the waveforms at different points for commutation circuit
		To study speed control of universal motor using TRIAC.
		To verify the operation of chopper using TRC principle with MATLAB simulation
		Measurement of linear displacement using LVDT
		To study Technical appraisaland prepare a real time project feasibility report containing Technical appraisal.
		To study Environmental appraisal and prepare a real time project feasibility report containing Environmental appraisal.
		To study Market appraisal and prepare a real time project feasibility report containing Market appraisal (including market survey forecasting future demand and sales)
		To prepare Project cost estimation for any project.
		To prepare projected income statements for any project.
		To study entrepreneurial competencies, traits and motivation. (McClelland Achievement motivation theory)
		To study Lean Canvas Model for a Start-up Idea.
		To prepare a detail project report of any solar industry based on your field visit.
2	PROJECT LAB	Write a program to demonstrate working of Shift cipher. Demonstrate the breaking of shift cipher (Brute force attack). (CO1)
		Write a program to demonstrate working of mono-alphabetic cipher. Demonstrate the breaking of mono-alphabetic (Crypto-analysis). (CO1)

		Write a program to demonstrate any transposition stream cipher algorithm. (CO1)
		Encrypt and decrypt a message using DES in ECB/CBC mode (CO2)
		Write a program to implement RSA algorithm by some suitable example. (CO2)
		Generate SHA-256 and SHA-512 Hashes for Given Messages. (CO3)
		Implementation of firewall using Cisco Packet Tracer. (CO4)
		Study of WLAN network and its virtual implementation using Cisco packet tracer. (CO4)
		To make straight and cross CAT 5 cables using RJ45 connectors and crimping tools. Check the crossover cable with two PC LAN connections.
		To create and check multiple PCs LAN connection using an Ethernet switch/hub and straight-through cable
		To configure various Network connecting devices using Packet Tracer Software. a) Repeater b) Bridge c) Hub d) Switch e) Router
		To configure various Network topology using Packet Tracer Software. a) Bus b) Ring c) Mesh d) Star
		To install and access Remote Computer desktop and share printer between PCs on local area network.
		To share File and Folder with various Permissions and security levels between two PCs in the network.
		To use Cisco packet tracer software to analyze traffic flow in the network.
		To install and configure wireless access points and verify various security settings.
3	ESD/VLSI LAB	To get familiarization with 8085A Kits. And verify the operation of all types of data transfer instructions. Write and execute an ALP for, a) 8-bit Addition b) 8 bit Subtraction c) 8 bit AND Operation d) 8 bit OR Operation
		Study of simulation environment and microcontroller kit and describe the internal memory organization of Microcontroller and write embedded-C program to make Independent Blinking of Multiple LEDs at Different Rates using 8051.
		Interface 7-segment display with AT89C51 microcontroller and write embedded-C program to Displaying Hex Digits (0–F).
		Interface DIP Switch and LEDs withe AT89C51 microcontroller and write embedded-C program to make LED/Relay ON/OFF based on switch condition.
		Interface LCD display with AT89C51 microcontroller and write embedded-C program to display name and roll number on the LCD.
		Interface stepper motor with AT89C51 microcontroller and write embedded-C program to rotate stepper motor continuously
		Develop an application to rotate DC motor clockwise and anticlockwise
		Interface DAC0809 with AT89C51 microcontroller and write embedded-C program to generate sine wave.
		Study of AVR Development board and ATMEL Studio 7, AVRDUDESS programmer and USBASP driver
		Alternate LED control with ATmega16 microcontroller.
		Seven Segment Display with ATmega16 microcontroller.
		16x2 LCD interfacing with ATmega16 microcontroller
		4x4 Keypad interfacing with ATmega16 microcontroller
		Process parameters measurement using ATmega16 microcontroller
		Stepper motor interfacing with ATmega16 microcontroller
		AVR ATmega16 PWM interface to control LED brightness
		Relay Interfacing with atmega16 microcontroller

4	DSP LAB	Write a MATLAB code to illustrate the Nyquist sampling theorem. The program should illustrate the effects on sampling the signal at1) Exactly the folding frequency 2) Frequency less than the folding frequency 3) Frequency greater than the folding frequency Plot the magnitude spectrum for all the above said cases.
		Write a MATLAB code to obtain the discrete Fourier transform and Inverse Discrete Fourier transform of given digital signal. Also plot the magnitude and phase response.
		Write a MATLAB code to obtain the Circular convolution of two given sequences
		Write a MATLAB code to compute the DFT of a sequence x(n) using Decimation in Time algorithm
		Write a MATLAB code to compute the DFT of a sequence x(n) using Decimation in frequency algorithm.
		Write a MATLAB code to verify the Low Pass and High Pass FIR linear phase filter design using Kaiser window method.(Beyond syllabus)
		Write a MATLAB code to verify the Low Pass Butterworth IIR filter design using bilinear transformation(BLT) method
		Write a MATLAB code to obtain the Autocorrelation and Cross correlation of a given sequence . .(Beyond syllabus)
		Write a MATLAB code to illustrate the effect of Decimation and Interpolation by an integer factor. Plot the magnitude spectrum.
5	ADC LAB	To study and implement Python basic syntax, variables, data types, input/output statements, and control structures such as conditional and looping statements.
		Demonstration of various operators used in Python with suitable example.
		a) Write a Python program to implement the operations that can be performed in a string. b) Demonstrate the functions/methods which operates on strings in Python with suitable examples:
		Demonstrate the use of file operations using Virtual labs: a) Write a Python program to perform read and write operations on a file. b) Write a Python program to copy the contents of one file to another file. c) Write a Python program to count frequency of characters in a given file Write a Python program to print each line of a file in reverse order.
		a) Demonstrate the different ways of creating list objects with suitable example programs. b) Demonstrate the functions/methods which operate on lists in Python with suitable examples.
		a) Demonstrate the different ways of creating tuple objects with suitable example programs. b) Demonstrate the functions/methods which operate on tuples in Python with suitable examples.
		To study the concept of Data Frame in Python and perform various operations such as creation, indexing, slicing, filtering, sorting, and statistical analysis uses the Pandas library.
		Write a program for Plotting various graphs using Python
		Study of Signal Processing Functions used in MATLAB
		Program to generate standard continuous Time Signals a) Sine Wave b) Square wave c) Cosine Wave d) Triangular wave
		Program to generate standard discrete Time Signals a) Impulse b) Step c) Ramp d) Exponential signal
		Program to perform basic operations on Signals Delay, Advance and Folding

		Program to find Even and Odd parts of a sine signal for selected amplitude and frequency
		Program to find the Energy & Power of analog signal.
		Write a MALAB Program to demonstrate the system is linear or non-linear.
		Program to find Fourier transform of given signal and plot its Fourier spectrum.
6	EDC LAB	To Verify V-I characteristics of p-n junction diode and obtain static and dynamic resistance values.
		To obtain the efficiency and ripple factor of Full-wave Bridge rectifiers without and with filter.
		To verify Zener diode as a voltage regulator.
		To perform and plot input-output response of positive, negative, and dual clipper circuits with and without reference voltage.
		To compute theoretical and practical frequency of oscillation of RC phase shift oscillator.
		To obtain the efficiency and ripple factor of Full-wave Bridge rectifiers without and with filter on Multisim s/w
		To plot the drain and transfer characteristics of JFET and calculate rd, gm and μ.
		To study and perform UJT as a relaxation oscillator.
		To verify an inverting and non inverting amplifier with as pe cificga in values in go amp IC741
		To verify operation of RC phaseshiftoscillatorusingopampIC741
		To design LPF and HPF forspecificfrequencyusingopampIC741
		To demonstrate comparator circuitusingIC 741
		To design low voltage regulator for output voltage of 5V using IC 723
		To design and test MonostablemultivibratorcircuitusingtimerIC555
		To design and study VCO using IC566 using multisim.
		TostudycharacteristicsofPhaseLockedLoopusingIC565
7	CE LAB	To Study CE amplifier with Variation of load Resistance
		To study and verify the operation of Inverting mode of op-amp using IC 741
		To study and verify the operation of non Inverting mode of op-amp using IC 741
		To study and verify the operation of Logic gates.
		Design and implementation of Half Adder and Half Subtractor using logic gates.
		To verify the operation of multiplexer using IC 74153 (Dual 4: 1 Mux)
		To verify the operation of De-multiplexer using IC 74155 (Dual 1: 4 Demux)
		To Verify the operation of 4 bit Comparator.
		Layout, physical verification, placement & route for design, static timing analysis, parametric analysis of CMOS Inverter on silicon using appropriate ASIC design tool.
		Layout, physical verification, placement & route for design, static timing analysis, parametric analysis of two input NAND and NOR logic gates on silicon using appropriate ASIC design tool.
		Layout, physical verification, placement & route for design, static timing analysis, of D-flip-flop with reset on silicon using appropriate ASIC design tool.
		To write verilog code for 3 to 8 decoder and simulate with test bench, synthesis implement on PLD
		To write verilog code for 1 to 4 Demux and simulate with test bench, synthesis implement on PLD

		To write verilog code for D-flip-flop with reset and simulate with test bench, synthesis implement on PLD
		To write verilog code for 4-bit counter and simulate with test bench, synthesis implement on PLD
		To write verilog code for 4 bit adder and simulate with test bench,synthesis implement on PLD
		Measurement of Linear Displacement using LVDT
		Measurement of Displacement using Photosensitive Transducer
		Measurement of Pressure using Piezo-electricTransducer
		Temperature Measurement using Temperature Sensor.
		Displacement Measurement by Resistive Transducer.
		Measurement of Force using Strain Gauge
		Liquid Level Measurement using Level Transducers.
		Study of Smart Sensors and Data Acquisition Systems.
		To verify the V-I characteristics of Gunn diode and Measurement of Gunn oscillator frequency.
		To measure the frequency of microwave signal using slotted line and reflecting sheet method.
		Introduction to microstrip line trainer kit & measurement of attenuation
		Microstrip power division using microstrip line trainer.
		Study of Micro Strip Ring Resonator **Using microstrip line trainer.**
		To study the output power and frequency characteristics of a Gunn oscillator as a function of applied voltage.
		To verify basic Properties of directional coupler
		To compare coaxial line, stripline, and microstrip line in terms of losses, dispersion, and bandwidth (Beyond syllabus)
8	WS LAB	To study the function of amplitude modulation and demodulation (under modulation, perfect modulation & over modulation) and also to calculate the modulation index.
		To study the process of frequency modulation and demodulation and calculate the depth of modulation by varying the modulating voltage.
		To study the time division multiplexing and demultiplexing techniques
		To study the pulse amplitude modulation & demodulation techniques. To study the effect of amplitude and frequency variation of modulating signal on PAM.
		To convert an analog signal into a pulse digital signal using PCM system and to convert the digital signal into analog signal using PCM demodulation system.
		To study the pulse width (PWM) modulation & demodulation techniques.
		To study ASK modulation and de-modulation
		To study FSK modulation and de-modulation
		Study and identification of electronics active and passivecomponents.
		To measure the Resistor value by color code and verify the same using multimeter.
		To identify Capacitors and measure the value by using LCR meter.
		To identify Inductors and measure the value by using LCR meter.
		Design and verify the basic logic gates using universal gates.
		Execute soldering and de-soldering of various electronics components on PCB for electronic circuits.
		Implement small electronics circuits using active and passive components on PCB.
		Getting an overview of PCBs available in PC and LAPTOP.

- **List of Major Equipment / Facilities in each Laboratory/Workshop**
- **List of Experimental Setup in each Laboratory/Workshop**

❖ **Department of Mechanical Engineering**

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	CAD/CAM	Auto Desk Inventor Series Pro-7 (Software)	Solid Modeling & Assembly Modeling
		Solid Edge V 14 (Software)	
		ANSYS Introductory Software V 9.0 (Software)	
		UG NX3 Advanced Designer Bundle (Software)	
		WITNESS 2006 (Rel 2.0) (Software)	
		SOLID WORKS 2025 (Software)	
		Computer System - Desktop PC I 3 Lenovo ThinkCentre (Qty 14) (Hardware)	

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	CAD/CAM	Introduction to design process and SW	Modeling & Assembly Software
2		Sketcher environment of SW & Creation of sketches of components	
3		Modeling environment of SW & Creation of models of components	
4		Assembly environment of SW & Creation of assemblies of components	
5		Simulation Languages and Packages	

SUBJECT: ENGINEERING GRAPHICS

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Drawing Hall	Drawing Boards	Drawing on Drawing Sheets
2		Solid Models	Projection of Solids And Sections
3		Drawing Instruments	Projection of Straight Lines, Planesand Solids
4		Three Dimensional Models	Orthographic and Isometric Projections
5		Drawing Boards	Drawing on Drawing Sheets

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Drawing Hall	Free hand skethes of simple machine elements	--
2		Projection of Straight Line	Demo Models and Drawing Instruments
3		Projection of Plane	Chart,Models of Plane
4		ProjectionofSolids	Solid Models
5		Projection of Section of Solid	Cut Section Solid Models
6		Development of sections of Solid	--
7		Orthographic Projection	Three Dimensional Models
8		IsometricViews & Projections	Three Dimensional Models
9		Drafting of Basic 2d Geometrical Shapes Using Cad Software	Cad Software (AUTOCAD)
10		Drafting of Basic 3d Geometrical Shapes Using Cad Software	Cad Software (AUTOCAD)

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Energy Conversion / IC Engine Lab	Blower Test Rig	Experimental setup of Blower
		Single Cylinder4-Stroke Diesel Engine	Experimental setup of single cylinder Diesel engine
		Three Cylinder 4-Stroke Petrol Engine	Experimental setup of 4-stroke3 cylinder Petrol engine
		computerized multi cylinder diesel engine	Experimental setup of computerized multi cylinder diesel engine
		Single Stage Reciprocating Air Compressor Test Rig	Experimental setup of single stage Compressor
		Two Stage Reciprocating Air Compressor Test Rig	Experimental setup of multistage Compressor

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	EC II	Trial on Reciprocating Compressor. (Single Stage & Multistage)	Experimental set up of single and multy stage Compressor
2		Trial on Centrifugal Blower	Experimental set up of Blower
3		Study of Gas Turbines With The Help Of Models	models
4		Study of Domestic Refrigerator.	Experimental set up of Refrigerator
5		Study of Room Air Conditioner	Experimental set up of Air Conditioner
6		Study of Photovoltaic System	Experimental set up of solar plate

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	I C Engine	Performance Test on A Single Cylinder Diesel Engine	Experimental set up of single cylinder diesel engine
2		Performance Test on A Single Cylinder Petrol Engine	Experimental set up of single cylinder Petrol engine
3		Evaluation of the Heat Balance for Single Cylinder Diesel Engine	Experimental set up of single cylinder diesel engine
4		Morse Test on Multy-Cylinder Petrol Engine	Experimental set up of single cylinder Petrol engine
5		Study of Bosch Type Single Plunger Fuel Pump	Model of Bosch fuel pump M & M
6		Study of Various Types of Fuel Injectros and Nozzles	On M & M Engine

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	EM Lab.	Differential Axle And Wheel	
		Single Purchase Winch Crab	
		Double Purchase Winch Crab	
		Simple Screw Jack	
		Worm & Worm Wheel	
		Fly Wheel	
		Coil Friction Apparatus	
		Compound Pendulum	
		Inclined Plane Apparatus	
		Simple Jib Crane	
		Simple Supported Beam	
		Universal Force Table	

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	EM Lab.	Verification of Law of Polygon of Forces	Universal Force Table
2		Reactions at the Supports of a Simple Beam	Simply Supported Beam
3		To Determine the Forces in the Members of Jib Crane	Simple Jib crane
4		Friction on Inclined Plane	Inclined Plane Apparatus
5		Differential Axel Wheel	Differential Axel Wheel
6		Single Purchase Winch Crab	Single Purchase Winch Crab
7		Simple Screw Jack	Simple Screw Jack
8		Graphical Solution of problems	Lab Manual

- **List of Experimental Setup in each Laboratory**

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Fluid Power & CFD Lab.	A) Pelton Wheel Turbine	Testing on Pelton Turbine Setup
		B) Francis Turbine	Testing on Francis Turbine Setup
		C) CFD Software (Ansys Fluent Mechanical & CFD, Teaching & Research Lic 25+1)	Simulation & Analysis of Fluid Flow Problems using Ansys Fluent CFD software

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Fluid Power & CFD Lab	Determination of Metacentric Height.	Ship Model
		Verification of Bernoulli's Equation	Bernoulli's Apparatus
		Flow Measurement By Venturimeter.	Venturimeter Apparatus
		Flow Measurement By Orifice meter.	Orifice meter Apparatus
		Forces of Impact of Jet	Impact of Jet
		Determination of Reynolds Number.	Reynolds Apparatus
		Trialon Pelton Turbine	Pelton Turbine
		Trialon Francis Turbine	Francis Turbine
		Trialon Centri fugal Pump	Centri fugal Pump
		Trialon Reciprocating Pump	Reciprocating Pump
		Trialon Hydraulic Ram	Hydraulic Ram
		Simulation of Fluid Flow and Heat Transfer Problems using Ansys Fluent CFD Software	CFD Software (Ansys Fluent Mechanical & CFD, Teaching & Research Lic. 25+1)

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Fluid Power & CFD Lab	Determination of Metacentric Height.	Ship Model
		Verification of Bernoulli's Equation	Bernoulli's Apparatus
		Flow Measurement By Venturimeter.	Venturimeter Apparatus
		Flow Measurement By Orifice meter.	Orifice meter Apparatus
		Forces of Impact of Jet	Impact of Jet
		Determination of Reynolds Number.	Reynolds Apparatus
		Trial on Pelton Turbine	PeltonTurbine
		Trial on Francis Turbine	FrancisTurbine
		Trial on Centrifugal Pump	Centrifugal Pump
		Trial on Reciprocating Pump	Reciprocating Pump
		Trial on Hydraulic Ram	Hydraulic Ram
		Simulation of Fluid Flow and Heat Transfer Problems using Ansys Fluent CFD Software	CFD Software (Ansys Fluent Mechanical & CFD, Teaching & Research Lic. 25+1)

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	MOMLAB	a) Computerized Universal Testing Machine ,Model-TUE-C-400b)Data Acquisition system c)Computer-Lenovo	Tensile Test, Compression Test, Shear Test , Bending Test
		Izod Impact Testing Machine (Capacity -30 Kg)	Toughness Testing
		Spring Apparatus	Deflection of compression & Tensile Spring
		Torsi on Test Apparatus	Twis to fmaterials or bars

List of Experimental Setup in each Laboratory

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	MOMLAB	TensionTest on Mild Steel Bar	Computerized Universal Testing machine
		Compression Test on Wooden Block	Computerized Universal Testing machine
		Shear Teston Metal	Computerized Universal Testing machine
		Modulus of RuptureTest	Computerized Universal Testing machine
		Torsion Teston Metals	Torsion Test Apparatus
		Impact Teston Metals	Izod Impact Testing Machine Apparatus
		Deflection of Springs	Spring Apparatus

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Material Science	Inverted Metallurgical Microscope	Experimental Set Up Of Inverted Metallurgical Microscope
2		Double Disc Polishing Machine	Experimental Set Up Of Double Disc Polishing Machine
3		Rockwell Hardness Testing Machine	Experimental Set Up Of Rockwell Hardness
4		Abrasive Belt Grinder Machine	Experimental Set Up Of Abrasive Belt Grinder
5		Muffle Furnace	Experimental Set Up Of Muffle Furnace
6		Trinoculer Metallurgical Microscope	Experimental Set Up Of Trinoculer Metallurgical Microscopewith

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Material Science	Preparation of specimen for micro-examination	Experimental Set Up Of Inverted Metallurgical Microscope
2		Preparation of specimen for micro-examination	Experimental Set Up Of Double Disc Polishing Machine
3		Study different heat treatment process for steel (Hardening and Tempering).	Experimental Set Up Of Rockwell Hardness
4		Preparation of specimen for micro-examination	Experimental Set Up Of Abrasive Belt Grinder
5		Study different Heat Treatment Process For steel (Annealing and Normalizing).	Experimental Set Up Of Muffle Furnace
6		Study of metallurgical microscope	Experimental Set Up Of Trinoculer Metallurgical Microscope

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Measurement Systems	Vibration Measurement (Piezoelectric Transducer)	Experimental Set Up Of Vibration Measurement
2		Speed Measurement Trainer (Magnet Ic Pick Up)	Experimental Set Up Of Speed Measurement
3		Angular Displacement Measurement	Experimental Set Up Of Angular Displacement
4		Flow Measurement	Experimental Set Up Of G.H. Endress Test Rig
5		Temperature Measurement	Experimental Set Up Of G.H. Endress Test Rig

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Measurement Systems	Vibration Measurement	Experimental Set Up Of Vibration Measurement
2		Speed Measurement by a Stroboscope	Experimental Set Up Of Speed Measurement
3		Performance of capacitance Transducer as angulardis placement measuring device.	Experimental Set Up Of Angular Displacement
4		Flow Measurement	Experimental Set Up Of G.H. Endress Test Rig
5		Temperature Measurement	Experimental Set Up Of G.H. Endress Test Rig

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Metrology Quality Control	To Measure the dimension of a gear.	Experimental Set Up Of Gear Tooth Vernier Calliper
2		Measurement of various angles of a single point cutting tool by using Tool Makers Microscope.	Experimental Set Up Of Tool Makers Microscope
3		To determine precision Angular Measurement using sine bar.	Experimental Set Up Of Granite Surface Plate & Sine Bar
4		To measure the dimension of a Gear	Experimental Set Up Of Geartooth Vernier Callper
5		To measure Screw thread of elments	Experimental Set Up Of Thread Pitch Gauge
6		To determine linear measurement using Vernier Height Gauge & Micrometer.	Experimental Set Up Of Vernier Height Gauge GAUGE & Micrometer.

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Refrigeration And Air conditioning	Vapour compression refrigeration system	Experimental Set Up Of Vapour compression refrigeration
2		Study of Window AC	Experimental Set Up Of Window AC
3		Household refrigerator	Experimental Set Up Of Household refrigerator
4		Water cooler unit	Experimental Set Up Of Water cooler unit
5		Leak testing & charging unit	Experimental Set Up Of Leak testing & charging
6		Dezart cooler Unit	Experimental Set Up Of Dezart cooler

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Refrigeration And Air conditioning	Vapour compression refrigeration system	Experimental Set Up Of Vapour compression refrigeration
2		Study of Window AC	Experimental Set Up Of Window AC
3		Study of Household refrigerator	Experimental Set Up Of Household refrigerator
4		Study of Water cooler	Experimental Set Up Of Water cooler unit
5		Study of Leak testing & charging	Experimental Set Up Of Leak testing & charging
6		Study of Dezart cooler	Experimental Set Up Of Dezart cooler

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Mechatronics	Pneumatic/ Electropneumatic Training Kit	Experimental Set Up Of Pneumatic / Electropnumatic Training
2		Pneumatic Rotary Indexing Kit	Experimental Set Up Of Pneumatic Rotary Indexing
3		Electro Hydraulic Training Kit	Experimental Set-Up For Electrohydraulic Training
4		Elevator Lift Model	Experimental Set Up Of Elevator Lift
5		Sensor Trainer Kit Conveyer Station Kit	Experimental Set Up Of Sensors
6		Automatic Integrated Assembly	Automatic Integrated Assembly Model

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Mechatronics	Pneumatic/ Electropneumatic Training	Experimental Set Up Of Pneumatic / Electropnumatic Training
2		Pneumatic Rotary Indexing	Experimental Set Up Of Pneumatic Rotary Indexing
3		Study Of Elevator Lift Model	Experimental Set Up Of Elevator Lift
4		STUDY OF SENSORS And CONVEYER STATION	EXPERIMENTAL SET UP OF SENSORS AND CONVEYER
5		Automatic Integrated Assembly	Automatic Integrated Assembly Model

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Robotics Lab	Panasonic TM -1400 G III & Industrial Robotic cell.	EXPERIMENTAL SET UP Of & Industrial Robot.
2		Automated Guided Vehicle (AGV) RAGHAV 1.5 Mobile Robot.	EXPERIMENTAL SET UP Of & Industrial AGV Robot.

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	Robotics Lab	Demonstration of Robot with 2DOF, 3DOF, 4DOF, etc.	EXPERIMENTAL SET UP Of & Industrial Robot.
2		Study of Positioning and orientation of Robot arm (Study of Robot Kinematics)	EXPERIMENTAL SET UP Of & Industrial Robot.
3		Study of drives of Panasonic TM -1400 G III Industrial Robot Arm.	EXPERIMENTAL SET UP Of & Industrial Robot.
4		To Study Robotic Control on Panasonic TM-1400GIII Industrial Robot Arm.	EXPERIMENTAL SET UP Of & Industrial Robot.
5		To Study Automated Guided Vehicle. (Mobile Robot) RAGHAV 1.5	EXPERIMENTAL SET UP Of Automated Guided Vehicle (AGV) RAGHAV 1.5 Mobile Robots.

List of Major Equipment/Facilities in each Laboratory/Workshop

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	TOM LAB	a) FFT Analyzer for Vibration Signals with Computer	Demonstration of Vibrations Signal Details
		B) Vibration Analysis Machine	Determination of Stiffness of a Spring, Determination of Inertia of Simple/ Compound Pendulum, Logarithmic Decrement in damped Vibrations.
		C) Whirling Speed of Shaft Apparatus	Experiment on study of Whirling Speed of Shafts.
		D) Set up for Balancing of Rotating Masses	Study of the balancing of rotating masses
		E) Gyroscope	Study of Gyroscopic Couple Effects

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	HT LAB	a) Heat Exchanger	Parallel Flow & Counter Flow Study & Perform
		B) Forced Convection Apparatus	Determination of HT Coefficient for Forced Convection

List of Experimental Setup in each Laboratory

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	TOM LAB	Determination of Inertia of Simple & Compound Pendulum	Vibration Analysis Machine
		Determination of Gyroscopic Couple	Motorized Gyroscope
		Experiment on free & Damped Vibrations of systems with one degree of freedom	Vibration Analysis Machine
		Experiment on Whirling Speed of Shaft	Apparatus for Whirling Speed of Shaft
		Experiment on Static & Dynamic Balancing of rotating masses	Balancing Apparatus

List of Experimental Setup in each Laboratory

SN	Name of the Laboratory	Name of the Experiments	Facilities in laboratory
1	HT LAB	Determination of Thermal Conductivity of Metal bar.	Metal bar. Apparatus
		Determination o f Thermal Conductivity of Insulating Powder.	Insulating powder.Apparatus
		Determination of Heat Transfer Co-efficient for forced convection.	Forced convection Apparatus
		Determination of Heat Transfer Co-efficient for Natural Convection.	Natural convection. Apparatus
		Verification of Stefan Boltzman law.	Stefan boltzman Apparatus
		Study of heat Exchanger	Heat Exchanger

Workshop Details

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Machine Shop	Center Lathe– 23nos Universal Milling Machine–03nos Shaping Machine - 03 nos Grinding machines – 05 nos RadialDrillingMachine–01nos	Convention almachining facilities
2	Welding Shop	3phase,Aircooled,WeldingTransformer- 01 Gaswel dingsetup–01 Single Phase Air Cooled Welding Transformer – 01 TIG/MIGWelding–01 Plasma Welding – 01 Spot welding – 01	Arc/Gas/Spot/Plasma welding
3	Foundry and Smithy Shop	Furnace – 01 Foundry vice 12	Casting facility
4	Carpentry and pattern making shop	Wood turning lathe–01	Wood turning
5	Fitting and Sheet metal shop	Bench drilling machine–01 Bench grinder – 01	Drilling,Grinding,Threading facilities

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Workshop (WP)	Preparation of given job in fitting shop	Fitting processes facilities
2		Preparation of given job in carpentry shop	Wood working facilities
3		Preparation of given job in welding shop	Arc/Gas/Spot/Plasma welding
4		Preparation of given job in blacks mithy shop	Forging facility
5		Preparation of given job in tapanddie shop	External and internal threading facility
6		Preparation of given job in sheet metal shop	Sheet metal operations facilities

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Workshop (MP)	Preparation of mouldin foundry shop	Mould preparation facility
2		Preparation of given job in pattern making shop	Wood turning and wood working facilities
3		Preparation of given job in welding shop	Arc/Gas/Spot/Plasma welding

SN	Name of the Laboratory	Name of the Major Equipments in each Laboratory	Facilities in laboratory
1	Workshop (MT)	Preparation of given job in machine shop	Machining like drilling, shaping, milling,turning,facing etc facilities

❖ **Department of Computer Science and Engineering**

- **List of Major Equipment / Facilities in each Laboratory/Workshop**

Sr. No.	Name of the Laboratory	Major Equipment available in Laboratory
1	Object Oriented Programming Laboratory	1) PC: (LENOVO i5 12th GN./8GB RAM /1 TB SATA HDD/512 GB M2 SSD: - Qty: - 17 With Windows 10 Operating System and Quick Heal Antivirus): - 18 Qty 2) 10 KVA Online UPS With Batteries: - 01 Qty 3) Scanner (HP G2410): - 01 Qty 4) External DVD Writer LG: - 01 Qty 5) Network Rack with Network Switches 24 Port: - 04 Qty and Control /Patch Panel: - Qty: -04 6) All Computers in the Laboratory equipment with Internet facility /White board.
2	System Software Laboratory	1) PC: (LENOVO i5 12th GN./8GB RAM /1 TB SATA HDD/512 GB M2 SSD With Windows 10 Operating System and Quick Heal Antivirus): - 18 Qty 2) All Computers in the Laboratory equipment with Internet facility /White board. 3) HP Lasejet (1020 plus) :- Qty 01
3	DBMS Laboratory	1) HP Server – 01 2) Computer System Qty (LENOVO I3 (3G) /4GB RAM /500GB HDD, With Windows 10 Operating System and Quick Heal Antivirus) – 20 Qty 3) KVA Online UPS with Batteries: - 01 Qty 4) All Computers in the Laboratory equipment with Internet facility / LCD Projector / White board.
4	Beginners Laboratory	1) PC HP-i5 HPPro28049MT,12th Gen.DDR4 8GBRAm, 512NVM2280 M2PC SSD, Bluetooth/Wi-fi,19.5" LED Monitor with Windows 11 Operating System and Quick Heal Antivirus -19 Qty 2) PC Lenovo Intel 481 Chip Set Mother Board, 4gb DDR3 RAM,500GB SATA SDD, USB Mouse, Keyboard, 18.5 Inch LED Monitor, With Windows 10 Operating System and Quick Heal Antivirus -3 Qty 3) UPS- 5KVA Online Digital (Numeric) 4) Network Rack with Network Switches 24 port: - 03 Qty Control /Patch Panel: -

		Qty: -03 5) All Computers in the Laboratory equipment with Internet facility /White board.
5	Data Science Laboratory	1) PC Lenovo- i3 6h generation Model 8510 Tower Desktop core i3Processor, 4GB DDR4 RAM ,500 GB SATA HDD, USB Mouse, Keyboard,19.5" Inch LED Monitor, Windows 10 Operating System, and Quick Heal Antivirus -16Qty 2) PC LENOVO -V520 PC i5-12 GEN processor,6core 2.4GHZ/single module 4GB DDR4 RAM,1TB SATA HDD, 7200RPM 512 M2SSD19.5" Inch LED Monitor, Windows 10 Operating System, and Quick Heal Antivirus -2 Qty 3) UPS- 3KVA Online Digital (Numeric) 4) All Computers in the Laboratory equipment with Internet facility /White board.
6	Machine Learning & IOT Laboratory	1) PC Lenovo i36h generation Model 8510 Tower Desktop core i3Processor, 4GB DDR4 RAM ,500 GB SATA HDD, USB Mouse, Keyboard,19.5" Inch LED Monitor with Windows 10 Operating System and Quick Heal Antivirus-7Qty 2) PC Lenovo Think center i3Intel 2GB RAM,500GB HDD,18.5" TFT Monitor, With Windows 10 Operating System and Quick Heal Antiviru-1Qty 3) ML -IOTRaspberry Pi3&Pi4 Kit, Arduino sensor Kit-Base, & various type of Relay 4) UPS -3KVA Online Digital (Numeric) 5) All Computers in the Laboratory equipment with Internet facility /White board.

- **List of Experimental Setup in each Laboratory/Workshop**

Laboratory Name: Object Oriented Programming
Subject: Data Structures (3CS204PC)

EXP No.	EXPERIMENT DESCRIPTION
1	Write a program to count how many positive, negative and zero values are present in an array.
2	Write a program to search for an element in an array using linear search.
3	Write a Program to insert an element at a specific position in an array.
4	Write a Program to delete from a specific position in an array.
5	To study and execute the Binary Search method.
6	To study and execute Bubble sort method.
7	To study and implement the insertion operation at the front of singly linkedlist.
8	Write a program to delete a last node from the linkedlist.
9	Understand the stack structure and execute the push, pop operation onit.
10	Understand the Queue structure and execute the insertion, deletion operation onit.
11	Understand the Queue structure and execute the deletion operation onit.
12	Understand the Tree structure and implement the Pre-order, In-order, post-order traversing operations on it.

Subject: Data Communication Network (4CS212PC)

EXP No.	EXPERIMENT DESCRIPTION
1	To study various LAN topologies and their creation using network devices, cables and computers.
2	To connect the computers in Local Area Network.
3	Familiarization with Networking Components and devices: LAN Adapters, Hubs, Switches, Routers etc.
4	Write a program of bit stuffing used by Data Link Layer.
5	Write a program to implement CRC (Cyclic Redundancy Check)
6	Write a program to implement Checksum.
7	Write a program to implement Sliding window.
8	Write a C Program to determine if the IP Address is in Class A, B, C, D, or E.

Subject: Software Engineering (6KS03)

EXP NO.	EXPERIMENT DESCRIPTION
1	To identify a suitable real-world case study and clearly define the problem statement.
2	To identify and document the functional and non-functional requirements of the selected case study.
3	To draw the use case diagram for the case study and write detailed specifications for each use case.
4	To identify analysis classes and design the class diagram for the case study.
5	To draw sequence diagrams representing interactions between system objects for selected use cases.
6	To draw activity diagrams to model the workflow and control flow of the system.
7	To draw state diagrams representing the states and state transitions of important system objects.
8	To draw the deployment diagram showing the physical architecture and deployment of software components.

Subject: Compiler Design (5KS07)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a C Program to Scan and Count the number of characters, words, blank spaces and lines in a given file. (Input File Name: abc.txt)
2	Write a C program to identify whether a given line is a comment or not.
3	Write a LEX Program to Scan and Count the number of characters, words, blank spaces and lines in a given file. (Input File Name: abc.txt)
4	Write a C Program to implement DFA. Shows a DFA recognizes the language represented by regular expression (a|b)*abb (First draw NFA and then convert into equivalent DFA and write code to simulate DFA)
5	Write a simple LEX program, that will count & Remove comments from a given text file "Text.c" and write into file "New.txt"
6	Study experiment of Lex tool in Compiler design.
7	Study experiment of Yacc tool in Compiler design.
8	Write a simple YACC program to implement and design a simple calculator to recognize the tokens defined by the given grammar. **E -> E + E | E * E | (E) | digit | id**

Subject: Machine Learning and Artificial Intelligence (8KS05))

EXP NO.	EXPERIMENT DESCRIPTION
1	To study and implement Python Libraries for ML applications such as Pandas, Numpy, and Matplotlib.
2	To study the procedure required for implementation of project.
3	To implement different data preprocessing techniques in ML.
4	Implementation of Logistic Regression using sklearn and its performance evaluation.
5	Implementation of KNN using sklearn and its performance evaluation.
6	Implementation of Decision Tree using sklearn and its performance evaluation.
7	Implementation of SVM using sklearn and its performance evaluation.
8	Implementation of Random Forest using sklearn and its performance evaluation.
9	Implementation of Linear Regression.
10	Implementation of end-to-end project using ML.

Laboratory Name: System Software
Subject: Object Oriented Programming (3CS203PCKS06)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a Java program to check whether the given integer number is odd or even.
2	Write a Java program to input 10 integer numbers and find their sum.
3	Write a Java program to demonstrate the declaration and scope of local, instance and static variables.
4	Write a Java program to sort elements of an array in ascending/descending order.
5	Write a Java program to demonstrate the concept of method overriding.
6	Write a menu driven Java program to demonstrate the different uses of super keyword in Inheritance.
7	Write a Java program to implement multiple inheritances using Interface.
8	Write a program to demonstrate the concept of defining and using user defined package in Java program.
9	Write exception handling Java program to demonstrate the concept of try with multiple catch.
10	Write a Java program to write a character, string, & array of characters into a file.
11	Write an Applet Java program to display various shapes in applet window
12	Write a program to display multiplication of two numbers using event handling concept in Java.

Subject: Operating System (4CS213PC)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study Linux Operating System along with its installation.
2	To simulate First Come First Serve process scheduling algorithm.
3	To simulate Preemptive Shortest Job First process scheduling algorithm.
4	To implement Round Robin Process scheduling Algorithm.
5	To implement Priority Based Process scheduling Algorithm.
6	To implement the FIFO page replacement policy.
7	To implement FCFS Disk Scheduling algorithm.
8	To implement Dining-Philosophers problem to deal with concurrency control mechanism.

Subject: EMERGING TECHNOLOGY LAB IV – Blockchain Fundamentals (7KS08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study of Blockchain Principles Including Types of Blockchain, Centralized vs Decentralized Systems, Cryptography, and Consensus Mechanisms, along with Setup of Blockchain Environment on Your M/C by Installing Node.js, VS Code, Ganache, Truffle, Hardhat, MetaMask, Geth, and Infura.
2	Write a program that implements a web-based an interactive blockchain mining visualizer to simulate block creation, proof-of-work, difficulty adjustment, and SHA-256 hash analysis. Also analyse how difficulty level impacts mining time and energy consumption
3	Write a Simple Web-Based Program to Implement RSA Public Key Encryption and Private Key Decryption Using the Web Crypto API.
4	Exploring Consensus Mechanisms **Implementation and Comparison**: Develop and compare various consensus mechanisms, such as Proof of Work (PoW) and Proof of Stake (PoS).**Performance and Security Analysis**: Evaluate the performance and security characteristics of each consensus mechanism and compare the performance of both mechanism.
5	Exploration of Remix IDE, Solidity Programming, Wallet Types, Blockchain Use Cases, and Public vs. Private Blockchains. Demonstrate the Programming Structure of Solidity with Syntax and Semantics. Write a Solidity Program to 1. Perform Arithmetic Operations. 2. Perform Looping Statements 3. Implement Cryptographic Functions. 4. Implement Smart Contract creation 5. Implement Restricted Access to a Contract.
6	Write a Simple Program to Understand RLP Encoding of Ethereum Transactions.
7	Write a Simple Smart Contract to Transfer Ether Between Accounts, Then Compile, Deploy, and Debug It Using Ganache, MetaMask, and Truffle.
8	Write a simple program to build a web-based application that simulates a blockchain, constructs Merkle Trees for transactions in each block, and demonstrates how tampering any transaction affects the blockchain integrity.

Subject: EMERGING TECHNOLOGY LAB VI – DLT (8KS06)

EXP NO.	EXPERIMENT DESCRIPTION
1	Studying Distributed Ledger Technology and its related sub-techniques, along with installing the LTS version of Ubuntu O.S. on Virtual Box in Windows 10 for virtualization.
2	Implementation and Analysis of SHA-256 Hash Functions: Characteristics and Output Patterns. Determining Plaintext from a SHA-256 Hash Using Brute Force and Dictionary Methods
3	Implementing Proof of Work with Dynamic Difficulty Levels in a Blockchain System Write a JavaScript program that performs the following tasks: **Finds a Valid Nonce**: Identifies a valid nonce and its corresponding hash for a block that satisfies difficulty levels ranging from 1 to 10. **Meets Difficulty Requirements**: Ensures the hash starts with a number of leading zeros equal to the difficulty level. **Computational Time Measurement**: Calculates and displays the time taken to find the valid nonce for each difficulty level.
4	Implement and demonstrate the concept of **key splitting** and **secure sharing** using **Shamir's Secret Sharing (SSS)** algorithm
5	Implementation and Validation of Digital Signatures Using Asymmetric Cryptography with Public and Private Key Pairs
6	Develop a Bitcoin Script Simulator with Lock Time Verification for Enhancing Transaction Security and Blockchain Integrity
7	Installation of Prerequisites for Hyperledger Fabric: cURL, Node.js, NPM, Go, Git, Python, and Libtool, Docker and Docker Compose, Followed by Hyperledger Fabric Network Configuration.
8	Constructing the initial Hyperledger Fabric network and deploying the chaincode (smart contract) onto this network

Subject: EMERGING TECHNOLOGY LAB V – SSS (8KS05)

EXP NO.	EXPERIMENT DESCRIPTION
01	Installation of Kali Linux Penetration Testing Linux distribution on virtual box.
02	Study and demonstrate following networking commands on Kali Linux:(i)ping (ii) traceroute (iii) unzip (iv) chmod (v) sudo(vi) passwd (vii) ps (viii) grep (ix) echo
03	Study about Wires harktool (What isWireshark? Wireshark features, who use wireshark? etc.).
04	Demonstrate packet capturing using Wireshark tool.
05	Demonstrate the following packet analyzing task on captured packet file using Wireshark (Challenge1.pcap): Findout what HOST is using in pcapfile? What other information you can get from same pcap file about host?
06	Demonstrate the following packet ana lyzing task on another captured packet file(c1.pcap): How many ping requests weresent in the c1.pcapcapture? What is the IP address of device associated With 08:00:27: 4b:e3:60?
07	Demonstrate the following packet analyzing task soncaptured packet file (c1.pcap). What version of IGMP (Internet Group Management Protocol) is in use? What is the name of the host located at 10.0.2.22? What is the name of the host located at 10.0.2.15?
08	Demonstrate the following packet analyzing task on captured packet file (c1.pcap).What is the IP address of the attacker?

Laboratory Name: Database Management System
Subject: C-Skill-Lab I (3KS09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write python program to store data in list and print list of numbers.
2	Write a program to create, concatenate and print a string and accessing sub-string from a given string.
3	Write a program to create, append, and remove lists in python.
4	Write a program to demonstrate working with tuples in python.
5	Write a program to demonstrate working with set in python.
6	Write a program to demonstrate working with dictionaries in python.
7	Write a python program to find largest of three numbers.
8	Write a python program to find factorial of a number using function.
9	Write python program in which a class is define, then create object of that class and call simple print function define in class.
10	Write a python program to sort a list of tuples using Lambda.

Subject: DBMS (5KS06)

EXP NO.	EXPERIMENT DESCRIPTION
1	Introduction to Database System, Structured Query Language (SQL), installation procedure of MySQL on Ubuntu and also write SQL queries to implement Data Definition Language commands.
2	Write SQL queries to implement Data Manipulation Language commands
3	Perform the operations based on select command using where, group by, order by and having clause to retrieve data from database.
4	Perform basic operations using arithmetic, comparison, and logical operators.
5	Perform the operation to combine rows from two or more tables, based on a related column between them using JOIN clause.
6	Create the database Trigger in SQL.
7	Implement Transaction Operations (Commit, Rollback, and Savepoint)
8	Implement lock-based protocols for concurrency control.
9	Develop an application to retrieve the information by connecting to the database using a host language (JAVA, C, C++).

Subject: Data Science & Statistics (5KS08)

EXP NO.	EXPERIMENT DESCRIPTION
1	To explore and understand the functionality of various essential libraries in Python for different applications.
2	To understand and implement variable creation, different data types (Numbers, Strings, Lists, Arrays, Tuples, Dictionaries, Sets, Range), associated operations, arithmetic and logical operators, loops, and conditional statements in Python
3	To understand and implement Simple Linear Regression and Multiple Linear Regression models using Python.
4	To understand and implement the Logistic Regression classification algorithm using Python.
5	To understand and implement the K-Nearest Neighbours (KNN) classification technique using Python.
6	To understand and implement Decision Tree algorithms for both classification and regression tasks using Python.
7	To understand and implement the Support Vector Machine (SVM) classification technique using Python.
8	To understand and implement the K-Means Clustering algorithm using Python.

Laboratory Name: Project Lab

Subject: COMPUTER GRAPHICS (7KS06)

EXP NO.	EXPERIMENT DESCRIPTION
01	Write a program to draw line using DDA algorithm.
02	Write a program to draw circle using Bresenham‘s algorithm.
03	Write a program for 2-D transformations, a) Scaling b) Translation c) Rotation
04	Write a program for 3-D transformations, a) Scaling b) Translation c) Rotation
05	Write program to fill polygon using scan line algorithm.
06	Write a program to draw line using Bresenham‘s algorithm
07	Write a program to draw following type of curve-Hilbert’s Curve.
08	Write a program of man walking in rain
09	Write a program to fill colour in rectangle.
10	Write a program to draw a house
11	Write a graphics program analog clock.
12	Write a program for moving a cycle

Subject: Data Warehouse and Mining (7KS07)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study of WEKA as a data mining tool.
2	Write a program to convert data set in to attribute relation file format using WEKA tool.
3	Write a program to apply Pre-Processing techniques to the training data set in WEKA.
4	Write a program to apply Pre-Processing techniques for missing values in WEKA.
5	Write a program to apply classification technique for given data set in WEKA
6	Write a program to apply clustering technique for given data set using WEKATool
7	Write a program to find association rule in a given data set using WEKA tool.
8	Study Data Cube or OLAP approach in data mining

Subject: C-Skill-Lab-IV (6KS09)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study different Linux Commands.
2	Study practical on installation of java, Tom cat Server.
3	Study practical on software development life cycle.
4	To study practic alon Dev Ops life cycle & stages
5	To setup and configure of Jenkins.
6	To create Job and manage it using Jenkins
7	To experiment plug in management with Jenkins
8	To study and demonstrate User role creation and management using Jenkins.

Subject: Design And Analysis of Algorithms (DAA) (6KS06)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study various algorithm designing strategies.
2	Write a program to sort numbers using bubble sort technique and analyze its time complexity.
3	Write a program to implement merge sort using divide and conquer technique and analyze its time complexity.
4	Write a program to implement knapsack problem using greedy method.
5	Write a program to implement Prim’s algorithm using greedy method.
6	Write a program to implement traveling salesman problem using dynamic programming.
7	Write a program to implement search and traversal using backtracking approach.
8	To study polynomial time and non-polynomial time algorithms.

Laboratory Name: Beginners Lab
Subject: COMPUTER PROGRAMMING (CP) (1AL105ES)

EXP NO.	EXPERIMENT DESCRIPTION
1	**To Study of Fundamental of the Computer and Computing Concepts.** a. Basic anatomy of computer system (Block Diagram). b. Types of software c. Various Networks and Topologies
2	Write a program to convert a given distance in kilometers to meters using function.
3	Write a program to find the smallest of three numbers using conditional operator.
4	Write a program to create a calculator using switch-case statement.
5	Write a program to find the average of first N natural numbers using looping statements.
6	Write a program to find largest element in an array and print its location.
7	Write a program to perform various operations on string using string library functions.
8	Write a program to create the array of structure of books having following fields i) Book ID ii) Book Name iii) Author Name iv) Cost of book After this display information of all books and total cost of the books on the screen.
9	Write a program to print the values and addresses of the variables in a program using pointers.
10	Write a program to copy contents of one file to another file using file handling concept.

Subject: C SKILL LAB –III (5KS09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Introduction to Node.js **Install Node.js. Write a basic program to print "Hello from Node.js!" to console.**
2	Using Node.js as a Web Server **Create a basic HTTP server that sends a welcome message to the browser.**
3	Working with Built-in Modules **Demonstrate the use of fs and os modules in Node.js.**
4	**Creating Custom Modules** Create and use a custom module that performs a mathematical operation.
5	**Handling URLs and File System** Parse a URL and return a file's content. Handle 404 errors gracefully.
6	**Using External NPM Packages** Install and use the upper-case NPM package to transform text.
7	**Event Handling in Node.js** Use the events module and EventEmitter to demonstrate event handling.
8	**Handling File Uploads** Use the formidable module to upload files to the server.
9	**Sending Emails with Node.js** Use the nodemailer module to send emails from a Node.js server.
10	**MySQL Database Connection** Install MySQL driver, establish a connection using Node.js.
11	**Creating a Database and Table:** Create a MySQL database and a table using Node.js code.
12	**Inserting Records into MySQL** Insert single and multiple rows into a MySQL table via Node.js.
13	**Email Sender App** Build a functional email sending app using nodemailer with user input.
14	**Basic User Management Web App** Create a simple web app for user sign-up/login and profile viewing using Express.js and MySQL.

Laboratory Name: Data Science
Subject: Emerging Technology LAB-ICS (5KS08)

<table>
<tr><th>EXP NO.</th><th>EXPERIMENT DESCRIPTION</th></tr>
<tr><td>1</td><td>StStudy of Cybercrime Lifecycle and Its Phases.</td></tr>
<tr><td>2</td><td>Demonstrate target’s information gathering using websites (min. 6) such as
https://hexillion.com/co
http://dnscheck.tool
http://cqcounter.com
https://www.virustotal.com
https://haveibeenpwned.com
https://who.is/whois/domainname
https://www.yougetsignal.com</td></tr>
<tr><td>3</td><td>Demonstrate target’s information gathering by using some advanced google operators (min. 6)such as
<table>
<tr><td rowspan="3">Advanced Operators</td><td>i) filetype</td><td>ii) allintitle</td><td>iii) inurl</td></tr>
<tr><td>iv) site</td><td>v) phonebook</td><td>vi) related</td></tr>
<tr><td>vii)intitle</td><td>viii)related:url</td><td>viii) define</td></tr>
</table></td></tr>
<tr><td>4</td><td>Demonstrate hiding secrete information using steganographic tool (Like Xiao) and without using steganographic tool (copy /b abc.rar + pqr.jpg xyz.jpg).</td></tr>
<tr><td>5</td><td>Setup virtual machine and installation of Kali Linux on windows O.S.</td></tr>
<tr><td>6</td><td>Demonstrate various Kali Linux commands (min. 10).</td></tr>
<tr><td>7</td><td>Demonstrate phishing attacks using social engineering toolkit (setoolkit).</td></tr>
<tr><td>8</td><td>Demonstrate the use of Nmap tool.</td></tr>
</table>

Subject: Digital Forensic LAB (7KS09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study of Computer Forensics and different tools used for forensic investigation
2	How to Recover Deleted Files using Forensics Tools
3	Study the steps for hiding and extract any text file behind an image file/ Audio file using Command Prompt.
4	How to Extract Exchangeable image file format (EXIF) Data from Image Files using Exifreader Software
5	How to make the forensic image of the hard drive using EnCase Forensics.
6	How to Restoring the Evidence Image using EnCase Forensics
7	How to Collect Email Evidence in Victim PC
8	How to Extracting Browser Artifacts
9	How to View Last Activity of Your PC
10	Find Last Connected USB on your system (USB Forensics)
11	Comparison of two Files for forensics investigation by Compare IT software
12	Live Forensics Case Investigation using Autopsy

Subject: ET-II- Cryptography (6KS06)

EXP NO.	EXPERIMENT DESCRIPTION
1	Implement **"Caesar cipher"** substitution technique of cryptography (C/ C++/ Java/Python).
2	Implement a **"rail-fence cipher"** transposition technique of cryptography (C/ C++/ Java/Python).
3	Demonstrate Installation and configuration of Add-on extensions for browsers like Mozilla Firefox, Google chrome (e.g. Ad Guard **Ad Blocker**, u Block Origin, Privacy Badger etc, implementation of any 3).
4	De Demonstrate Creation of tiny URLs using various tools like (i) Tiny URL (ii) **Check Short Url** **(iii**) trib.al etc
5	In Install Vera Crypt tool and demonstrate encryption and decryption of file.
6	De Demonstrate Encryption and Decryption of drive using VeraCrypt tool.
7	In Install Jcrypt tool and demonstrate Encryption and decryption using Symmetric key Cryptographic Algorithm.
8	Use Jcrypt tool to demonstrate Encryption and Decryption using Asymmetric key Cryptographic Algorithm.
9	Identify tools and demonstrate Hash Algorithm (MD-5, SHA-256), Digital Signature Algorithm (DSA) using tool.
10	Study the features of firewall in providing network security and to set firewall security in windows.

Subject: BIG DATA ANALYTICS (PE-II) (6KS08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study Python Basics along with environmental Setup.
2	WAP to apply Python libraries including Scipy, Pandas, Numpy and Matplotlib.
3	WAP to process Data in using CSV, JSON and xls files.
4	WAP to Visualize data using any plotting framework.
5	WAP to analyze statistical data by measuring central tendency and variance.
6	WAP to calculate P value for Hypothesis.
7	WAP to implement Linear Regression using Seaborn Library.
8	*Study of Hadoop architecture including HDFS, YARN and MAPREDUCE*.

Subject: Community Engagement Project/Field Project (CEP) 3CS400EL

EXP NO.	EXPERIMENT DESCRIPTION
1	**DiDigital Empowerment & Awareness** Bridging the Digital Divideb.Smart Village Initiative
2	**SSSmart Agriculture & Technology Adoption** **a.** Tech-Driven Farming Solutions **b.** Sustainable Agricultural Practices
3	**WWater & Resource Management** **a.**Water Conservation & Management b.Energy-Efficient Practices
4	**E-Commerce & Rural Entrepreneurship Development** **a.**Market Linkage & Skill Development b. Rural Women Empowerment
5	**HeWealth& Hygiene Awareness** **a.** Basic Healthcare Services b. Sanitation & Waste Management
6	**EdEducation& Skill Development** **a.** STEM Awareness Programs b. Career & Competitive Exam Guidance
7	**CyCyber Security & Online Safety** **a.**Safe Digital Practices
8	**ReRenewable Energy & Sustainability** **a.** Eco-Friendly Energy Solutions **b.** Green Village Initiative
9	**WWaste Management & Circular Economy** **a.** Plastic-Free Village Campaign **b.** Low-Cost Housing Solutions
10	**SSSmart Infrastructure & Community Development** **a.** Smart Village Concept b.Community-Driven Innovation

Subject: C-Skill Lab-III (5KS09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study Node.js as an open-source server environment.
2	Write a program to print simple messages through node.js
3	Study HTTP module and working of Node.js as a web server
4	Write a program to demonstrate built-in module in Node.js
5	Write a program to construct custom module in Node.js
6	Write a program to underhand the working of file system.
7	Write a program that opens the requested file and returns the content of the file to the client. If anything goes wrong, throw a 404 error.
8	Write a program convert the output into upper-case letters by installing the “upper-case” package of NPM.

❖ **Department of Information Technology**

- **List of Major Equipment / Facilities in each Laboratory/Workshop**

Sr. No.	Laboratory	Available Resources
01	Data Engineering Laboratory	**1. Computer Systems** – 18 Nos.: HP Computer Systems (PCS510 Tower Model, Gen6) with Intel Core i5, 8 GB RAM, 500 GB SATA HDD, 20" HP Monitor, USB Wired Keyboard and Optical Mouse. **2. NVIDIA Tesla GPU Cards** **3. DICA Kits** **4. XPO 8086 Microprocessor Kits** with Study Cards
02	Programming Laboratory	**1. Computer Systems** (Lenovo) – 12 Nos.: Lenovo Computer Systems (PCS510 Tower Model, Gen6 H110) with Intel Core i3, 4 GB RAM, 500 GB SATA HDD, 19.5" LED Lenovo Monitor, USB Wired Keyboard and Optical Mouse. **2. Computer Systems** (HP) – 07 Nos.: HP Computer Systems (PCS510 Tower Model, Gen6 H110) with Intel Core i5, 8 GB RAM, 500 GB SATA HDD, 20" HP Monitor, USB Wired Keyboard and Optical Mouse. **3. Oasis Embedded System Kits** – 08 Nos. **4. ARM7 Boards with Software** – 07 Nos.
03	Web Tech Laboratory	**1. Computer Systems –** 17 Nos.: **HP 280 G1 Business Desktop with** Intel Core i3-4160 (3.6 GHz), 4 GB DDR3 RAM, 500 GB HDD, HP-Compaq 18" Monitors, USB Keyboard and Mouse.
04	Artificial Intelligence Laboratory	**1. Computer Systems –** 16 Nos.: HP 280 G1 Business Desktop with **Intel Core i3-4160 (3.6 GHz), 4 GB DDR3 RAM, 500 GB HDD, HP-Compaq 18" Monitors, USB Keyboard and Mouse.**
05	Operating System Laboratory	**1. Computer Systems –** 15 Nos.: Lenovo ThinkCentre M72 Series with **Intel Core i3-2120 (2nd Generation), Intel Motherboard, 2 GB DDR3 RAM, 500 GB HDD (7200 RPM), DVD RW Drive, PCI / PCI-E Slots, Ower Cabinet (4×3), 18.5" TFT Monitor, USB Keyboard and Mouse.**
06	Project Laboratory	**1.** Computer Systems **– 10 Nos.: Lenovo Computer Systems (PCS510 Tower Model, Gen6 H110) with Intel Core i3, 4 GB RAM, 00 GB SATA HDD, 19.5" LED Lenovo Monitor, USB Wired Keyboard and Optical Mouse.**

- **List of Experimental Setup in each Laboratory/Workshop**

Laboratory Name: Data Engineering Laboratory
PE-I: Data Science & Statistics (5IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Installation of Anaconda and Jupyter Notebook and implementing basic code of python.
2	Study and Implement of NumPy Library Functions
3	Study and Implementation of Pandas Library Functions in Python
4	Study and implantation of null value analysis on Data Frame
5	Study and Implementation of plots using MatplotLib for data analysis and statistical tests.
6	Performance of exploratory data analysis on DataFrame.
7	To perform Linear Regression Model and Evaluate its Performance.
8	To perform Logistic Regression for Classification and Evaluate its Performance.
9	To study and implement Decision tree algorithm and Evaluate its performance

Subject: Data Structure (4IT212PC)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a program to create an array and perform basic operations such as insertion, deletion, and displaying elements
2	Write a program to search an element in an array using searching methods.
3	Write a program to create a singly linked list and perform traversal operations.
4	Write a program to perform insertion and deletion operations on a singly linked list.
5	Write a program to implement stack operations such as push and pop
6	Write a program to implement queue operations such as insertion and deletion.
7	Write a program to create a binary tree and perform pre-order, in-order, and post-order traversal operations.
8	Write a program to implement selection sort to sort array elements.
9	Understand the concept of Recursion and write a program to calculate factorial of a number using Recursion.
10	To implement a **Hash Table** and perform insertion, deletion, and searching operations.

Subject: PE-IV: Business Intelligence (7IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	To understand Business Intelligence and explore Power BI through installation and interface overview.
2	To perform data pre-processing, including data cleaning, transformation, and preparation using Power BI
3	To perform ETL in Power BI by importing data, cleaning and transforming it, and loading it into the data model for analysis.
4	To perform data modelling in Power BI by creating relationships between tables for efficient analysis
5	To study and apply different visualization in Power BI for effective data presentation and analysis
6	Case Study: Google Analytics
7	Case Study: How Business Intelligence is Helping Companies
8	Mini Project: To design and develop a Power BI dashboard

Subject: Compiler Design (6IT01)

EXP NO.	EXPERIMENT DESCRIPTION
1	Design a C program to test whether a given identifier is valid or not.
2	Design a C program to simulate a lexical analyzer for validating operators.
3	Design a C program to identify whether a given line is a comment or not.
4	Design a C program to recognize strings under 'a*', 'a*b+', 'abb'.
5	Design a LEX Program to scan reserved words and Identifiers of C Language.
6	Design a Predictive Parser for the given language.
7	Design a LEX Program to convert the substring ‘abc’ to ‘ABC’ from the given input string.
8	Design a C program to generate three address codes.
9	Design a C program to implement Program semantic rules to calculate the expression that takes an expression with digits, + and * and computes the value.

Laboratory Name: Programming Laboratory
Subject: Data Communication & Networking (4IT213PC)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study various Computer Network Topologies and Transmission Media
2	To study Conversion of Digital to Digital, Analog to Digital and Digital to Analog Techniques.
3	Design & implement a Local Area Network (LAN) that connect multiple computers and share files between connected computers.
4	Execute TCP/IP network commands: ipconfig, ping, tracert,netstat, pathping, route
5	To Install Wireshark Network Analyzer tool and inspect its basic operation.
6	To study and Implement analyzing data packets using Wireshark Network analyzer.
7	To install & implement Ping Test Utility Tool.
8	Use FTPprotocoltotransferfilefromonesystemto another system
9	To study Network IP and IP Addressing Techniques.
10	Create HTTPserver

Subject: PE-I: Information Security System (5IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Demonstration of Confidentiality,Integrity and Availability (CIA Triad)
2	Explore the security features of Windows OS.
3	To simulate a password guessing attack (brute force/dictionary) using tools (like John the Ripper) to demonstrate the importance of strong passwords.
4	Implementing encryption and decryption(using simple shift cipher).
5	To demonstrate how the Caesar (shift) cipher can be broken by a brute-force attack (trying all possible shifts) and to show a basic automated method to select the most likely plain-text using English letter frequency scoring.
6	To demonstrate how attackers can hide sensitive data inside using Stenography tool.
7	To study working of ransomware attacks, their impact on organizations, and countermeasures.
8	To analyze a real-world cyber incident focusing on legal/ethical aspects and risk management strategies, and to understand how organizations respond to such challenges.

Subject: PE-II: Big Data Analytics (6IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	To setup the necessary environment for experiments in R.
2	To perform data analysis using MS-Excel.
3	To declare variable, expression, function and execute R script.
4	To create list and perform various operations on it.
5	To manipulate & process data in R.
6	To perform data visualization in R.
7	To perform file handling in R.
8	To Implement a big data mining algorithm in R.

Subject: PE-III: Machine Learning (7IT07)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a program in python to implement Perceptron Algorithm using Scikit-learn.
2	Write a program in python to implement Logistic Regression on Titanic Dataset.
3	Write a program in python to Handle Missing and Categorical Data using Pandas &Scikit-learn.
4	Write a program in python for Feature Scaling and Train-Test Split on a dataset.
5	Write a program in python to implement Simple Linear Regression on Boston Housing Dataset.
6	Write a program in python to implement K-Means Clustering on Iris Dataset.
7	Write a program in python to implement Random Forest Classifier on Nonlinear Data.
8	Write a program in python to implement Train a Neural Network to Classify Handwritten Digits (MNIST).

Laboratory Name: AI Laboratory
Subject: Computer Skills-I (4IT215VS)

EXP NO.	EXPERIMENT DESCRIPTION
1	To Study Internet Architecture and Web Standards
2	To Study and Implement HTML Text Formatting, Lists, and Data Representation
3	To Study and Implement Hyperlinks, Images, and Multimedia in Web Pages
4	Web Page Styling Using Cascading Style Sheets (CSS)
5	Design of Responsive Web Layout Using CSS Flexbox and Media Queries
6	To Study and Implement JavaScript for Client-Side Interactivity and Form Validation
7	Server-Side Web Programming Using PHP
8	Simple Login Form using PHP & Sessions
9	Case Study on Design and Development of a Web-Based Application
10	Mini Project: Design and Development of a Personal Portfolio Website

Database Management System (5IT06)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study different database systems
2	To study DDL commands in SQL
3	To study DML commands in SQL
4	To write basic SQL Select statements
5	To display data from multiple tables
6	To aggregate data using group functions
7	To study different types of subqueries
8	To study different constraints in SQL
9	To study different operations on views

Subject: PE-II: Cryptography & Network Security (6IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Explore & download various security tools which are available on Internet
2	Write a program to implement Caesar Cipher.
3	Write a program to implement RSA algorithm with suitable example.
4	Write a program to generate has hands imulate a simple digital Signature processusing SHA-256.
5	Create a table to identify potential security and vulnerabilities for each OSI model layer (e.g. Sniffing at the network layer,Session hijack in gatthe session layer)
6	Encrypta file using tool slike GPG or Python's Cryptography Library to ensure only authorize duser scan accessit.
7	Study and explore activation of fire wall on the system & their Setting
8	To study and under stand how to use scanning tools (AngryIP, Hping,IPScanner)

Subject: PE-IV: Block chain Fundamental (7IT08)

EXP NO.	EXPERIMENT DESCRIPTION
1	Study of Cryptographic Primitives Part I Implementing RSA Algorithm for Encryption and Decryption
2	Write a program to generate hash and simulate a simple digital signature process using SHA-256.
3	Visual demonstration of Block chain
4	Write a program to create chain of at least 3 block containing one Genesis blocks & all others blocks are connected by hash function.
5	Demonstration of Metamasks.
6	Compilation & deployment of Smart Contract in Solidity using Remix IDE
7	Checking SepoliaTestnet Balance with Node.js
8	Study and Execution of Alternative Blockchain Platform – Kadena

Laboratory Name: WEBTECH Laboratory

Subject: Object Oriented Programming (3IT203PC)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a Java program to demonstrate following concepts: A] Data declaration B] Data initialization C] Data conversion
2	Write Java Program to find area of circle using the concept of class, object and method
3	Write Java program to demonstrate this keyword
4	Write Java program to implement multiple inheritance using interfaces
5	Write a Java program to demonstrate access modifiers and user defined packages
6	Write Java program to demonstrate following of operation on file : A] Creating a new file B] Writing data to a file C] Reading data from file D] Deleting a file
7	Write a simple Java applet to draw a circle on every corner of a square
8	Write a Java program to draw a circle every time when the mouse is clicked.
9	To develop a Java program that uses multithreading to concurrently compute the values of `sin(x)`, `cos(x)`, and `tan(x)` for a given angle `x`, and then calculate their sum.
10	To develop a Java GUI application using **Swing** that takes two numbers as input from the user and displays their sum when a button is clicked.
11	Mini Project

Subject: Computer Skill Lab-III (5IT09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Install Angular CLI and Create First Angular Application
2	Create, Use and Navigate Components life cycle event
3	Variables and Data binding
4	Working with Signal's in Angular
5	Working with angular Directives.
6	Control flow statements in Angular
7	Template-Driven Forms in Angular
8	Reactive Forms in Angular

Subject: Computer Skill Lab IV (6IT09)

EXP NO.	EXPERIMENT DESCRIPTION
1	Write a Program to simulate a simple intelligent reflex agent operating in a vacuum cleaner environment of two rooms (A and B)
2	Write a Program to implement Depth First Search using Python
3	Write a Program to implement Best First Search using Python.
4	Write a program to implement A* Algorithm using Python
5	Write a program to implement Informed Search Technique: AO* Algorithm using Python
6	Write a program to implement Minimax Algorithm using Python
7	Write a program to Implement a Text Classifier for Spam Vs Not-Spam Using Naïve Bayes Classifier
8	Write a program to perform various natural language processing tasks using NLTK library in python

Subject: PE-V: Virtual & Augmented Reality (8IT06)

EXP NO.	EXPERIMENT DESCRIPTION
1	Installation and set up of Visual Studio 2022 Community Edition installation on Windows 10 and FREEGLUT.
2	Executing the first opengl triangle using freeglut.
3	Write a program in GLUT to display a circle.
4	Write a program in GLUT to display a 3D object (e.g., a sphere or cube) and apply basic transformations (translation, rotation, scaling).
5	Write a program in GLUT to track and display mouse movements to manipulate a 3D object (e.g., rotate a cube).
6	Use GLUT to create gesture-based interaction, such as drawing lines or shapes using mouse clicks.
7	Exploring virtual reality by immersive 360° environments using Google cardboard.
8	Install Blender and Create a basic 3D scene with objects like a cube, sphere, and plane, then render.

Laboratory Name: Operating System Laboratory

Subject: Software Engineering (5IT07)

EXP NO.	EXPERIMENT DESCRIPTION
1	To Study the SDLC Process & Models
2	Prepare broad SRS (Software Specification requirement) for the selected problem statement.
3	Estimation of Project matrix and design Gnatt chart
4	Design Graphical user interface of selected problem statement.
5	Prepare use cases and draw use case diagram using modeling tools
6	Draw E-R diagram, data flow diagram and sequence diagram for problem statement.
7	Write tests cases to validate requirements of problem statement from SRS document.
8	Exploring Bugzilla and Jira for Bug Tracking and Issue Management.
9	Write a Selenium script to automate login, search, and logout on a demo site (e.g. OrangeHRM).

Subject: Design & Analysis of Algorithm (6IT02)

EXP NO.	EXPERIMENT DESCRIPTION
1	To study various algorithm designing strategies
2	To study and implement effective bubble sort algorithm
3	To study and Implement divide and conquer technique
4	To study and implement merge sort Algorithm
5	To study and Implement Knapsack problem using greedy method
6	To study and Implement travelling salesman problem using dynamic programming.
7	To study and Implement Shortest Path Algorithm
8	To study implement 8 Queens Problem

Subject: Object Oriented Analysis & Design (8IT05)

EXP NO.	EXPERIMENT DESCRIPTION
1	Identifying the requirements from problem Statements
2	Modeling UML Use Case Diagrams and Capturing Use Case Scenarios
3	To draw state chart and activity modeling
4	Modeling UML class diagrams and sequence diagrams
5	Draw component diagrams assuming that you will build your System reusing existing components along with a few new ones.
6	To draw the diagrams [use case, activity, sequence, Collaboration, class for the E-ticket Booking system.
7	To study of adapter design pattern
8	Preparing class diagram on Hospital and ATM Example

Laboratory Name: Project Laboratory

Subject: Community Engagement Project (3IT400EL)

EXP NO.	EXPERIMENT DESCRIPTION
1	Demonstration of Arduino Uno development board, various sensors, and installation of Arduino IDE software.
2	Write and upload a simple program using the C language on an Arduino Uno board to display "Hello" over the Serial Monitor.
3	Write and upload a basic LED blinking program using the C language on an Arduino Uno board.
4	Write and upload a basic C language code on an Arduino Uno board for interfacing Push Button with LED.
5	Demonstrate OLED Display with Arduino Uno for Text and Sensor Data Visualization.
6	Demonstrate DHT11 Temperature and Humidity Sensor with Arduino Uno.
7	Demonstration of Semaphore for Serial Monitor Access Control using Arduino
8	Demonstration of Inter-Process Communication (IPC) Simulation using Arduino and DHT11 Sensor.

Department of Applied Sciences and Humanities

- **List of Major Equipment / Facilities in each Laboratory/Workshop**

Sr. No.	Name of the Laboratory	Major Equipment available in Laboratory
1	**Engineering Physics Laboratory**	Cathod Ray Oscilloscope , Function generator, Laser Kit , Franck Hertz Experiment , Hall effect setup , Newton's ring apparatus, e/m by Bar Magnet ,Fire Extinguisher - 6Kg(DCP) Computer System- HCL - Intel (R) Pentium(R) 4 CPU 3.20GHz 3.20GHz RAM – 2.50 GB 32- bit operating system
2	**Engineering Chemistry Laboratory**	Muffle Furnace, Electric Oven, Distillation Assembly, Redwood Viscometers, Pensky Marten apparatus, Digital Balance, PH Meter. TDS Meter. Fire Extinguisher - 6 Kg (ABC), Fire safety stand with sand bucket Computer System – Lenovo – Intel (R) Pentium(R) D CPU 2.80GHz 2.79GHz RAM – 3 GB 32- bit operating system, Refrigerator. Three Exust Fans
3	**Language Laboratory**	Projector Optoma W400lve with standard accessories and as per po specification Q7d7243waaa1b066 (issued on date 17-01-2023)=rs 42240/-
		5 KVA UPS with DC Powerpack Sr.no.V140700206 Po.no. 2100002458 (Issued dated 05.09.2014) 3yrs warranty,01 set=Rs.91011/-
		23 Lenovo thinkcentre M 73 with i3-4310-4th generation desktop PC as per the following specification: Intel core i3-4130 processor-4th generation3.44 Hz (3M Cache,65 W) Intel H81 chipset, Single 4GB RAM,1600 mhz UDIMM,500GB,7200 RAM SATA3 HDD, INTEL HD Graphics, Gigabit Ethernets Kld, USB optical scroll mouse,18.5" inch LED Monitor, no preloaded OS,3 yrs onsite comprehensive warranty.(issued in 2014) Posting 100000590 23 sets, each cost rs.34,150/-
		24 HP make headphones with mic with std. Accessories rs.600 each. (issued in 2024 Jan.)

- **List of Experimental Setup in each Laboratory/Workshop**
  Engineering Chemistry

EXP No.	EXPERIMENT DESCCRIPTION
1	Determination of alkalinity of water sample in the given Alkali Mixture w.r.t. NaOH Na_2CO_3 and $NaHCO_3$.
2	Determination of hardness of water ample by EDTA method.
3	Determination of Dissolved Chlorine (Cl_2) in given water sample (Iodometry).
4	Determination of Dissolved Oxygen in water sample.
5	Determination of Acid value of Lubricating oil.
6	To carry out the proximate analysis of coal.
7	Determination of viscosity of given Lubricating oil at different temperatures by Redwood Viscometer No.1/2.
8	Determination of Flash point of Lubricating oil by Pensky-Marten's apparatus.

Engineering Physics

EXP No.	EXPERIMENT DESCCRIPTION
1	Study of Hall Effect
2	Study of Semiconductor Diode
3	Study of the Characteristics of Zener Diode
4	Amplitude and Frequency Measurement of AC Voltage using CRO
5	Study of Light Emitting Diode
6	Energy Band Gap of Semiconductor Material by using Reverse Biased P-N Junction Diode
7	Diffraction due to Plane Transmission Grating
8	Study of Photo Cell.

English Language Lab

EXP No.	EXPERIMENT DESCCRIPTION
1	Subject Introduction (C.Skills)
2	Extempore Speech Practice
3	Presentation/Seminar Practice
4	Grammar Practice
5	Group Discussion Practice
6	Debate Practice
7	Job Interview Practice
8	Practicing Public Speaking Skills
9	Technical Writing Practice
10	Project Report Writing

❖ **Department of M.B.A.**

- **List of Major Equipment / Facilities in each Laboratory/Workshop**

  24 Desktop Terminals

- **List of Experimental Setup in each Laboratory/Workshop**

  Nil

xv.	**Innovation Cell**
	Innovation is an essential component for success. Globalization and rapid technical changes in the education sector has created a need for change in teaching style which leads to continuous innovation. Teaching innovation is the process of creating new ideas, theories, methodologies and solutions that can be shared with the classroom. Innovation in four-year degree program ensures that it transforms the students into graduates, those who prepare themselves for employment in the engineering industry and update them according to rapid changing technology. The use of innovative method in educational institutes has the potential not only to improve education, but also empower people and mobilize the effort to archive the skilled engineer for country. The success of these practices results qualitatively as well as quantitatively. The qualitative factor improves student's curiosity and desire to learn. Also it changes student's perspective towards life. The quantitative factor improves academic performance and participation in co- curricular activities. Also Alumni of SSGMCE doing very well in corporate world.
xvi.	**Social Media Cell**
	Media is a gift of technology that provides us with the medium for mass communication. Communication tools used to store and deliver information or data. Social media plays an important role in every student's life. It is easier and convenient to access information, provide information and communicate via social media. Teachers and students are connected to each other and can make good use of these platforms. Platforms like WhatsApp, Facebook, LinkedIn, Instagram, YouTube and Twitter are used by almost everyone. Print Media includes newspapers, weeklies, magazines, blogs, banners, graphics, posters and other forms of printed material. In order to effectively make of use of media, it has been decided to publicize our College events through social and print media
xvii.	**Compliance of the Academic Bank of Credit (ABC), applicable to PGCM/ PGDM Institutions and University Departments.**
	YES
xviii.	**To upload the respective short video (1-2 min) of Infrastructure and facilities available w.r.t. the courses in the website.**
	YES - https://www.youtube.com/watch?v=COMGjbhBcUE
xix.	**Games and Sports Facilities**
	YES - AVAILABLE

xx. Teaching Learning Process

- **Department of Electrical Engineering (Electronics and Power)**

The department follows a structured and systematic approach to curriculum planning, ensuring transparency, faculty competency, and compliance with university guidelines while fostering a holistic, student-centered learning environment. The institute implements standardized and transparent procedures to enhance the effectiveness of the teaching-learning process, focusing on two key aspects: effective teaching and meaningful learning. The revised Bloom's Taxonomy is integrated into instructional strategies and assessment methods to improve learning outcomes. The Quality Teaching Learning Process is described in the figure.

xx.	Teaching Learning Process
Department of Electrial Engineering (Electronics and Power): https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process - Dept. of Electrical Engineering (Electronics and Power)_2025-26.pdf	
Department of Electronics and Telecommunication: https://www.ssgmce.ac.in/uploads/pdf/Teahing Learning Process_EXTC Dept_2025-26.pdf	
Department of Mechanical Engineering : https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process_MECH dept_2025-26.pdf	
Department of Computer Science and Engineering : https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process_CSE dept_2025-26.pdf	
Department of Information Technology : https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process_IT dept_2025-26.pdf	
Department of Applied Science and Humanities : https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process_ASH dept_2025-26.pdf	
Department of MBA : https://www.ssgmce.ac.in/uploads/pdf/Teaching Learning Process_MBA dept_2025-26.pdf	

xxi. For each Post Graduate Courses give the following:

xxii. Title of the Course

xxiii. Laboratory facilities exclusive to the Post Graduate Course

Title of the Course	Name of Laboratory	Name of Facilities
M.E. Electrical Power Systems	Power Quality Lab	Power quality parameter monitoring setup
		Power Quality event detection & classification setup
		Real time data monitoring of electrical equipment's
	Advance Power System Lab	Shunt Active filter.
		Dynamic Voltage Restorer
		Unified Power Quality Conditioner
		HVDC trainer kit
Digital Electronics	PG Laboratory	• Practical and Projects are Conducted on the Computational, Kits and Boards facilities available.
Master of Engineering (AM&Mechanical System Design)	PG Laboratory	• Advanced Manufacturing Processes Lab • Computer Aided Design & Engineering Lab • Mechatronics in System Design Lab Experimental Stress Analysis Lab
M.E. Computer Engineering	PG Laboratory	1) Computer System: (LENOVO I3 (3G) /4GB RAM /500GB HDD, With Windows 10 Operating System and Quick Heal Antivirus) 5 Qty. 2) Computer System: (LENOVO I3 (3G) /4GB RAM /1 TB HDD, With Windows 10 Operating System And Quick heal Antivirus) 8 Qty. 3) Computer System: (Lenovo I3 (2G) /2GB RAM /500 GB HDD, With Windows 10 Operating System And Quick heal Antivirus) – 6 Qty. 4) 5 KVA Online UPS With Batteries: - 01 Qty 5) Optoma Projector – 01 Qty 6) All Computers in the Laboratory equipment with Internet facility /White board.

Master of Business Administration	--	24 desktop terminals

18.16 Enrollment and placement details of students in the last 3 years

SN	Name of Course	Enrollment			Placements		
		2022-23	2023-24	2024-25	2022-23	2023-24	2024-25
1	B.E. Computer Science and Engineering	81	75	77	72	59	41
2	B.E. Electronics and Telecom. Engg.	155	147	142	90	80	29
3	B.E. Mechanical Engineering	73	71	67	90	85	45
4	B.E. Electrical Engg. (Electronics & Power)	76	64	76	62	71	51
5	B.E. Information Technology	77	76	74	55	60	18
6	M.E. Electrical Power System	02	01	04	--	--	--
7	M.E. Digital Electronics	02	01	00	--	--	--
8	M.E. Computer Engineering	08	00	02	--	--	--
9	M.E. Mechanical Engineering	01	05	01	--	--	--
6	M.B.A.	61	72	66	60	44	36

18.17 List of Research Projects/Consultancy Works

1. Deparrtment of Electrical Engineering (Electronics and Power)

   -Nil-

2. Department of Electronics and Telecommunication Engineering

   -Nil-

3. Department of Mechanical Engineering

   -Nil-

4. Department of Information Technology

SN	Project Title	Sponsoring Agency
1	Integration of Chat GPT to verify and update the End-of-life database	Apexa IQ, USA
2	Effective End-of-Life(EOL) management of devices in the organization Snowflake integration	Apexa IQ, USA
3	Student Educational Platform for One Smarter	One Smarter Inc., USA

5. Department of MBA
   - Four (04) Doctoral Research Projects (Ongoing) in the Department's Research Center.
   - One (01) student group will represent University at the State level Competition. (Ongoing)
   - Two (02) out of Five (05) groups were shortlisted for the Podium round (final round) at University level. (Completed)
   - Five (05) out of Seven (07) participating groups won at District level Aavishkar 2025 (University Student Research Convention). (Completed)
   - Five (05) Marketing Internship Project for Agrowsure Products and Innovations Pvt. Ltd., Akola (Completed).
   - Summer Internship Projects of students in various companies. (Completed)

- Final year Dissertation Projects of Students (Ongoing)Research articles on Varkari Sect under

18.18 MoUs with Industries

SN	Name of the Organization	MOU Signing Date
Department of Electrical Engineering (Electronis and Power)-		
1	I - Robots Innovative Solutions, Pune	5th April 2025
2	TPSDI, Shahad Mumbai	21st June 2024
3	Adani Electricity Mumbai Limited, ADTPS, Dahanu	1st June 2024
4	Mew Technologies, Bengaluru	4th March 2024
5	Samarthan System Private Limited, Pune	10th January 2024
6	SCR Electronics, Mumbai	8th February 2023
7	Mitsubishi Electric India Private Limited.	6th January 2023
8	ISIE INDIA, Noida	18th January 2022
Department of Electronics and Telecommunication Engineering		
1	ADOLF SOLUTIONS (OPC)PVT. LTD	---
2	DAccess IT Infra Pvt. Ltd., Pune	---
3	Iravan Tehnologies, Pune	---
4	SSG Embedded Solutions, Nagpur	---
5	Symbiosis Institute of Technology, Pune	---
Department of Mechanical Engineering		
1	Krishan Vishwa Vidyapeeth "Deemed to be University", Karad, Maharashtra,	16th January 2024
2	**Endress+Hauser** Automation Instrumentation Pvt. Ltd., Ch. Sambhaji Nagar (Aurangabad)	31st March 2022
3	**Tool Tech Toolings** Kirdak Auto Com Pvt. Ltd. Sambhaji Nagar	27th July 2022
4	**Wadhokar Groups of Companies** SM Rolling Pvt. Ltd., Pune	19th January 2019
5	**Kala Groups of Companies,** Pune	19th January 2019
6	**Mechatol Enginering Solutions,** Kothrud, Pune	19th January 2019
7	**Vinodrai Engineers Pvt. Ltd.**Jalna (Ch. Sambhaji Nagar)	16th March 2019
Department of Computer Science and Engineering		
1	**Bharat Software Solutions, Pune Mr. Y. P. Murumkar**, CEO & Director Email: yogeshpm5555@gmail.com Contact: 9657080905	5th April 2025
2	**Pragmatyc Global Private Limited, Nagpur Mr. Rahul Bajait,** Co-founder and Chief Strategy Officer Email : https://pragmatyc.com Contact :- 9766538821	5th April 2025
3	**True Scholar- Asset Chain Techniligence Private Ltd., Amravati Mr.MayurZanwar**, Founder & Director Email: - info@truscholar.io Contact: 9422855955	5th April 2025
4	**Intel Technology India Private Limited 23-56P, Mr.Girish. H:** National Business Manager, AI, H.PC E Unnati Email :- singh@edgate.in Contact No. :- 9880170673	24th March 2025
5	**PM Shri School Jawahar Navodaya Vidyalaya, Shri R. R. Kasar Principal**	5th March 2025
6	**MITU Skillogogies, Pune Rashmi R. Thorave**, Managing Director.Email: tushar@tusharkute.com Contact: 7588594665	21st December 2023
Department of Information Technology		

1	Prodevans Technologies Pvt. Ltd., Bengaluru	05/10/2023
2	BridgeLabz Solutions Pvt. Ltd., Mumbai	31/01/2023
3	Expert Global Solutions Pvt. Ltd., Aurangabad Name: Mr. Omkar Deshpande Email ID:onkard@expertgs.com,Contact No: 9130096211	24/11/2022
4	Renuka Technologies Private Limited, Nagpur Mr. ChetankumarAkarte, Mob: 9867748519	19/01/2019
5	Cluebix Software Pvt. Ltd, Nagpur Hemant Barapatre Phone: 7588807491 Email: hemant@cluebix.com	19/01/2019
6	VNurt Technology Solutions Pvt. Ltd., Bengaluru Mr. Hemant Sharma, Email: hemant@vnurt.com, Mob: 9632444388	19/01/2019
7	JDM Semiconductor Technologies Pvt. Ltd., Nagpur Mr. Ashish Khachane, Mob: 9028626018	19/01/2019
8	Vidharbha Industry Defence Hub, Mihan Nagpur	19/01/2019
Department of MBA		
1	MoU signed with Bajaj Finserv Ltd. CSR Wing for conducting Certificate Program in Banking, Finance and Insurance (CPBFI) for Employability enhancement and industry aligned course.	---
2	MoU signed with Kalash Seeds Pvt. Ltd., Jalna for Research, Academics, Training and Placement.	---
3	MoU signed with Kalash Seeds Pvt. Ltd., Jalna for Research, Academics, Training and Placement.	---
4	MOU with Leben Life Sciences, Akolafor Academics, Visits, Training and Placement.	---
5	MoU signed with Lyceum of the Philippines University, Laguna for International Connect.	

Place: Shegaon
Date: 24th January 2026
(Mandatory Disclosure 2025-2026)

(Authorized Signature)
Dr. Sunil Bhikamchand Somani
Principal, SSGMCE
Shegaon-444203